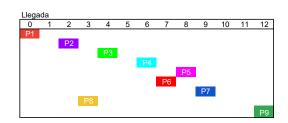
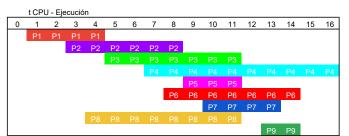
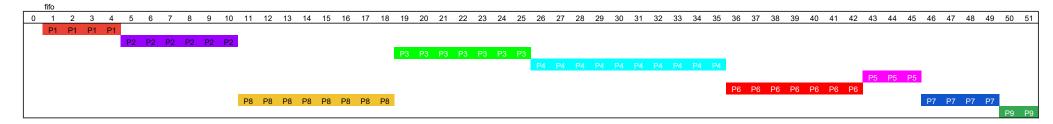
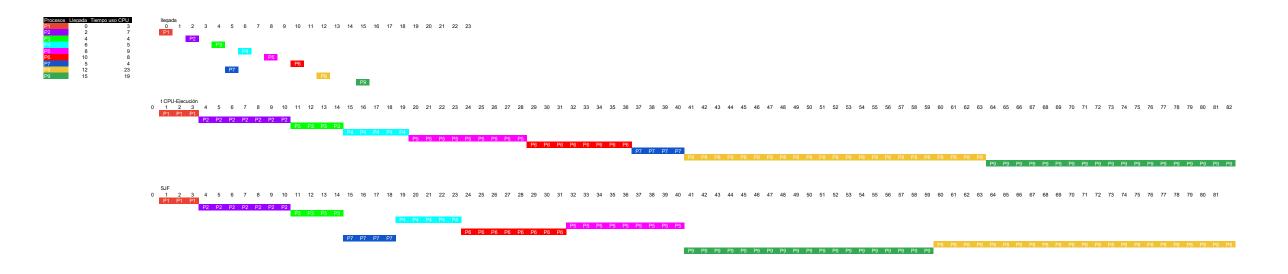
Procesos	Llegada	Tiempo uso CPU
P1	0	4
P2	2	6
P3	4	7
	6	10
P5	8	3
P6	7	7
P7	9	4
	3	8
P9	12	2

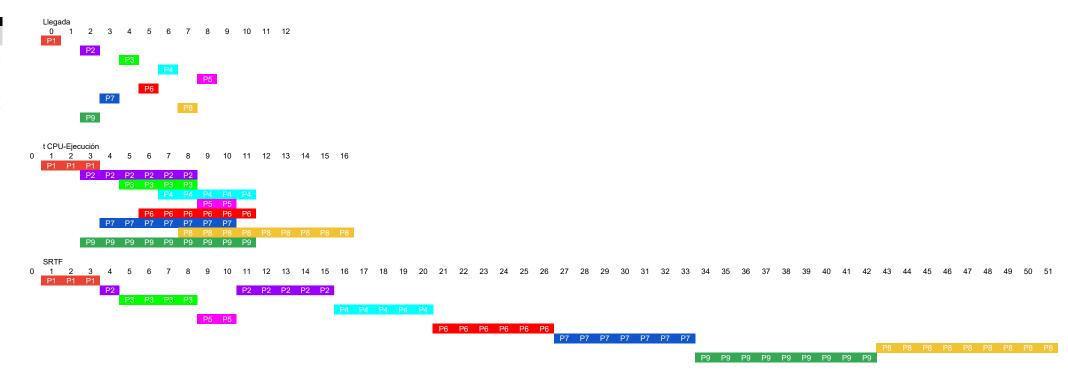




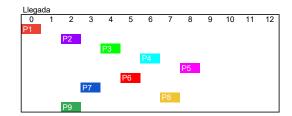


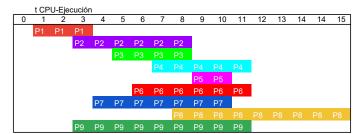


Procesos	Llegada	Tiempo uso CPU
P1	0	3
P2	2	6
P3	4	4
P4	6	5
P5	8	2
P6	5	6
P7	3	7
P8	7	9
P9	2	9

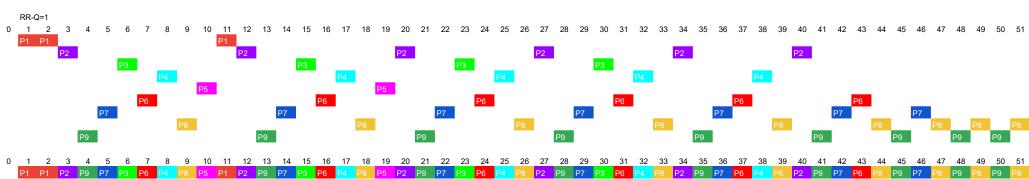


Procesos	Llegada	Tiempo uso CPU
D1	Licgada 0	Tiempo aso or o
PT	U	3
P2	2	6
P3	4	4
P4	6	5
P5	8	2
P6	5	6
P7	3	7
P8	7	9
P9	2	9

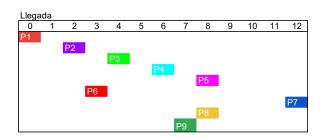


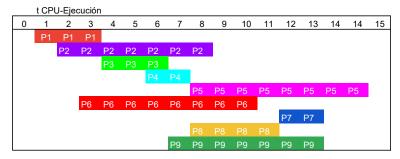


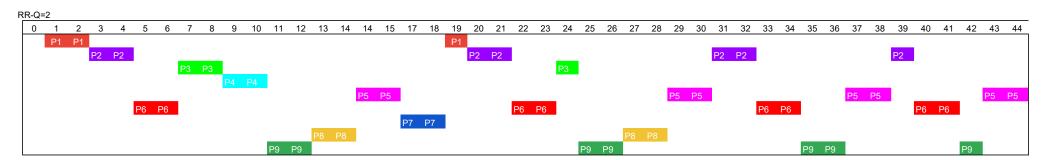
Procesos	Llegada	Tiempo uso CPI
P1	0	
P2	2	
P3	4	
P4	6	
P5	8	
P6	5	
P7	3	
P8	7	
P9	2	

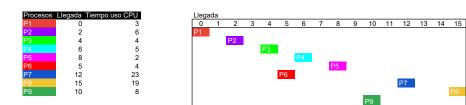


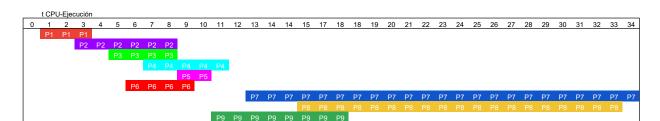
Procesos	Llegada	Tiempo uso CPU
P1	0	3
P2	2	7
P3	4	3
P4	6	2
P5	8	8
P6	3	8
P7	12	2
P8	8	4
P9	7	7



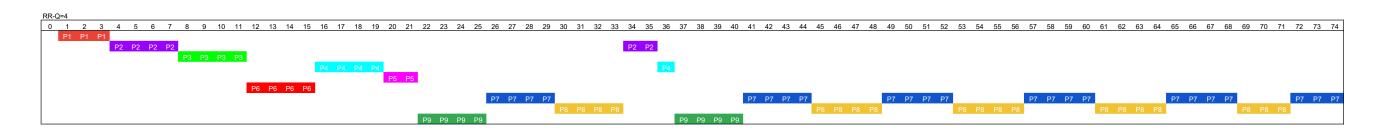




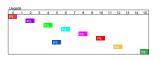




Procesos	Llegada	Tiempo uso CPU
P1	0	3
P2	2	6
P3	4	4
P4	6	ŧ
P5	8	2
P6	5	4
P7	12	23
P8	15	19
P9	10	8











Procesos	Llegada	Tiempo uso CPU
P1	0	230
P2	2	180
P3	4	150
P4	6	100
P5	8	100
P6	10	100
P7	5	220
P8	12	120
P9	15	130