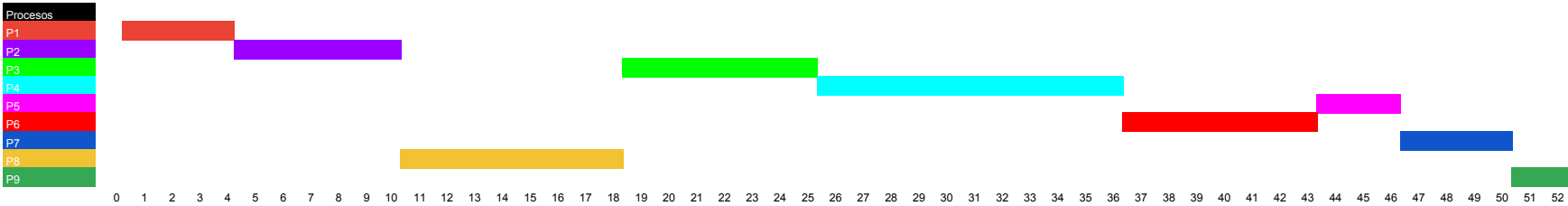
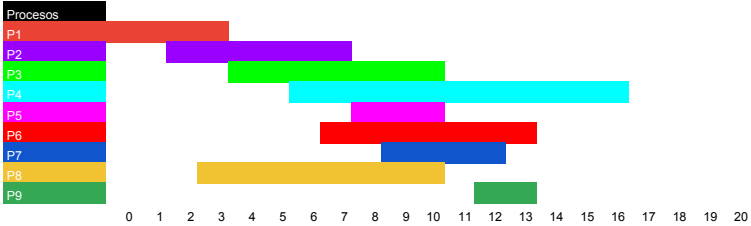
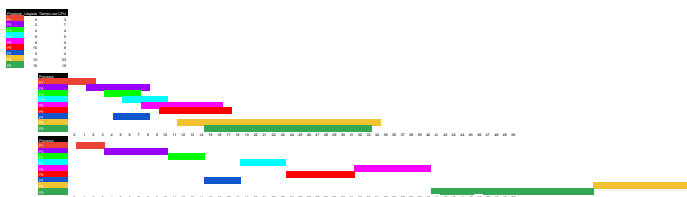


Procesos	Llegada	Tiempo uso CPU
P1	0	4
P2	2	6
P3	4	7
P4	6	10
P5	8	3
P6	7	7
P7	9	4
P8	3	8
P9	12	2

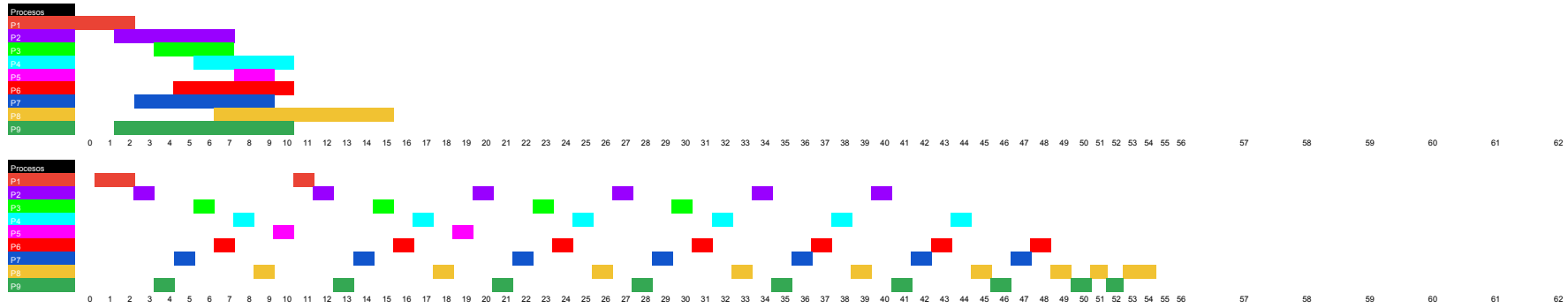


Procedura	Limite	Temps (sec CPU)
P1	0	2
P2	2	6
P3	4	4
P4	6	5
P5	8	2
P6	5	6
P7	3	7
P8	7	9
P9	2	9





Procesos	Llegada	Tiempo uso CPU
P1	0	3
P2	2	6
P3	4	4
P4	6	5
P5	8	2
P6	5	6
P7	3	7
P8	7	9
P9	2	9



Proceso	Librada	Tiempo uso CPU
P1	8	3
P2	2	7
P3	4	3
P4	6	2
P5	8	8
P6	3	8
P7	12	2
P8	8	4
P9	7	7

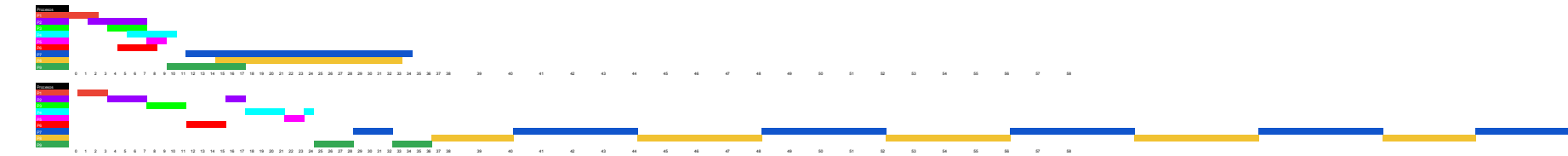
Proceso
P1
P2
P3
P4
P5
P6
P7
P8
P9

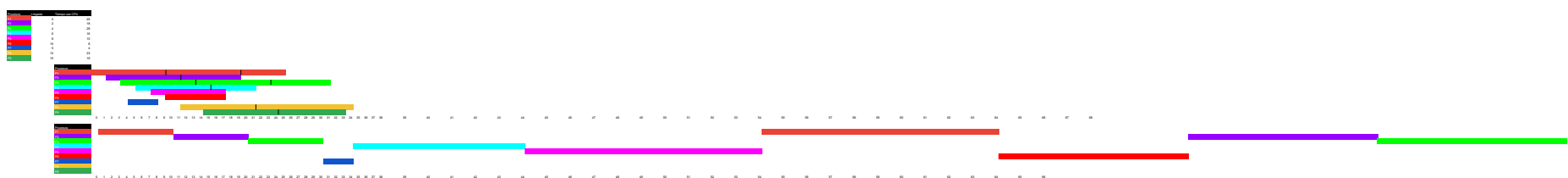
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60

Proceso
P1
P2
P3
P4
P5
P6
P7
P8
P9

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60

Category	Category	Category
1	1	1
2	2	2
3	3	3
4	4	4
5	5	5
6	6	6
7	7	7
8	8	8
9	9	9
10	10	10
11	11	11
12	12	12
13	13	13
14	14	14
15	15	15
16	16	16
17	17	17
18	18	18
19	19	19
20	20	20
21	21	21
22	22	22
23	23	23
24	24	24
25	25	25
26	26	26
27	27	27
28	28	28
29	29	29
30	30	30
31	31	31
32	32	32
33	33	33
34	34	34
35	35	35
36	36	36
37	37	37
38	38	38
39	39	39
40	40	40
41	41	41
42	42	42
43	43	43
44	44	44
45	45	45
46	46	46
47	47	47
48	48	48
49	49	49
50	50	50
51	51	51
52	52	52
53	53	53
54	54	54
55	55	55
56	56	56
57	57	57
58	58	58





Procesos	Llegada	Tiempo uso CPU
P1	0	230
P2	2	180
P3	4	150
P4	6	100
P5	8	100
P6	10	100
P7	5	220
P8	12	120
P9	15	130