

HW4
CSc 137, Fall 2015, Faroughi

Due Oct. 7 in class

Problems 2.29 and 3.1.

Instructions for how to run Verilog codes are on the web. For examples, refer to Example 2.14 and 2.15.

Instructions for how to use Verilog compiler on Titan refer to the instructions on the class web page.

Turn in the followings:

1. Your source code, including the code for the “tester” module, and the output.
2. Comment on the output page that the values generated by the \$monitor matches the expected values.

Must show work to earn points.