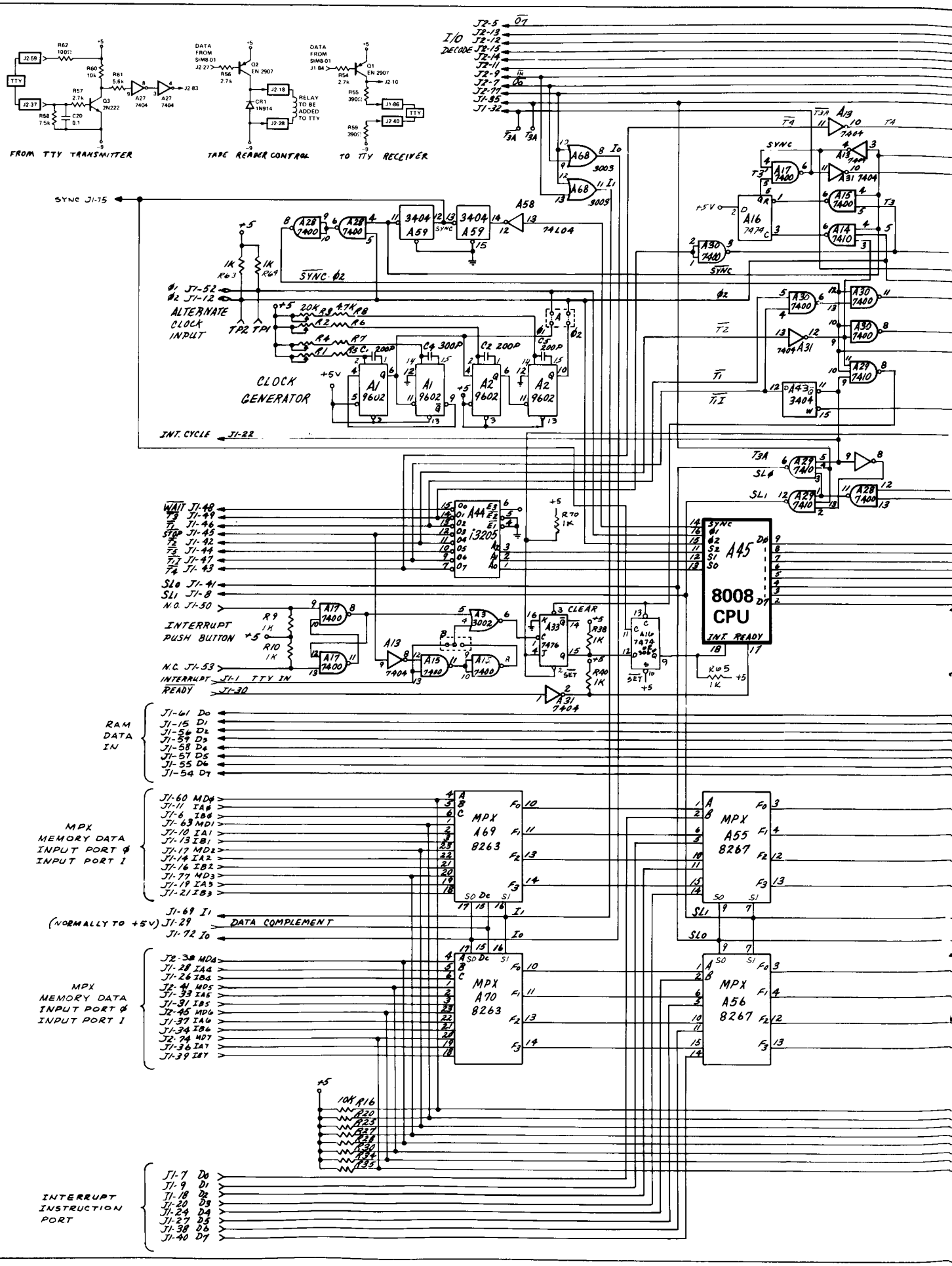
FROM TTY TRANSMITTER
TAPE READER CONTROL
TO TTY RECEIVER
I/O DECODE
SYNC J1-75
CLOCK GENERATOR
ALTERNATE CLOCK INPUT
INT. CYCLE J1-22
8008 CPU
INT. READY
INTERRUPT PUSH BUTTON
RAM DATA IN
MPX MEMORY DATA INPUT PORT 0 INPUT PORT 1
(NORMALLY TO +5V) J1-29 DATA COMPLEMENT
INTERRUPT INSTRUCTION PORT

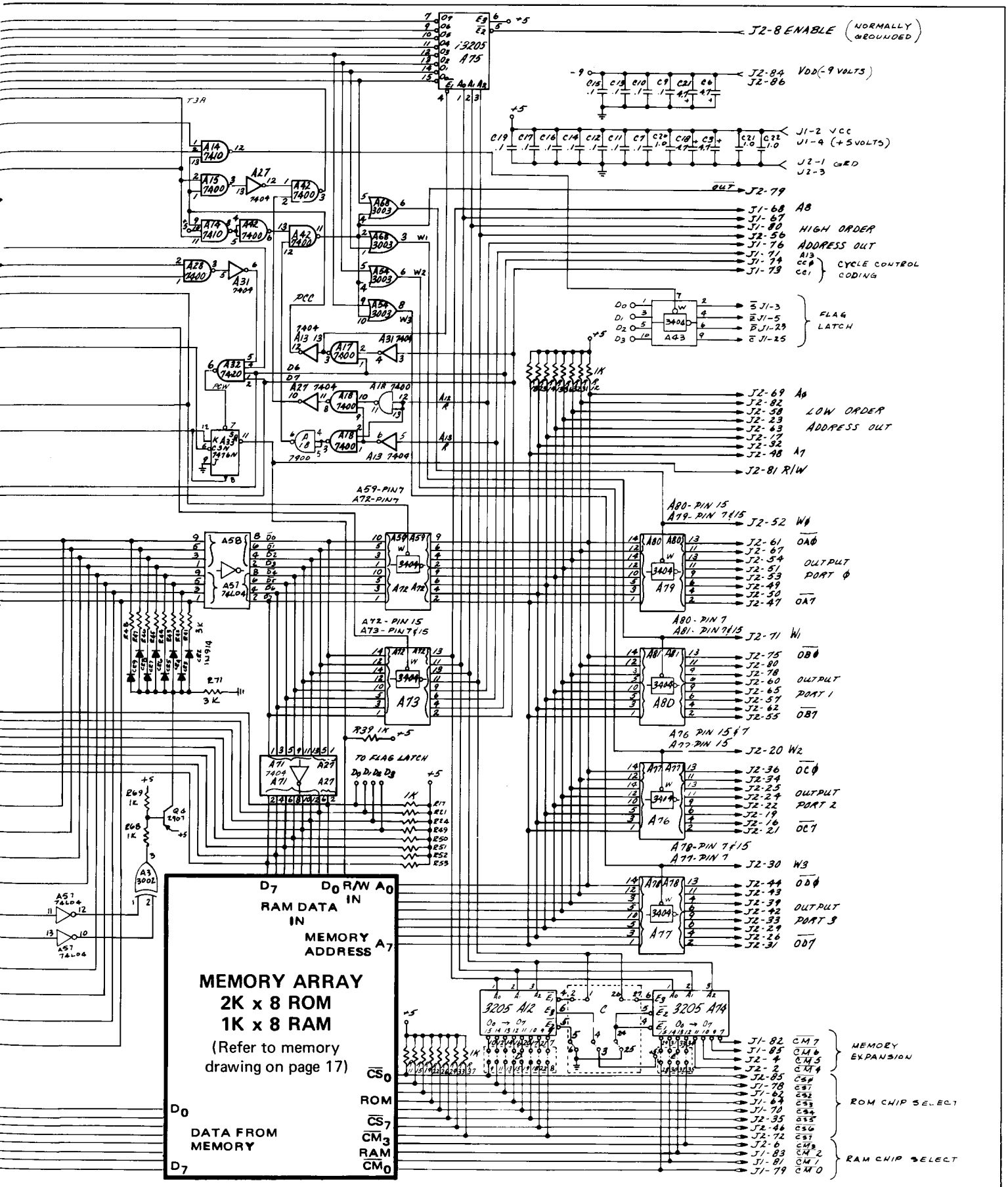

Figure 10. Complete SIM8-01 Schematic