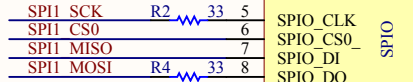


SPI 1 Port Master



3.3V @ 200mA
1.8V @ 160mA (typ)
MicroController

U2A

SPIO_CLK
SPIO_CS0
SPIO_DI
SPIO_DO

MODE_8_GPC[4]
MODE_9_GPC[5]
MODE_10_GPC[6]

NAND I/F

NCS0
NCS1
NALE
NCLE
NRE
NWR
NBUSY0
NBUSY1

ND/MODE[0]
ND/MODE[1]
ND/MODE[2]
ND/MODE[3]
ND/MODE[4]
ND/MODE[5]
ND/MODE[6]
ND/MODE[7]

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

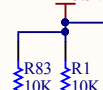
ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

NAND3.3V



3.3V @ 30mA (typ)
NAND Flash

U1

R/B#
RE#
CE#
CLE
ALE
WE#
WP#

I/O0
I/O1
I/O2
I/O3
I/O4
I/O5
I/O6
I/O7

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

NC
NC
NC
NC
NC
NC
NC
NC

VCC
VCC
VCC
VCC
VCC
VCC
VCC
VCC

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

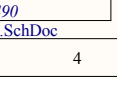
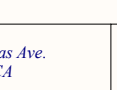
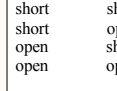
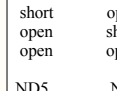
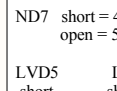
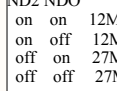
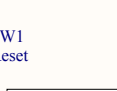
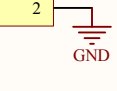
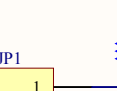
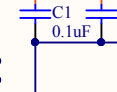
VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS

VSS
VSS
VSS
VSS
VSS
VSS
VSS
VSS



Prototype Only

Micro SD

CN1

DAT2
DAT3/CD
CMD
VDD
CLK
VSS
DAT0
DAT1

microSDCARD

Mini-B USB USB 2.0

CN2

VBUS
D-
D+
NC
GND

1734035-2

USB Host 1.0 Lite USB-A

CN4

VBUS
D-
D+
GND
SHIELD

87583-2010BLF

SDDAT[2]
SDDAT[3]
SDCMD
SDCLK
SD 0
SD 1

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

UD_CDET
UD_DM
UD_DP
UD_REXT

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

RST_

ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN

N32905U1DN

N32905U1DN

RTC_XIN
RTC_XOUT

XIN
XOUT

RTC_VDD
RTC_RAWAKE
RTC_RPW

UART
URTXD
URRXD

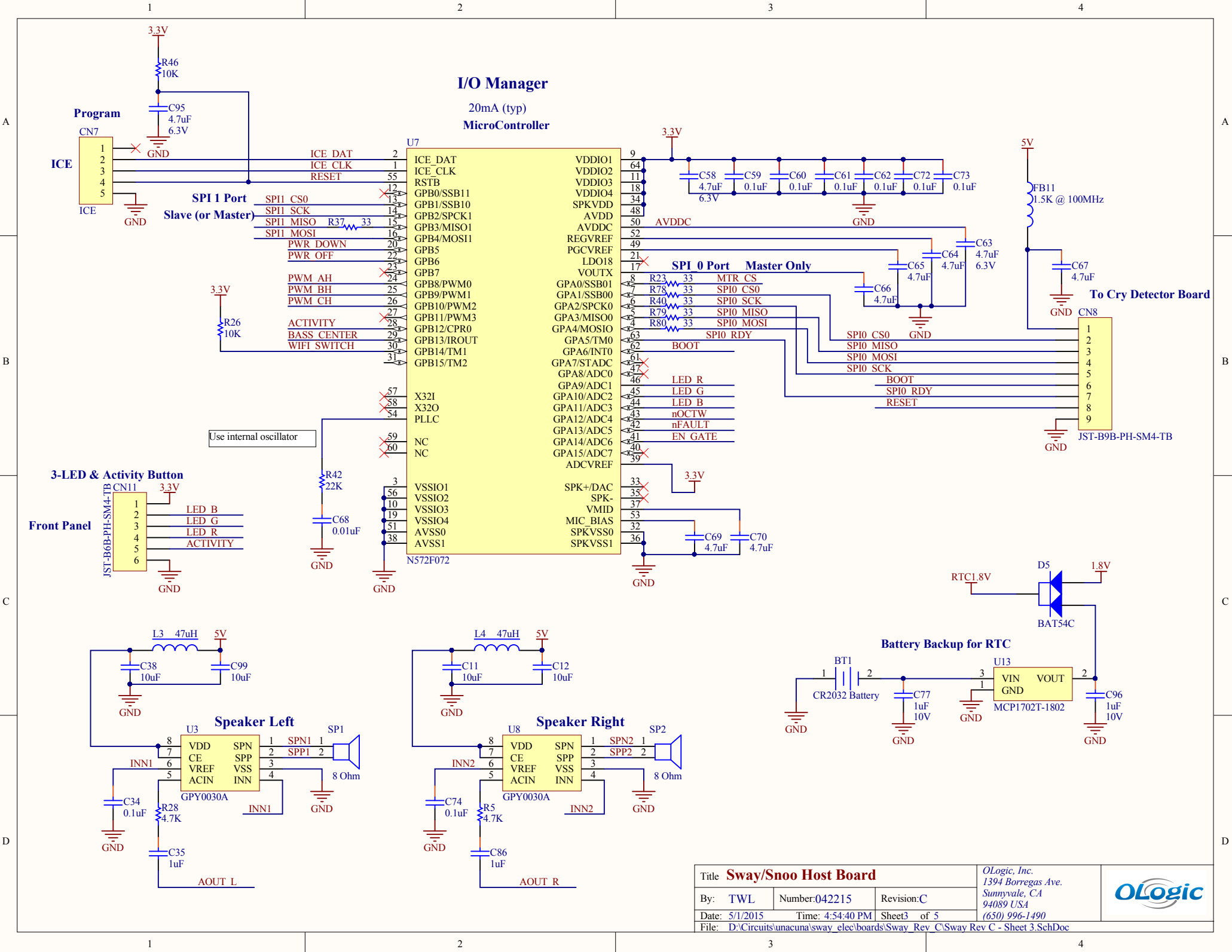
RST_

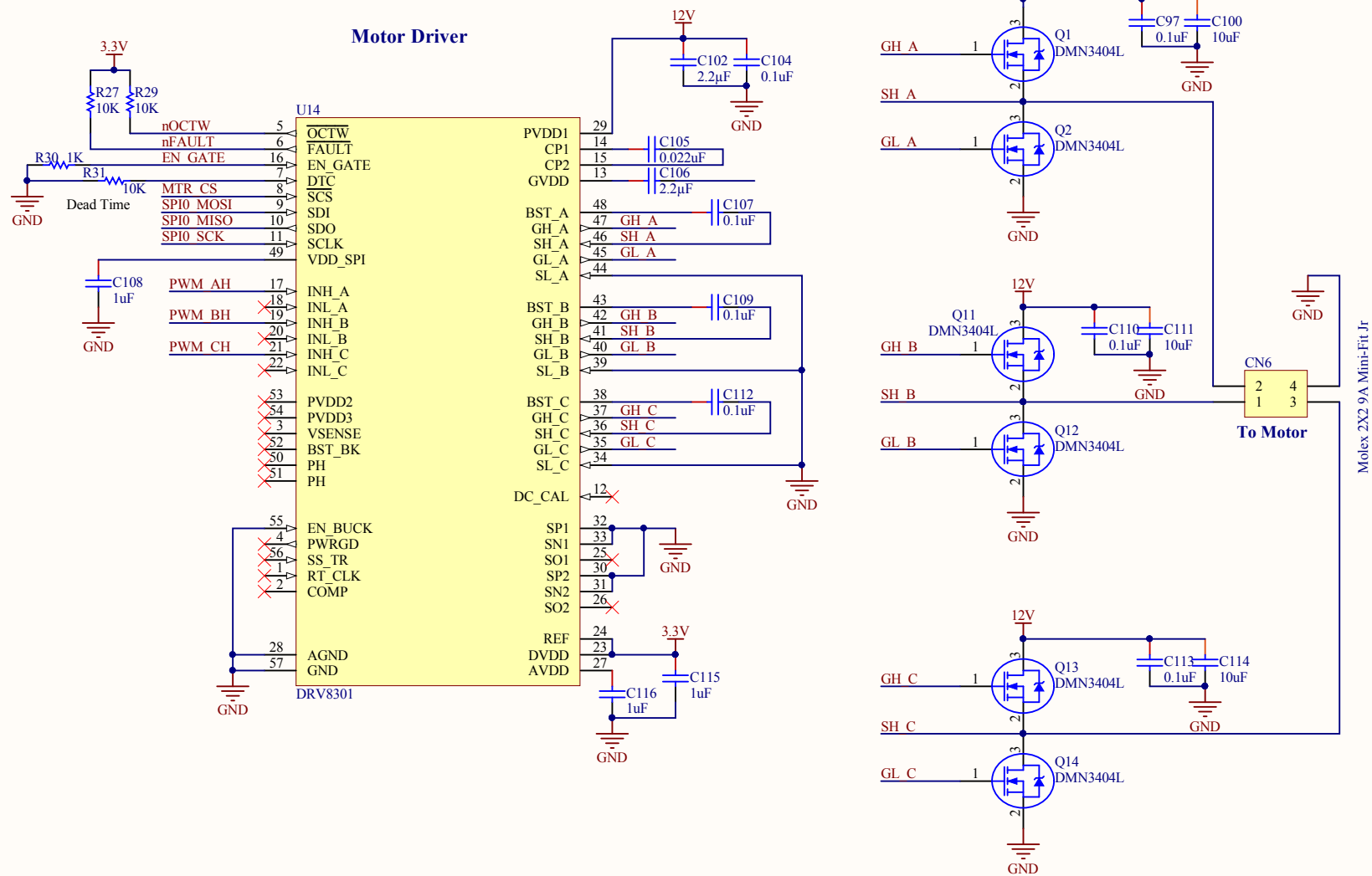
ADAC_HPOUT_L
ADAC_HPOUT_R

JTAG
TMS
TCK
TDO
TDI
TRST_

H_DM/GPA[4]
H_DP/GPA[3]

N32905U1DN





Title **Sway/Snoo Host Board**

By: TWL

Number: 042215

Revision: C

Date: 5/1/2015

Time: 4:54:40 PM Sheet 5 of 5

File: D:\Circuits\unacuna\sway_elec\boards\Sway_Rev_C\Sway Rev C - Sheet 5.SchDoc

OLogic, Inc.
1394 Borregas Ave.
Sunnyvale, CA
94089 USA
(650) 996-1490

