Icepi Zero Testing Guide

Setup

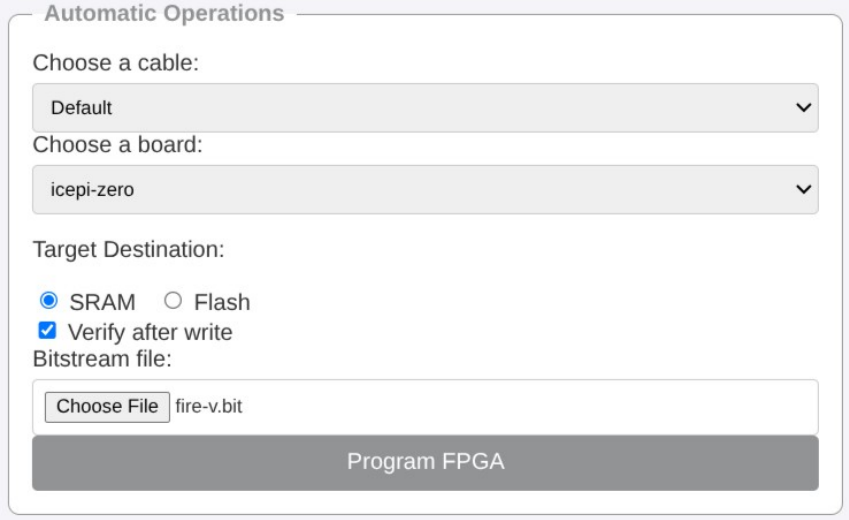
Download the following bitstream file:
https://github.com/cheyao/icepi-zero/raw/refs/heads/main/documentation/fire-v.bit

Testing

1. Plug in a USB-C data cable to the left-most USB port on the PCB. (One next to the HDMI port, labeled Flash). Connect the other end to a PC/laptop. Also use a micro-HDMI to HDMI cable to connect the HDMI port to a monitor.

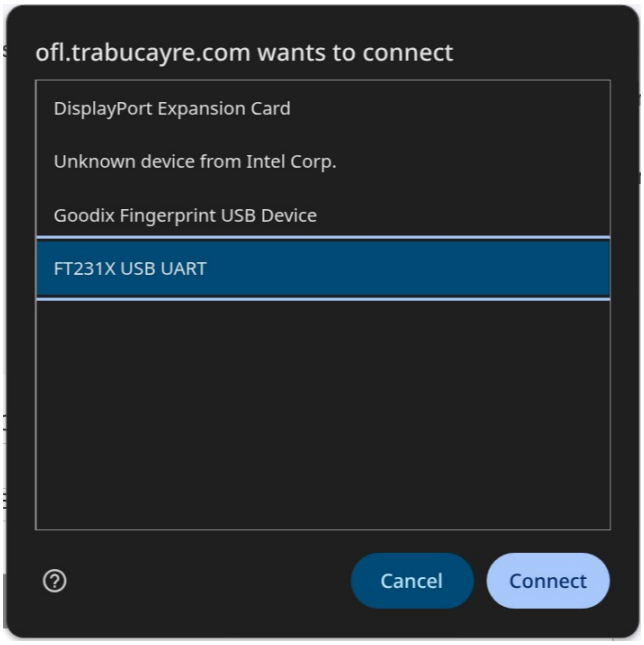
2. Open https://ofl.trabucayre.com/ in Google Chrome (Firefox does **NOT** work)

3. On the bottom left, in the “Automatic Operations” section, select the following options:

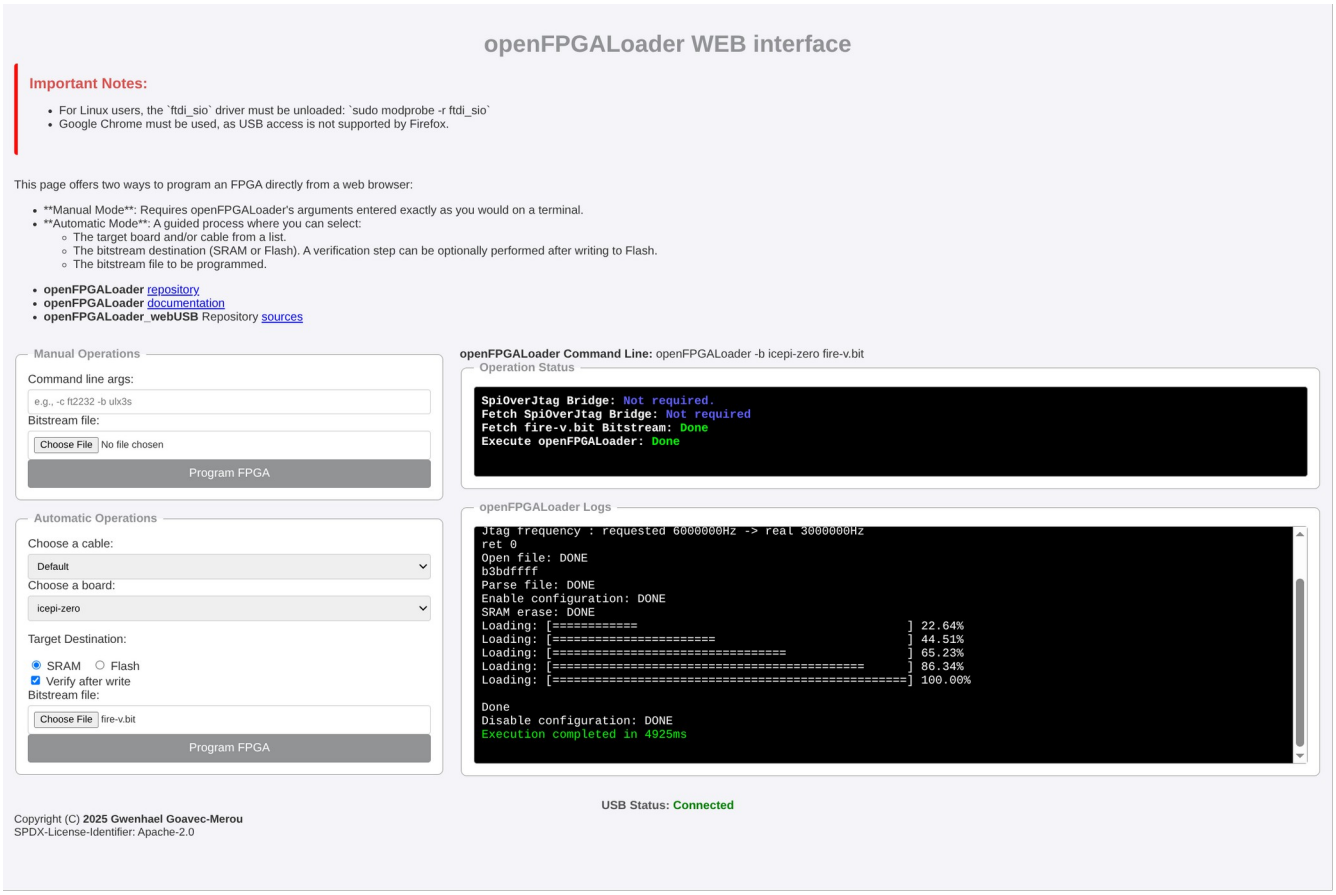


(Click “Choose File” and select the fire-v.bit file downloaded during setup)

4. Click on “Program FPGA” and select “FT231X USB UART” in the popup menu and click connect:



6. Wait for a few seconds, “Done” should be written in “openFPGALoader Logs”:



7. If the PCB works, you should see LEDs blinking and the following HDMI output on the monitor: