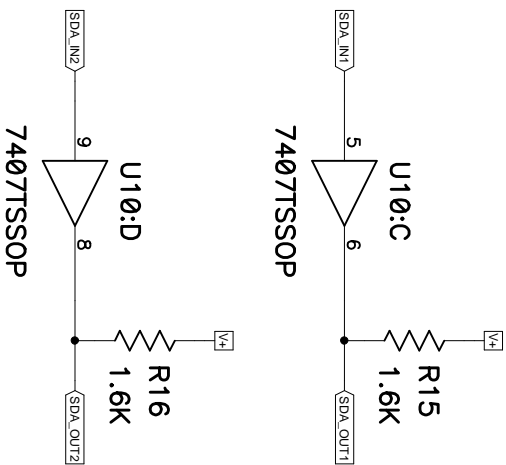
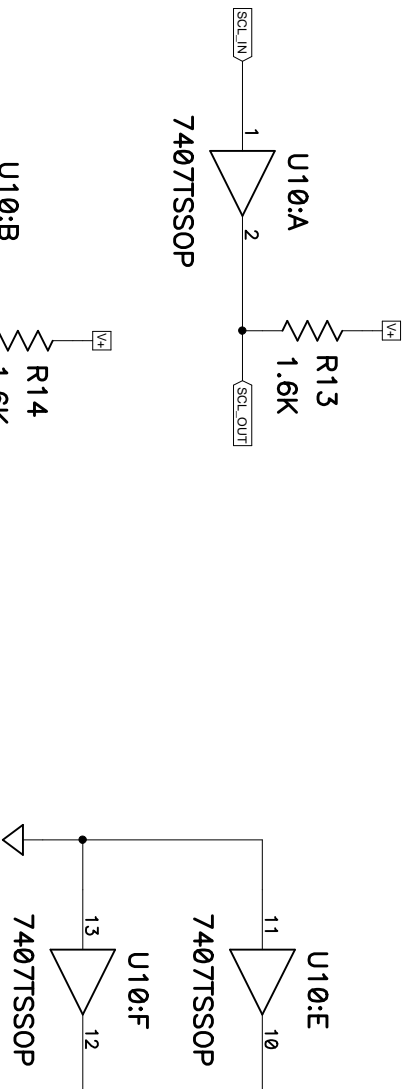
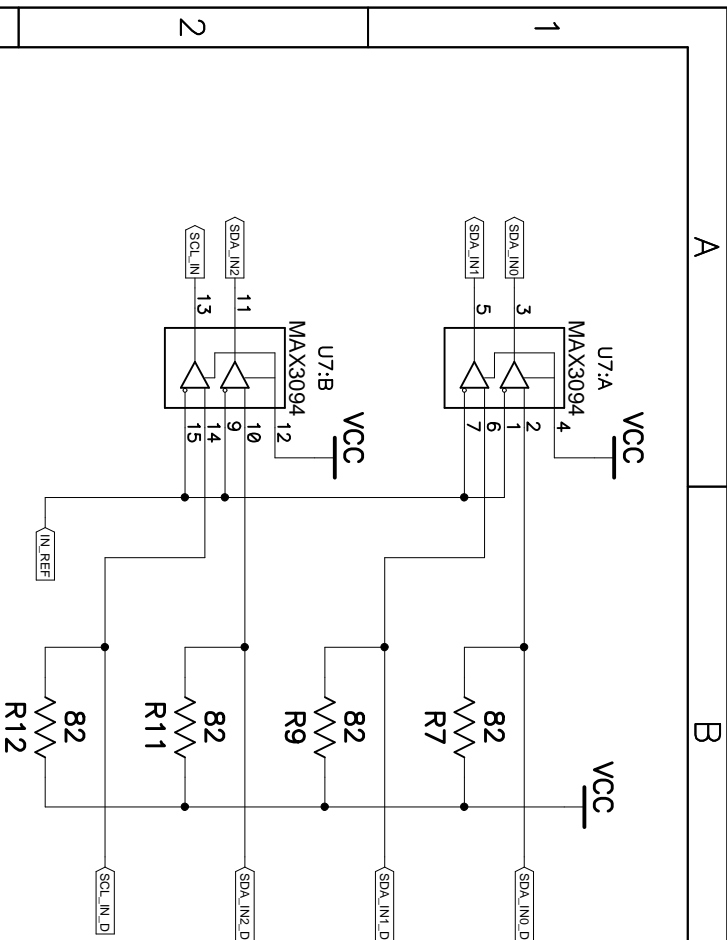
connectors.

These are the signal pins. We also pass the +3.3V supply in case we want it for temperature sensor or something.

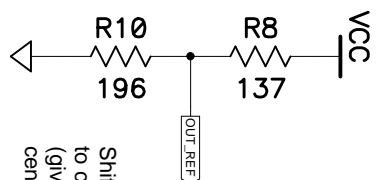


I2C buss is pulled up to V+ (5V nominal) even though U2 runs on 3.3V Vcc. The outputs tolerate this, and it speeds transition to a logic high level.

Title			
Micron 6DOF handle: main			
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Lines are terminated to Vcc because this is the idle state.

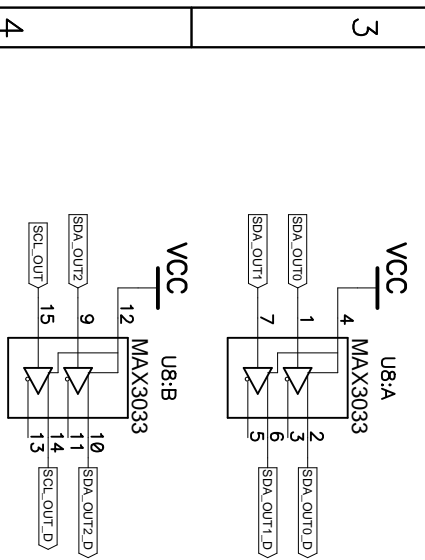


Shift ref level approx 300 mV positive to compensate for termination offset (given approx 10 ohm driver output resistance), centering threshold. $R8 \parallel R10 = Z0$

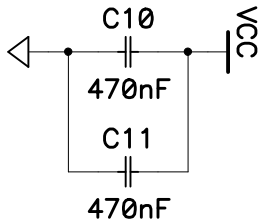
These drivers are digital interface to the motor driver chips in the handle. The cable is too long for the I2C busses themselves to be routed over it, so a different electrical interface is used in the cable based on RS485. To save wires in the cable, and so the cable doesn't need to have a twisted-pair structure, we use a quasi-differential connection where all of the outgoing signals share a single negative reference at $V_{cc}/2$, and similarly all of the incoming signals have a single reference set at the handle end.

Compared to a normal differential connection this halves the noise margin because the differential swing is halved, and also degrades common mode rejection insofar as the interference experienced by the different wires will vary somewhat. It also forgoes the property of the complementary signal acting as a HF return.

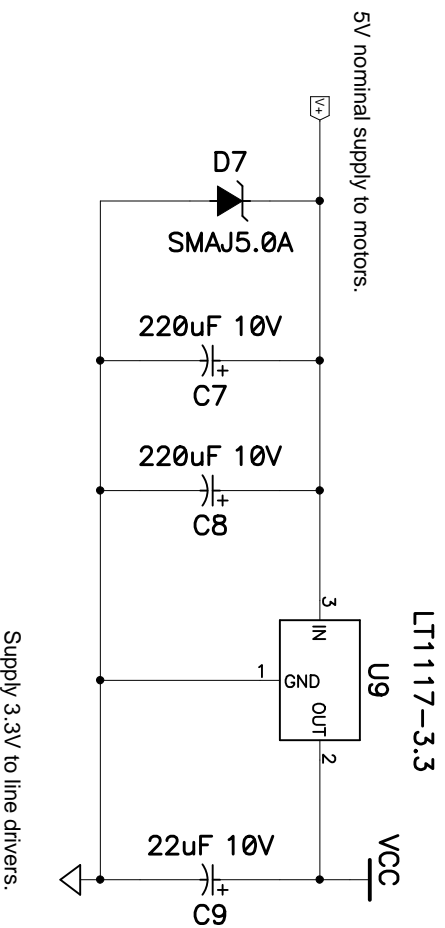
All these compromises seem acceptable given that we are primarily concerned with external interference and not crosstalk, since all the digital signals are synchronized (and the LED drive is band-limited.) This also addresses the issue that there may be substantial voltage drops and ground bounce in the motor power return, making it problematic as a signal reference.



The MAX3033 is slew rate limited to minimize reflections with imperfect impedance matching. 40 ns transition time typical.

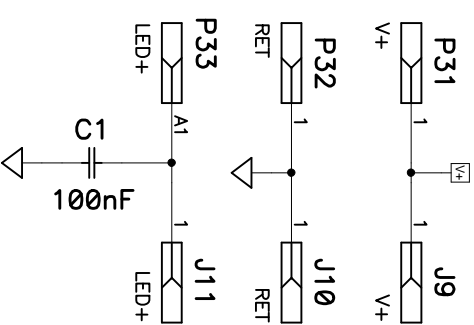
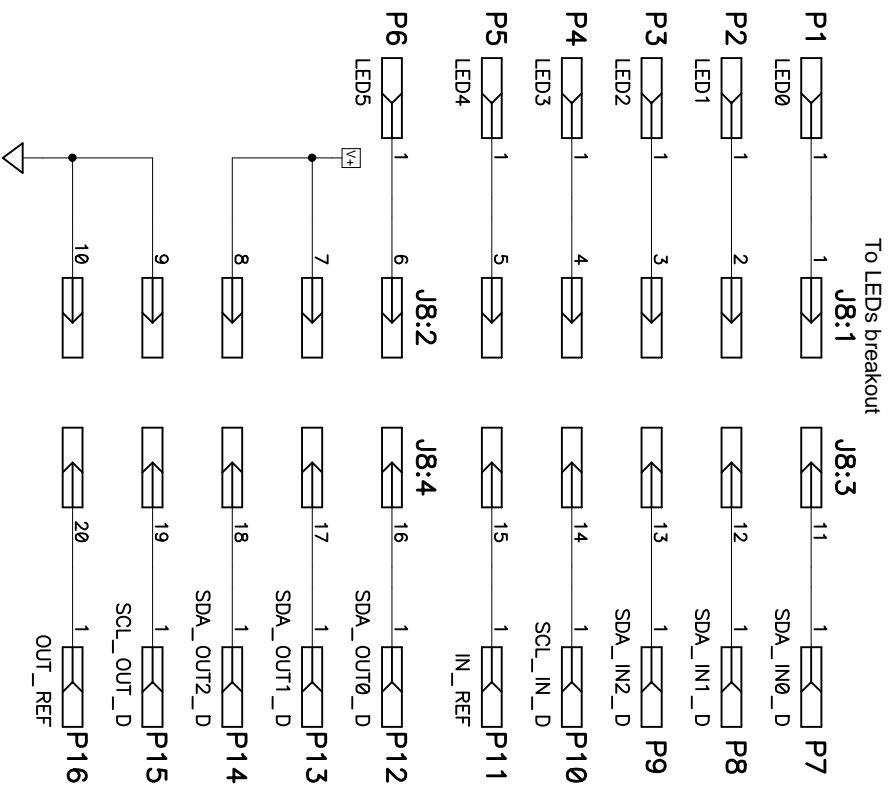


Title				
Micron 6DOF handlet: Line drivers				
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This regulator powers the line drivers and other 3.3 V components, and is located on the LED breakout board. The worst-case current consumption is considerable, > 160 ma, due to the 81 ohm line impedance. Average current should be considerably less when lines are mostly in the high (idle) state. Current could be > 325 ma for 100 ns or more, but this only happens when all inputs are low and all outputs high, which is not a sensible stable state. If inputs are low, outputs should follow, and the OC bus logic should enforce this.

Title			Micron 6DOF handle: Supplies		
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Filename handle.sch			Sheet 4 of 6		



Since LED+ is the shield of the cable, the HF currents of the line drivers must go there, but the drivers are referenced to ground. C1 ensures that LED+ is an AC ground for signals > 1 MHz.

Title		
Micron 6DOF handle: cable connections		
Size	Number	Rev
A		A
Date	Mon Sep 29, 2014	Drawn by
Filename	handle.sch	Sheet 5 of 6

