

MARTe2

A real-time control framework for nuclear fusion

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Roadmap

- 1 Introduction
- 2 Core library
- 3 Real-Time Applications
- 4 StateMachine
- 5 Integrating Simulink models
- 6 MDSplus

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Multi-threaded Application Real-Time executor

MARTe is a **C++ modular** and **multi-platform framework** for the development of **multi-threaded real-time control system applications**.

MARTe1 is the previous version of this framework.

MARTe1 was deployed in many fusion real-time control systems, e.g., at the JET tokamak.

The use of **MARTe1** increased the number of supported environments and platforms.



Figure 1: JET tokamak.

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One of the main features of the MARTe2 architecture is the **decoupling** of:

- the platform **architecture**, *i.e.*, x86, armv8...
- the **environment** details, *i.e.*, Linux, FreeRTOS, Windows;
- the **real-time algorithms**, *i.e.*, the user code.

Code organisation

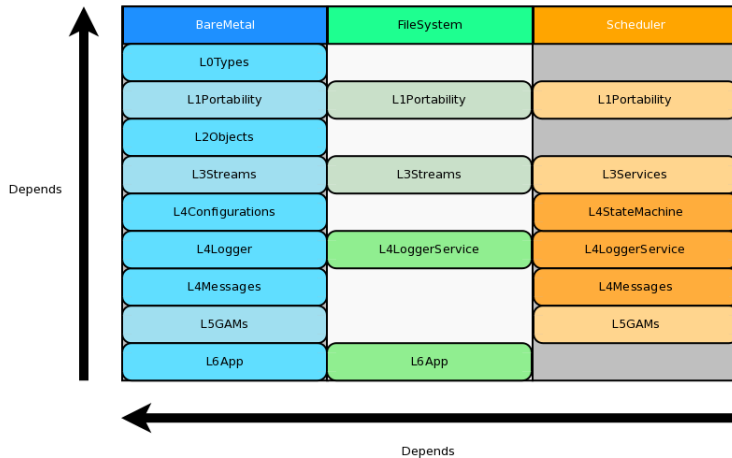


Figure 2: Core libraries organization.

Makefile

The build of the core library, as well as any MARTe2 project, follows this structure:

- 1 The Makefile.os-arch defines the **TARGET** operating system and architecture.
- 2 The Makefile.inc defines all the common rules.
- 3 The MakeDefaults defines the specific rules for the **TARGET**.

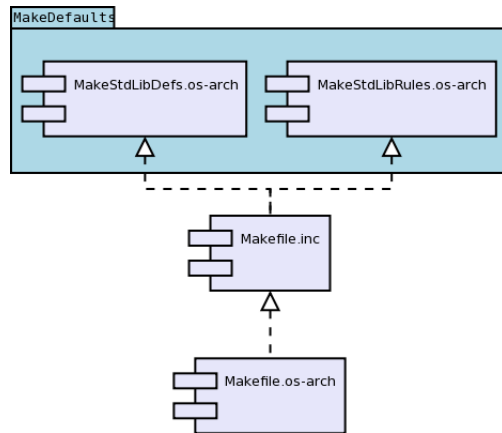


Figure 3: Makefile structure (bottom to top).

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MARTe2 offers a generic base application, see `MARTeApp.cpp`.

Real-Time Applications are built (more like "bootstrapped" or "put together") from the base one through **configuration files** (`.cfg`).

The **configuration files** define all the software components of a RTApp and their configuration properties, e.g., the algorithms to be executed (**GAMs**) and the hardware or software modules involved (**Data Sources**).

Configuration file: RTApp

```
1 $RTApp = {  
2   Class = RealTimeApplication  
3   +Functions = { // GAMs  
4     Class = ReferenceContainer  
5     ...  
6   }  
7   +Data = { // Data Sources  
8     Class = ReferenceContainer  
9     ...  
10  }  
11  +States = { // RT States  
12    Class = ReferenceContainer  
13    ...  
14  }  
15  +Scheduler = { // Scheduler  
16    ...  
17  }  
18 }
```

Listing 1: RTApp high-level configuration structure.

Generic Application Module

The **GAMs** are the software components where **real-time user algorithms** must be implemented.

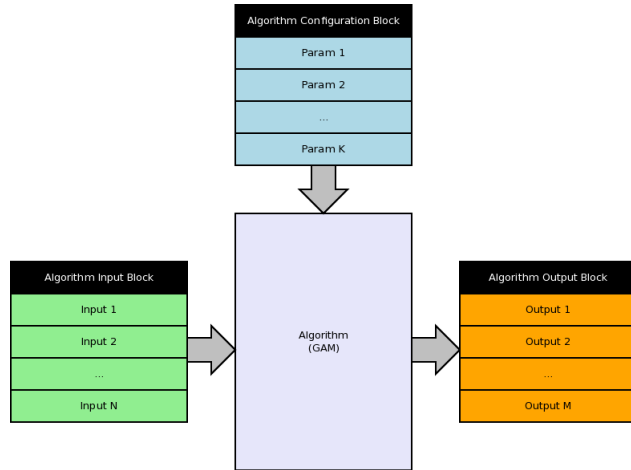


Figure 4: GAM operational scheme.

Generic Application Module

The **GAMs** are the software components where **real-time user algorithms** must be implemented.

They should not perform neither data I/O operations nor OS calls (e.g., accessing files, network sockets, devices...).

Configuration file: GAM

```
1 +GAM1 = {  
2   Class = ExampleGAM  
3   InputSignals = {  
4     Input1 = {  
5       DataSource = DDB1  
6       Type = uint32  
7     }  
8   }  
9   OutputSignals = {  
10    Output1 = {  
11      DataSource = DDB1  
12      Type = uint32  
13    }  
14  }  
15  Parameters = {  
16    Param1 = (uint32) 1000  
17  }  
18 }
```

Listing 2: GAM configuration structure.

DataSources & Brokers

DataSources

The **DataSources** are the software components that provide an interface for the **exchange of input and output signals data** with the memory and the hardware.

Brokers

The **Brokers** are the software components that provide the **interface between the GAMs and the DataSources memory areas**, exchanging data between the two.



Figure 4: DataSources and Brokers operational scheme.

Configuration file: DataSource

```
1 +Data = { // Identifies DataSourcees section in the cfg
2     Class = ReferenceContainer
3     +DDB1 = {
4         Class = GAMDataSource
5     }
6     +Timer = {
7         Class = LinuxTimer
8         SleepNature = Default
9         Signals = {
10             Counter = {
11                 Type = uint32
12             }
13             Time = {
14                 Type = uint32
15             }
16         }
17     }
18 }
```

Listing 3: DataSource configuration structure.

GAMs are grouped in **real-time threads** which are executed in the context of specific **states**.

A Real-Time Application shall be in one (**and only one**) state at a given time.

Configuration file: States

```
1 +States = { // Identifies the States section
2   Class = ReferenceContainer
3   +State1 = { // For every state, multiple threads
4     Class = RealTimeState
5     +Threads = {
6       Class = ReferenceContainer
7       +Thread1 = {
8         Class = RealTimeThread
9         CPUs = 0x8 // CPU affinity
10        Functions = {GAMTimer, ...} // Multiple GAMs
11      }
12    } ...
13  }
14 }
15 ...
16 }
```

Listing 4: States section of a configuration file.

Configuration file: Scheduler

A **real-time, OS-abstracting scheduler** handles the execution of real-time threads.

```
1 +Scheduler = { // Identifies the Scheduler section
2   Class = GAMScheduler
3   TimingDataSource = Timings
4 }
```

Listing 5: Scheduler section of a configuration file.

Example: Real-Time Application

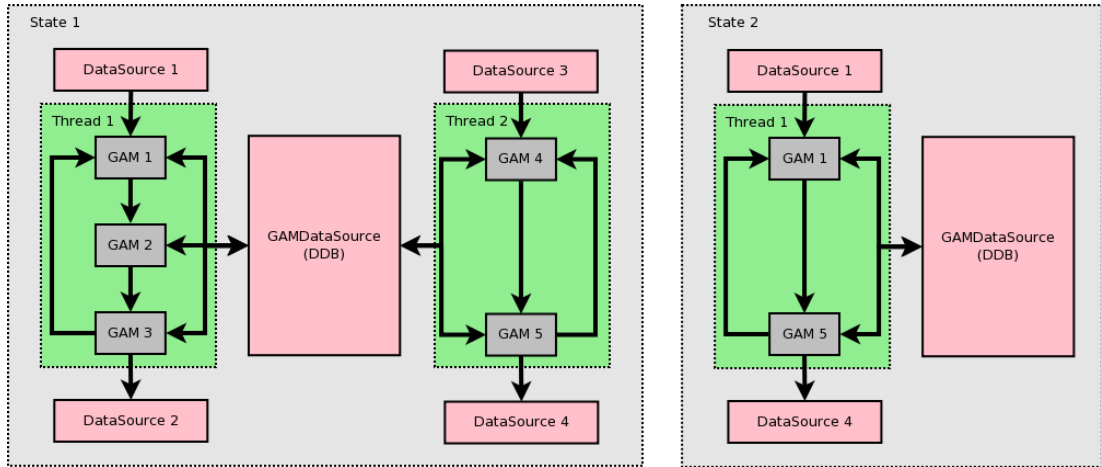


Figure 5: Example of a multi-state and multi-threaded Real-Time Application.

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StateMachine

The **StateMachine** is a software component used to **synchronize the application states** with the external environment:

- it can be in **one and only one** state at a given time;
- transitions between states are handled by **Events**;
- it allows to associate the sending of **Messages** to **Events**, *i.e.*, to **trigger transitions externally**.

Warning

Be careful not to confuse the states of the **Real-Time Application** with the states of the **StateMachine**! The latter is just another software component you can use to implement FSMs.

Example: StateMachine

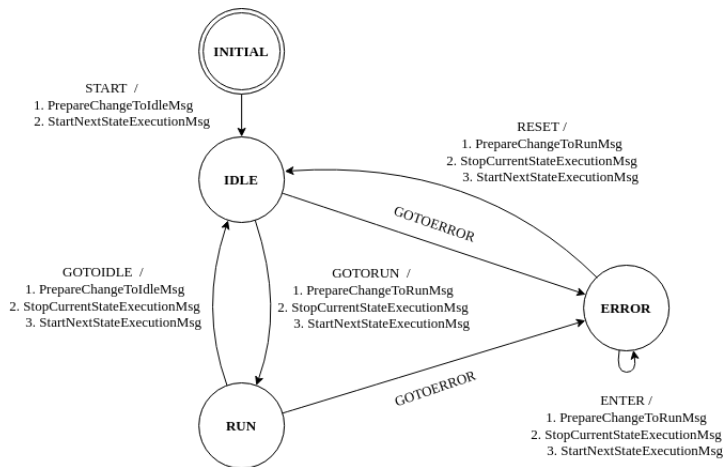


Figure 6: Example of StateMachine diagram.

Configuration file: StateMachine

```
1 +FSM = { // Declares a StateMachine named FSM
2     Class = StateMachine
3     +STATE1 = {
4         Class = ReferenceContainer
5         +GOTOSTATE2 = {
6             Class = StateMachineEvent
7             ...
8         }}
9     +STATE2 = {
10        Class = ReferenceContainer
11        +GOTOSTATE1 = {
12            Class = StateMachineEvent
13            ...
14        }}}
```

Listing 6: StateMachine high-level configuration structure (brackets due to space constraints).

A **StateMachineEvent** represents a transition and defines:

- **NextState**, the next state to go to;
- **NextStateError**, the state to go to on error;
- one or more **Messages** to send when each of the following is executed:
 - ▶ `PrepareNextState`
 - ▶ `StopCurrentStateExecution`
 - ▶ `StartNextStateExecution`

Configuration file: StateMachineEvent

```
1 +GOTOSTATE2 = {  
2     Class = StateMachineEvent  
3     NextState = STATE2  
4     NextStateError = ERROR  
5     +PrepareChangeToState2Msg = {  
6         Class = Message  
7         Function = PrepareNextState}  
8     +StopCurrentStateExecutionMsg = {  
9         Class = Message  
10        Function = StopCurrentStateExecution}  
11    +StartNextStateExecutionMsg = {  
12        Class = Message  
13        Function = StartNextStateExecution}  
14 }
```

Listing 7: StateMachineEvent configuration structure (brackets due to space constraints).

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Model creation

Create the model paying attention to data types.

Define inputs as **Inport blocks** and name them.

Define outputs as **Outport blocks** and name them.

Compiled **shared libraries** can then be loaded by a **SimulinkWrapperGAM**.

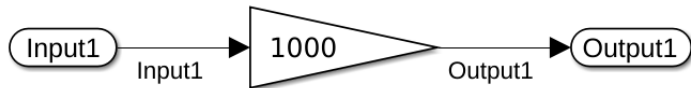


Figure 7: Basic Simulink model example.

Parameters

Parameters can be:

- **static**, no longer modifiable after the code generation;
- **tunable**, so they can be modified at runtime.



Figure 8: Basic Simulink model with a tunable parameter.

Code generation

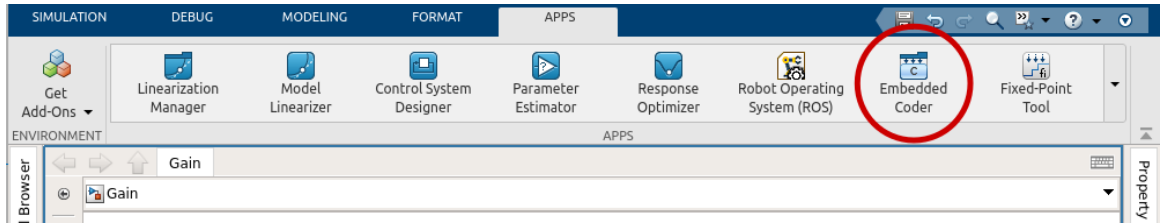


Figure 9: Embedded Coder app location.

Code generation

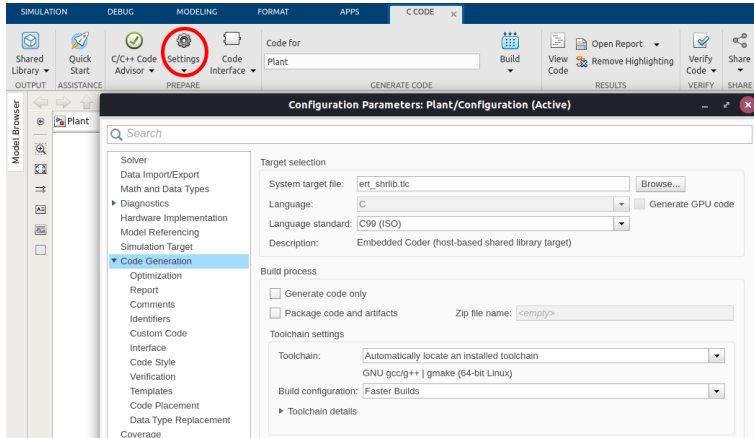


Figure 10: Code generation settings.

Code generation

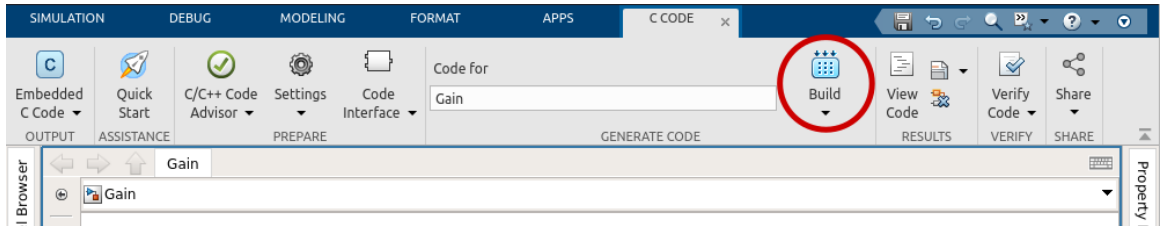


Figure 11: Build button location.

Configuration file: SimulinkWrapperGAM

```
1 +GAMGain = {  
2     Class = SimulinkWrapperGAM  
3     Library = Gain.so // Library name  
4     SymbolPrefix = Gain // Model name  
5     InputSignals = {  
6         Input1 = {  
7             DataSource = DDB1  
8             Type = int32}}  
9     OutputSignals = {  
10        Output1 = {  
11            DataSource = DDB1  
12            Type = int32}}  
13    Parameters = {  
14        Param1 = (int32) 1000}  
15 }
```

Listing 8: SimulinkWrapperGAM configuration structure (brackets due to space constraints).

Example: Control system

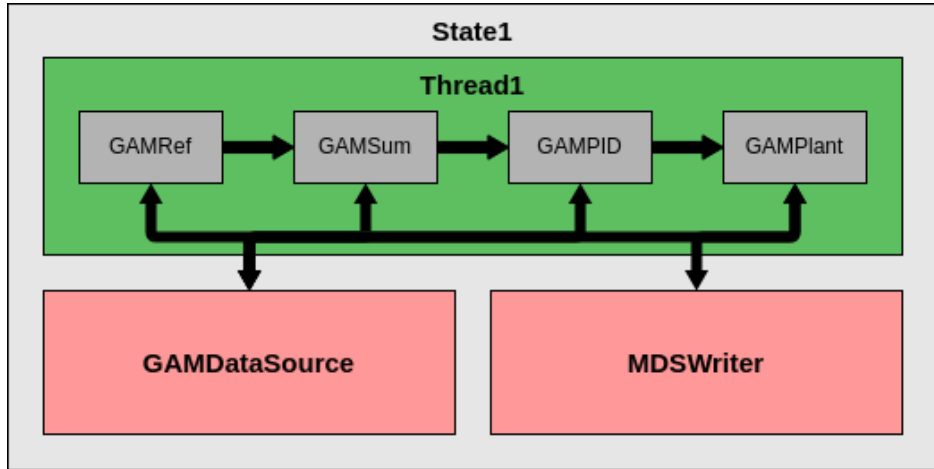


Figure 12: Control system MARTe app operational scheme.

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MDSplus

MDSplus is a tool for **data acquisition and storage**.

MDSplus stores data in a user-defined hierarchical structure, namely a **tree**.

A **tree** is formed by **nodes**, each of which represents a **data field**.

Experiments of the same type have the same tree structure and an **incremental pulse number**.

Trees contents can be inspected with:

- **jScope** to plot signals;
- **jTraverser** to navigate the tree, inspect nodes and their values;
- **MDSReader**, **MDSWriter** DataSources.

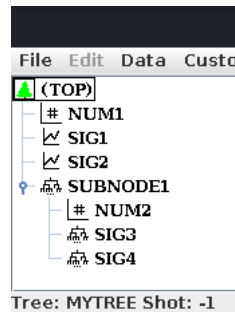


Figure 13: Example of MDSplus tree visualized with jTraverser.