

MARTe2

Introduction and Examples

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TOR VERGATA
UNIVERSITY OF ROME

School of Engineering

Roadmap

- 1 Introduction
- 2 Core library
- 3 Real-Time Applications
- 4 State Machine
- 5 MATLAB Coder
- 6 MDSplus

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Multi-threaded Application Real-Time executor

MARTe is a **C++ modular** and **multi-platform framework** for the development of **multi-threaded real-time control system applications**.

- **MARTe1** is the previous version of this framework;
- **MARTe1** was deployed in many fusion real-time control systems, i.e. JET tokamak;
- The use of **MARTe1** increased the number of supported environments and platforms;



Figure 1: JET Tokamak

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- the **environment** details, i.e. Linux, FreeRTOS, Windows;
- the **real-time algorithms**, i.e. the user code;

Code organisation

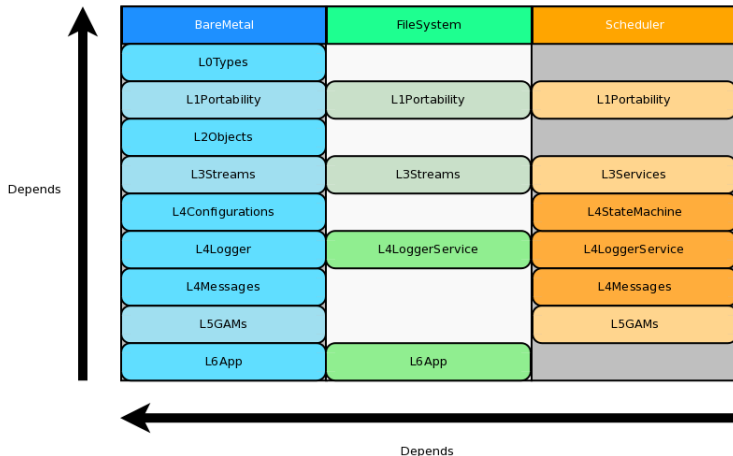


Figure 2: Code library

Makefile

The build of the core library (and all MARTe2 base projects) follows this structure:

- The **Makefile.os-arch** defines the **TARGET** operating system and architecture;

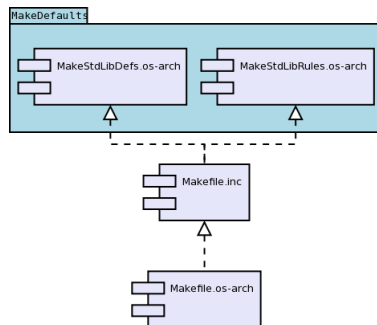


Figure 3: Makefile structure, from bottom to top

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- The **Makefile.inc** defines all the common rules;

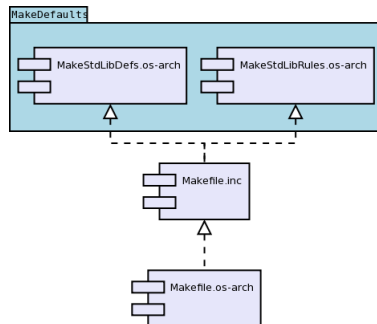


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- The **Makefile.os-arch** defines the **TARGET** operating system and architecture;
- The **Makefile.inc** defines all the common rules;
- The **MakeDefaults** defines the specific rules for the **TARGET**;

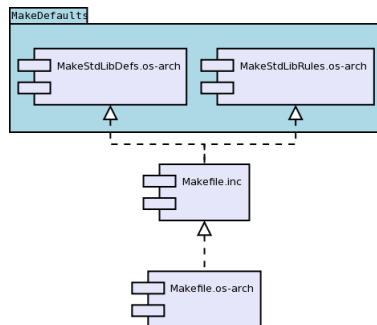


Figure 3: Makefile structure, from bottom to top

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- MARTe2 offers a generic base application, see **MARTeApp.cpp**;
- Real-Time Applications are built from the base one through a **Configuration Files** (.cfg);
- The **Configuration Files** defines the algorithms to be executed (**GAMs**) and the hardware or software involved (**Data Sources**).

Configuration file: RT App

```
1 $RTApp = {  
2   Class = RealTimeApplication  
3   +Functions = { // GAMs  
4     Class = ReferenceContainer  
5     ...  
6   }  
7   +Data = { // Data Sources  
8     Class = ReferenceContainer  
9     ...  
10  }  
11  +States = { // RT States  
12    Class = ReferenceContainer  
13    ...  
14  }  
15  +Scheduler = { // Scheduler  
16    ...  
17  }  
18 }
```


Generic Application Module

The **GAMs** are the components where user-algorithms are to be implemented.

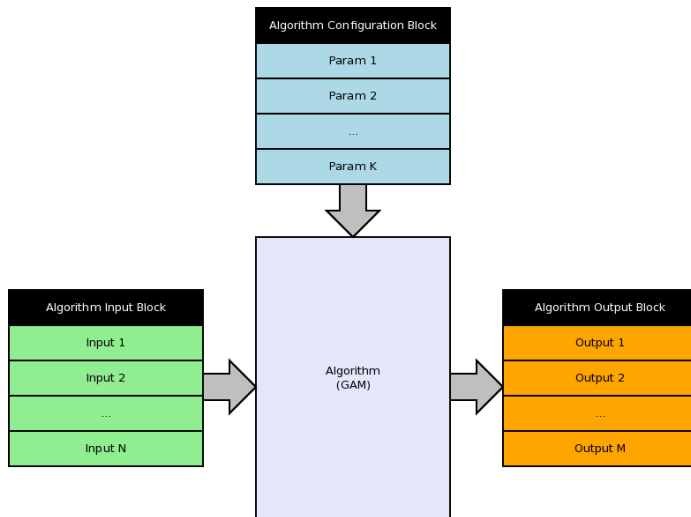


Figure 4: GAM

Generic Application Module

The **GAMs** are the components where user-algorithms are to be implemented.

Warning

No interface with operating system (e.g. reading from files/sockets)

Configuration file: GAMs

```
1 +GAM1 = {  
2     Class = ExampleGAM  
3     InputSignals = {  
4         Input1 = {  
5             DataSource = DDB1  
6             Type = uint32  
7         }  
8     }  
9     OutputSignals = {  
10        Output1 = {  
11            DataSource = DDB1  
12            Type = uint32  
13        }  
14    }  
15    Parameters = {  
16        Param1 = (uint32) 1000  
17    }  
18 }
```

Data Sources & Brokers

Data Sources

The **Data Sources** are the components that provide a interface for the interchange of input and output signals with the memory and the hardware.

Brokers

The **Brokers** are the components that provide the interface between the GAMs memory and the DataSource data.

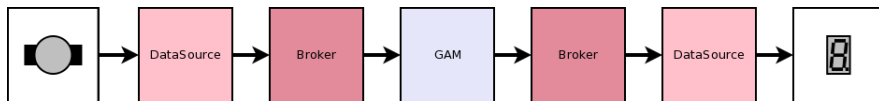


Figure 4: Data Sources & Brokers

Configuration file: Data Sources

```
1 +Data = { // Data Sources
2     Class = ReferenceContainer
3     +DDB1 = {
4         Class = GAMDataSource
5     }
6     +Timer = {
7         Class = LinuxTimer
8         SleepNature = Default
9         Signals = {
10            Counter = {
11                Type = uint32
12            }
13            Time = {
14                Type = uint32
15            }
16        }
17    }
18 }
19 }
```

- **GAMs** are grouped in real-time **threads** which are executed in the context of specific **states**.
- A Real-Time application shall be in one (**and only one**) state at a given time.

Configuration file: States

```
1 +States = { // RT States
2     Class = ReferenceContainer
3     +State1 = {
4         Class = RealTimeState
5         +Threads = {
6             Class = ReferenceContainer
7             +Thread1 = {
8                 Class = RealTimeThread
9                 CPUs = 0x8
10                Functions = {GAMTimer}
11            }
12            ...
13        }
14    }
15    ...
16 }
```


Configuration file: Scheduler

- A **real-time scheduler** handles thread execution;

```
1 +Scheduler = { // Scheduler
2   Class = GAMScheduler
3   TimingDataSource = Timings
4 }
```

Example: Real-Time Application

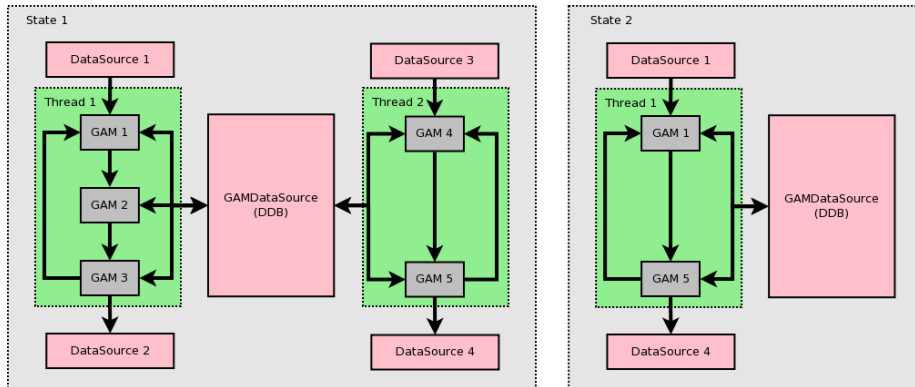


Figure 5: Example of a multi-state and multi-threaded real-time application

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State Machine

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The **State Machine** is a component used to synchronise the application states against the external environment.

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- The State Machine can be in **one and only one** state at a given time.
- The transitions between states are handled by **Events**;
- The State Machine components allows to associate the sending of **Messages** to **Events**;

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- The transitions between states are handled by **Events**;
- The State Machine components allows to associate the sending of **Messages** to **Events**;

Warning

Be careful not to confuse the states of the **Real-Time application** with the states of the **State Machine**!

Example: State Machine

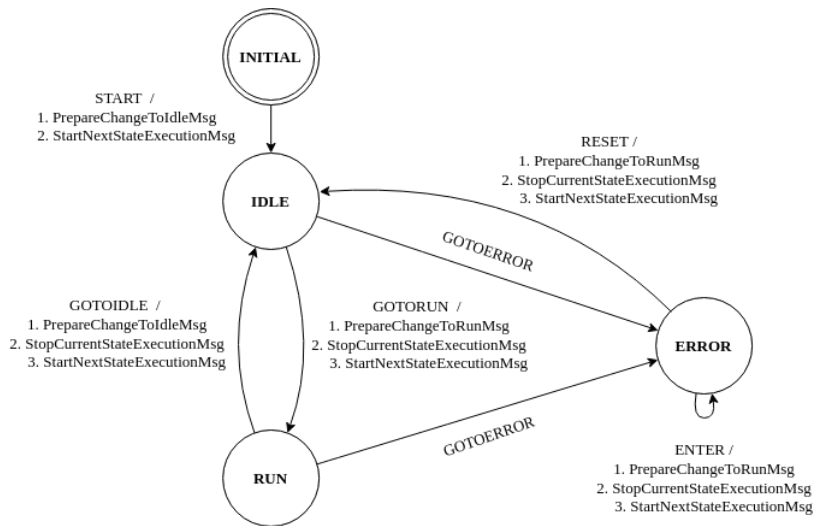


Figure 6: State Machine diagram

Configuration file: State Machine

```
1 +FSM = {  
2     Class = StateMachine  
3     +STATE1 = {  
4         Class = ReferenceContainer  
5         +GOTOSTATE2 = {  
6             Class = StateMachineEvent  
7             ...  
8         }  
9     }  
10    +STATE2 = {  
11        Class = ReferenceContainer  
12        +GOTOSTATE1 = {  
13            Class = StateMachineEvent  
14            ...  
15        }  
16    }  
17 }
```


State Machine Event

A **StateMachineEvent** represents a transition and defines:

- **NextState**, the next state to pass through;
- **NextStateError**, the state to pass through on error;
- One or more **Messages** to send when is triggered:
 - ▶ PrepareNextState:
 - ▶ StopCurrentStateExecution:
 - ▶ StartNextStateExecution:

Configuration file: State Machine

```
1 +GOTOSTATE2 = {
2     Class = StateMachineEvent
3     NextState = STATE2
4     NextStateError = ERROR
5     +PrepareChangeToState2Msg = {
6         Class = Message
7         Function = PrepareNextState
8     }
9     +StopCurrentStateExecutionMsg = {
10         Class = Message
11         Function = StopCurrentStateExecution
12     }
13     +StartNextStateExecutionMsg = {
14         Class = Message
15         Function = StartNextStateExecution
16     }
17 }
```

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Model creation

- Create the model paying attention to data types;
- Define inputs as **Inport blocks** and name them;
- Define outputs as **Outport blocks** and name them;

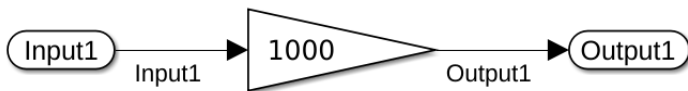


Figure 7: Base model

Param configuration

Parameters can be:

- **static**, so no longer modifiable after the code generation;
- **tunable**, so they can be modified at runtime;



Figure 8: Base model with a tunable parameter

Code generation

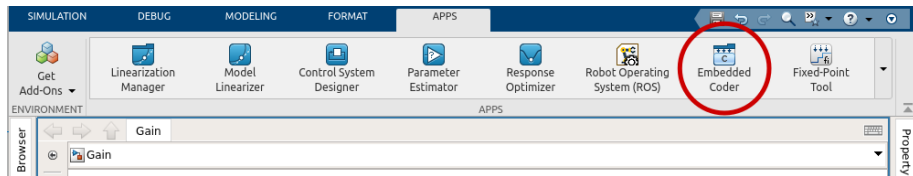


Figure 9: Embedded coder app

Code generation

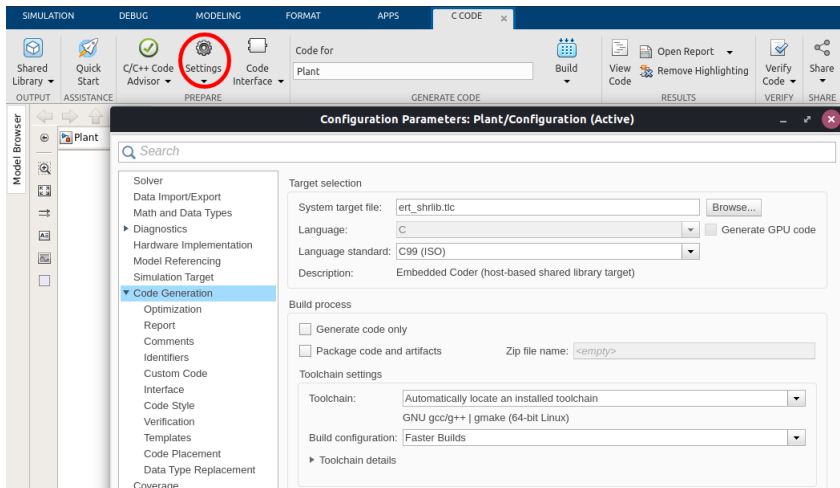


Figure 10: Code generation settings

Code generation

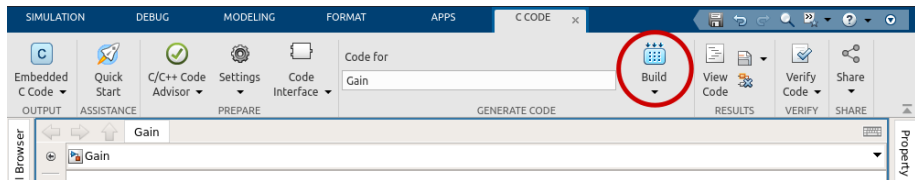


Figure 11: Base model with tunable parameter

Configuration file: SimulinkWrapperGAM

```
1 +GAMGain = {
2     Class = SimulinkWrapperGAM
3     Library = Gain.so // Library name
4     SymbolPrefix = Gain // Model name
5     InputSignals = {
6         Input1 = {
7             DataSource = DDB1
8             Type = int32
9         }
10    }
11    OutputSignals = {
12        Output1 = {
13            DataSource = DDB1
14            Type = int32
15        }
16    }
17    Parameters = {
18        Param1 = (int32) 1000
19    }
20 }
```

Example: Control System

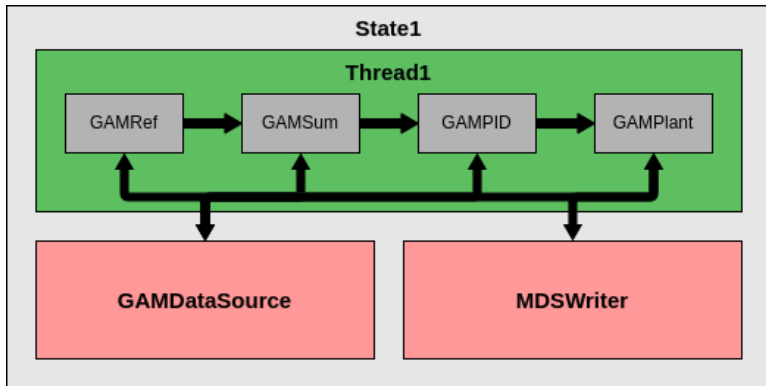


Figure 12: Control system app scheme

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- **MDSplus** is a tool for data acquisition and storage;
- **MDSplus** stores data in a user-defined hierarchical structure, namely a tree;
- A tree is formed by nodes, each of which represents a data field;
- Experiments of the same type have the same tree structure and an incremental pulse number;

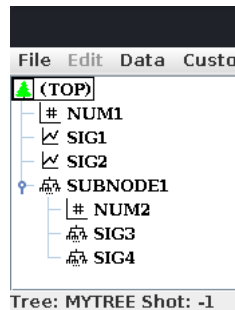


Figure 13: MDSplus tree