

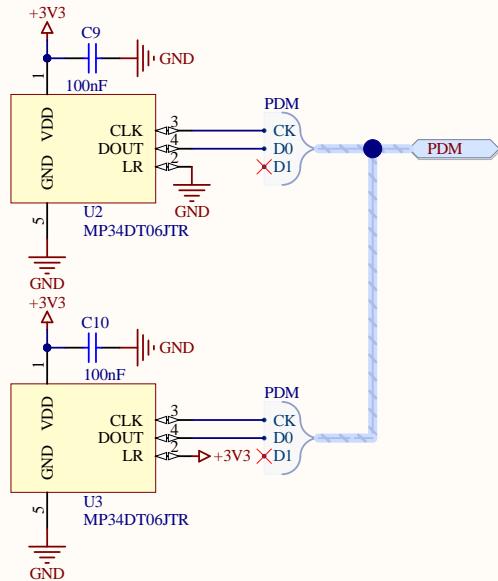
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Title: Portenta Vision Shield	
ID: SL-ASX00021	Revision: 4.0
Date: 1/26/2021	Time: 4:36:36 PM
Sheet 1 of 9	
File: TOPView.SchDoc	Author: Francesca Cenna
	Rev author:

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Title: Portenta Vision Shield

ID: *

Revision 4.0

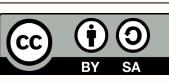
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Time: 4:36:39 PM

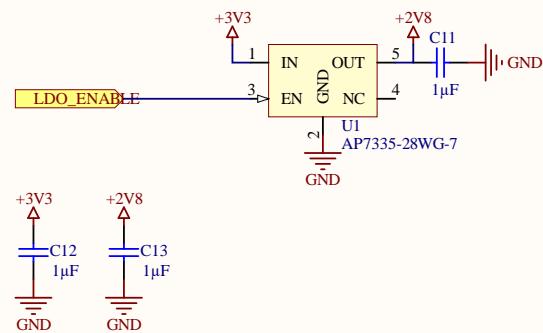
Sheet 1 of 9

File: Mics.SchDoc

Author: Francesca Cenna



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Title: Portenta Vision Shield

ID: *

Revision: 4.0

Date:

1/26/2021

Time:

4:36:40 PM

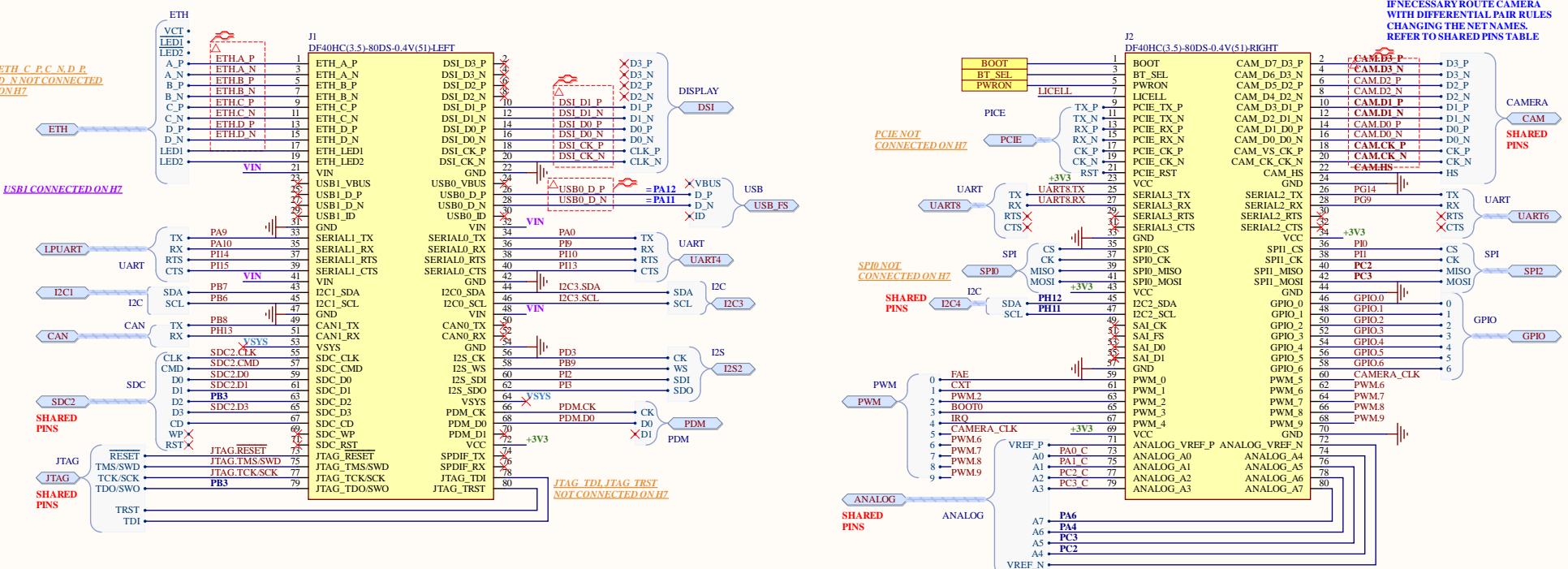
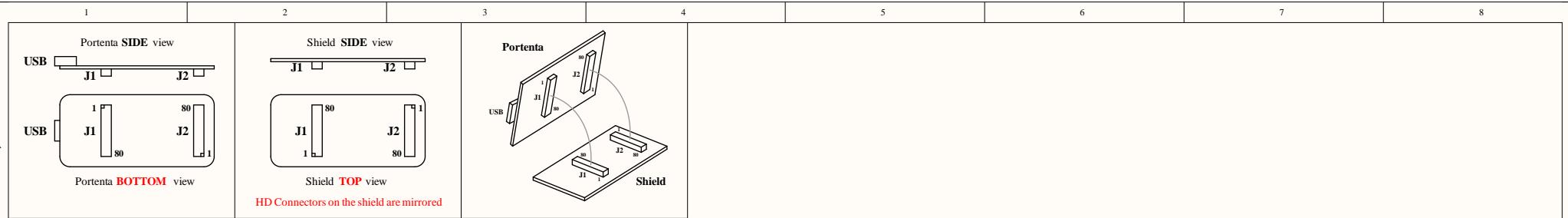
Sheet:

5 of 9

File: LDOs.SchDoc

Author: Francesca Cenna





SHARED PINS TABLE

NET	BUS 1	BUS 2
PB3	JTAG	SDC2
CAM_DL_N	CAM	I2C4
CAM_DL_P	CAM	I2C4
CAM_CK_P	CAM	SAI2A
CAM_DL_N	CAM	SAI2A
CAM_DL_P	CAM	SAI2A
CAMHS	CAM	ANALOG
CAMCK_N	CAM	ANALOG
PC2	ANALOG	SP12
PC3	ANALOG	SP12

POWER NETS TABLE

NET	TYPE	RANGES	DESCRIPTION	
VIN +3V3 VSYS LICELL	VIN VCC VSYS LICELL	PORTENTA INPUT PORTENTA OUTPUT PORTENTA RESERVED OUTPUT PORTENTA INPUT	4.1V to 6V. 1.1V to 3.3V in steps, max 1A. RESERVED, DO NOT USE 3.5V to 4.2V, max 600mA. Circuit cell max 3.6V, max 46uA.	Default 3.3V, PMIC (U10) programmable output. Default 3.3V, PMIC (U10) programmable output. Default 4.2V, PMIC (U10) programmable output which is also the input voltage of the bus inside the PMIC itself. Max 4uA with PMIC (U10) in coin cell mode, max 46uA with PMIC in standby/suspend mode.

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Title: Portenta Vision Shield			
ID: *.	Revision: 4.0		
Date: 1/26/2021	Time: 4:36:41 PM	Sheet * of 9	
File: HDConn.schDoc	Author: *	RevAuthor:	

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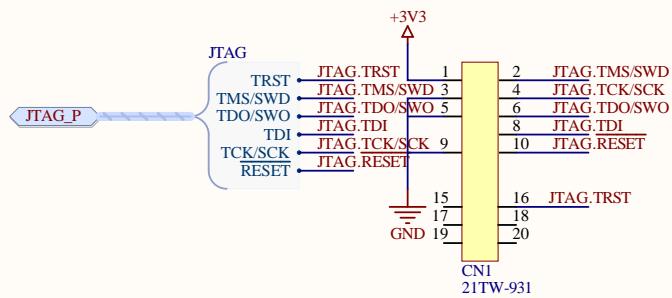
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Title: Portenta Vision Shield

ID: *

Revision 4.0

Date: 1/26/2021

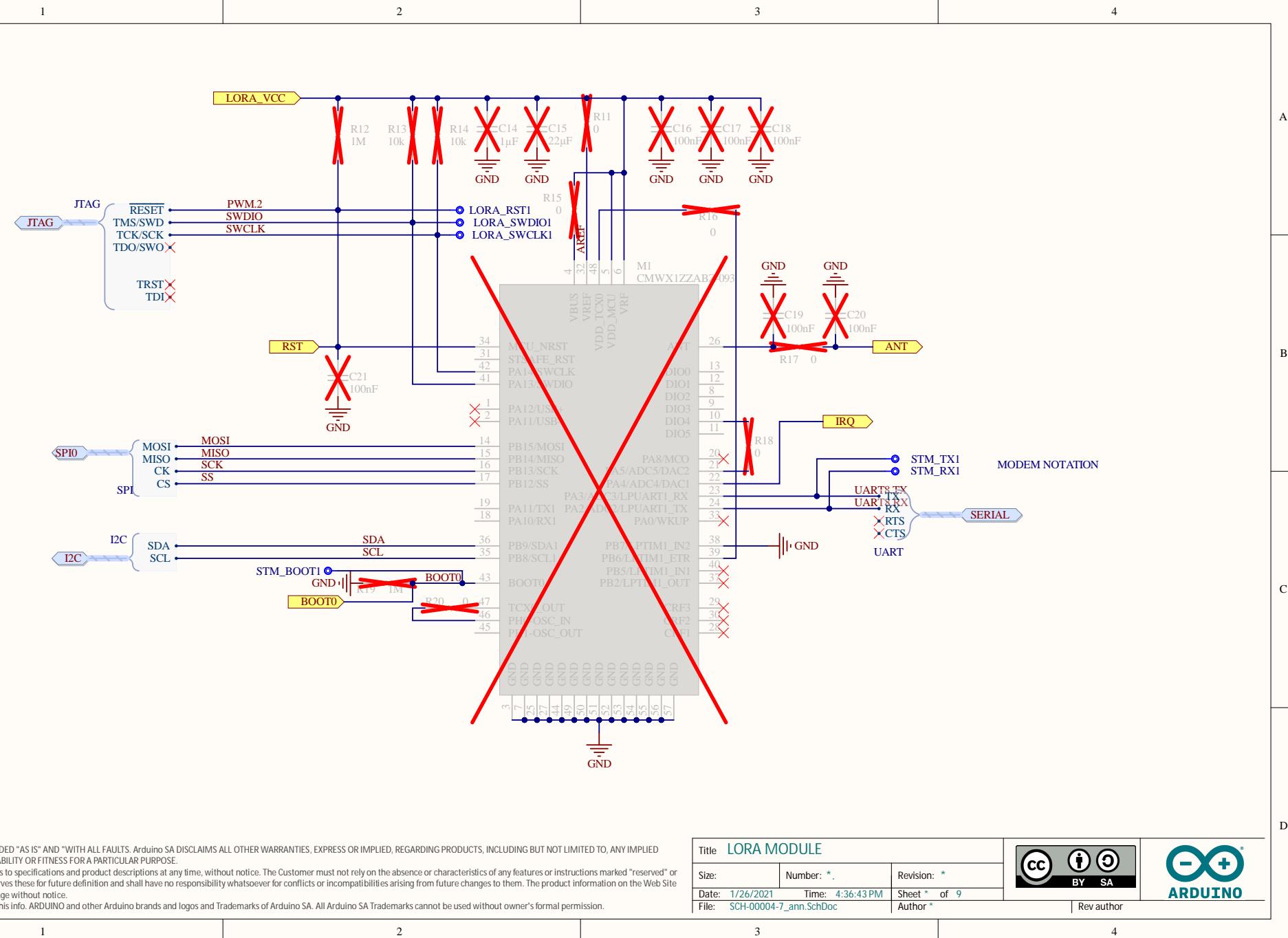
Time: 4:36:42 PM

Sheet 1 of 9

File: JTAG.SchDoc

Author: Francesca Cenna





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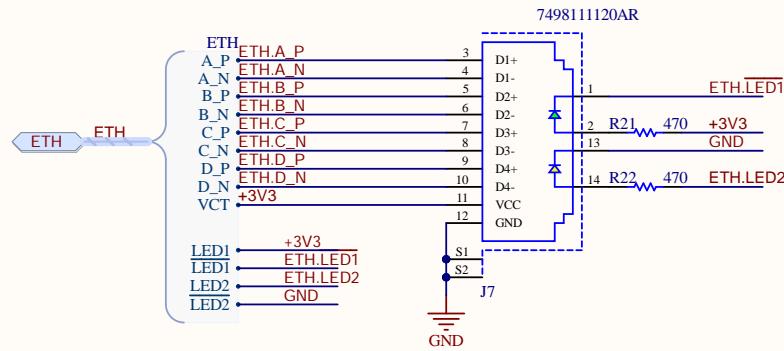
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Title: Ethernet 7498111120AR

ID: *

Revision: *

Date: 1/26/2021 Time: 4:36:44 PM Sheet: 9 of 9

File: SCH-00005-6_ann.SchDoc Author: *



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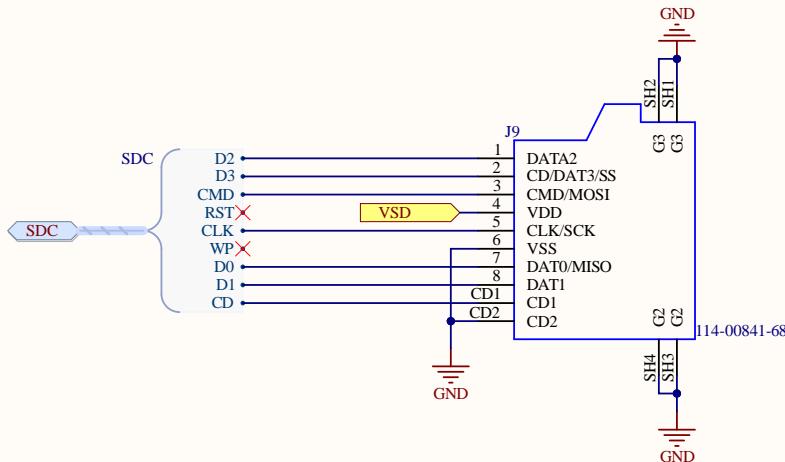
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Title: CameraShield

ID: *

Revision: *

Date: 1/26/2021 Time: 4:36:45 PM

Sheet of 9

File: SCH-00009-2_anno.SchDoc

Author: Francesca Cenna

