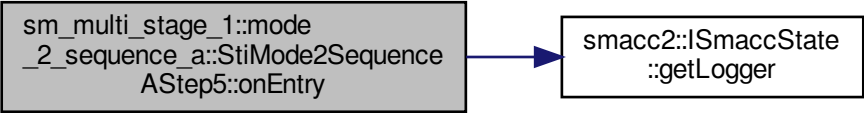


sm_multi_stage_1::mode
_2_sequence_a::StiMode2Sequence
AStep5::onEntry



```
graph LR; A["sm_multi_stage_1::mode_2_sequence_a::StiMode2SequenceAStep5::onEntry"] --> B["smacc2::ISmaccState::getLogger"]
```

smacc2::ISmaccState
::getLogger