

```
graph BT; A["smacc2_sm_reference  
_library/sm_multi_ur5  
_sim/src/sm_multi_ur5  
_sim/sm_multi_ur5_sim_node.cpp"] --> B["smacc2_sm_reference  
_library/sm_multi_ur5  
_sim/include/sm_multi  
_ur5_sim/sm_multi_ur5_sim.hpp"]; B --> C["smacc2_sm_reference  
_library/sm_multi_ur5  
_sim/include/sm_multi  
_ur5_sim/st_move  
_end_effector.hpp"]
```

smacc2\_sm\_reference  
\_library/sm\_multi\_ur5  
\_sim/include/sm\_multi  
\_ur5\_sim/st\_move  
\_end\_effector.hpp

smacc2\_sm\_reference  
\_library/sm\_multi\_ur5  
\_sim/include/sm\_multi  
\_ur5\_sim/sm\_multi\_ur5\_sim.hpp

smacc2\_sm\_reference  
\_library/sm\_multi\_ur5  
\_sim/src/sm\_multi\_ur5  
\_sim/sm\_multi\_ur5\_sim\_node.cpp