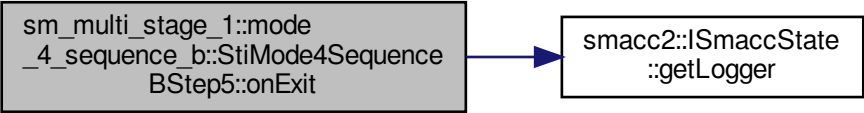


sm_multi_stage_1::mode
_4_sequence_b::StiMode4Sequence
BStep5::onExit



```
graph LR; A["sm_multi_stage_1::mode  
_4_sequence_b::StiMode4Sequence  
BStep5::onExit"] --> B["smacc2::ISmaccState  
::getLogger"]
```

smacc2::ISmaccState
::getLogger