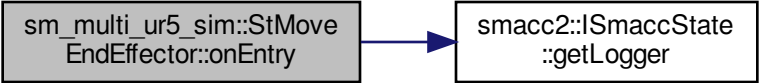


sm_multi_ur5_sim::StMove
EndEffector::onEntry



```
graph LR; A["sm_multi_ur5_sim::StMove  
EndEffector::onEntry"] --> B["smacc2::ISmaccState  
::getLogger"]
```

smacc2::ISmaccState
::getLogger