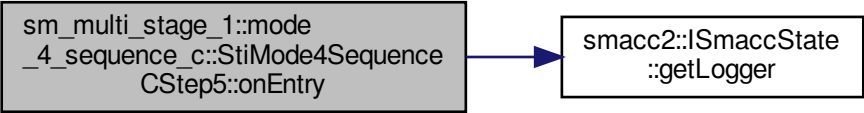


sm_multi_stage_1::mode
_4_sequence_c::StiMode4Sequence
CStep5::onEntry



```
graph LR; A["sm_multi_stage_1::mode_4_sequence_c::StiMode4SequenceCStep5::onEntry"] --> B["smacc2::ISmaccState::getLogger"]
```

smacc2::ISmaccState
::getLogger