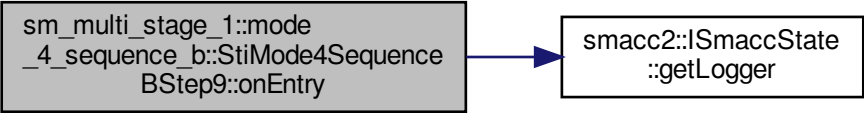


sm_multi_stage_1::mode
_4_sequence_b::StiMode4Sequence
BStep9::onEntry



```
graph LR; A["sm_multi_stage_1::mode_4_sequence_b::StiMode4SequenceBStep9::onEntry"] --> B["smacc2::ISmaccState::getLogger"]
```

smacc2::ISmaccState
::getLogger