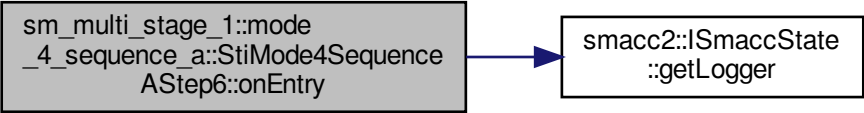


sm_multi_stage_1::mode
_4_sequence_a::StiMode4Sequence
AStep6::onEntry



```
graph LR; A["sm_multi_stage_1::mode_4_sequence_a::StiMode4SequenceAStep6::onEntry"] --> B["smacc2::ISmaccState::getLogger"]
```

smacc2::ISmaccState
::getLogger