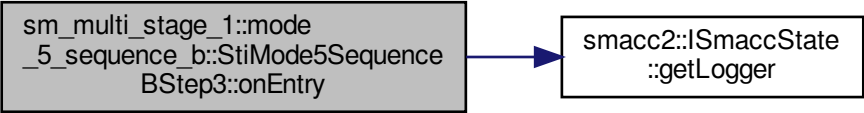


sm_multi_stage_1::mode
_5_sequence_b::StiMode5Sequence
BStep3::onEntry



```
graph LR; A["sm_multi_stage_1::mode_5_sequence_b::StiMode5SequenceBStep3::onEntry"] --> B["smacc2::ISmaccState::getLogger"]
```

smacc2::ISmaccState
::getLogger