```
{smacc2::ISmaccStateMachine
|# nh
# timer
#stateMachinePub
# stateMachineStatusPub_
# transitionLogPub
# transitionHistoryService_
# currentState
# currentStateInfo
#status_msg_
# orthogonals_
# stateMachineInfo_
- m mutex
eventQueueMutex_

    stateMachineCurrentAction

- stateCallbackConnections
- globalData
- transitionLogHistory_
- runMode

    signalDetector_

    stateSeqCounter_

+ ISmaccStateMachine()
+ ~ISmaccStateMachine()
+ reset()
+ stop()
+ eStop()
+ getOrthogonal()
+ getClientBehavior()
+ getOrthogonals()
+ requiresComponent()
+ postEvent()
                                           {boost::statechart::
+ postEvent()
                                           asynchronous_state_machine
                                           < DerivedStateMachine, InitialState
+ getGlobalSMData()
+ setGlobalSMData()
                                           Type, SmaccFifoScheduler, SmaccAllocator >
+ mapBehavior()
+ getStateMachineName()
+ state_machine_visualization()
+ getCurrentStateInfo()
+ publishTransition()
+ onInitialize()
+ getTransitionLogHistory()
+ createSignalConnection()
+ notifyOnStateEntryStart()
+ notifyOnStateEntryEnd()
+ notifyOnRuntimeConfigured()
+ notifyOnStateExitting()
+ notifyOnStateExited()
+ disposeStateAndDisconnect
Signals()
+ notifyOnRuntimeConfiguration
Finished()
+ getCurrentStateCounter()
+ getCurrentState()
+ getStateMachineInfo()
+ buildStateMachineInfo()
+ getNode()
+ getLogger()
+ getMutex()
# checkStateMachineConsistence()
# initializeROS()
# onInitialized()
# createOrthogonal()
propagateEventToStateReactors()
- updateStatusMessage()
                          {smacc2::SmaccStateMachine
                          Base< DerivedStateMachine,
                                InitialStateType >
                                                                {boost::statechart::
                          ||+ SmaccStateMachineBase()
                                                                asynchronous_state_machine
                          + ~SmaccStateMachineBase()
                                                                < SmCoretestTransitionSpeed1,
                                                                State1, SmaccFifoScheduler,
                          + reset()
                                                                       SmaccAllocator >
                          + stop()
                          + eStop()
                          + initiate_impl()
                                        }
                                          < SmCoretestTransitionSpeed1,</p>
                                                     State1 >
                      {smacc2::SmaccStateMachine
                      Base < SmCoretestTransitionSpeed1.
                                     State1 >
                      ||+ SmaccStateMachineBase()
                      + ~SmaccStateMachineBase()
                      + reset()
                      + stop()
                      + eStop()
                      + initiate_impl()
                         {sm_coretest_transition
                         _speed_1::SmCoretestTransition
                                     Speed1
                         ||+ onInitialize()
```