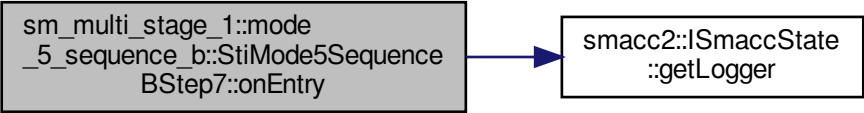


sm_multi_stage_1::mode
_5_sequence_b::StiMode5Sequence
BStep7::onEntry



```
graph LR; A["sm_multi_stage_1::mode  
_5_sequence_b::StiMode5Sequence  
BStep7::onEntry"] --> B["smacc2::ISmaccState  
::getLogger"]
```

smacc2::ISmaccState
::getLogger