

mruby Bytecode Loader Using Bluetooth in Multi-VM Environment

Takuro Yamamoto
Graduate School of
Engineering Science, Osaka
University
Osaka, JAPAN

Hiroshi Oyama
OKUMA Corporation
Aichi, JAPAN

Takuya Azumi
Graduate School of
Engineering Science, Osaka
University
Osaka, JAPAN

ABSTRACT

In recent years, the productivity of embedded systems has become a problem due to their complexity and large-scale. For the purpose of improving the productivity for embedded software development, the mruby on TECS framework has been proposed that is applied mruby (Lightweight Ruby) and supports component-based development. In the current mruby on TECS, the mruby programs have to be compiled and linked every time the programs are modified because the mruby bytecodes are incorporated in the platform. Moreover, while the framework supports multi-VM, developers need to be familiar with the functions of real-time operating systems to effectively execute multiple mruby programs in concurrent or/and parallel. To improve the development efficiency, this paper proposes an mruby bytecode loader using Bluetooth as an extension of mruby on TECS. The loader executes two mruby bytecodes, mruby application bytecode and mruby library bytecode. mruby application bytecode modified frequently is sent from a host to a target device by developers. mruby library bytecode modified infrequently is preserved beforehand with the platform in a storage/ROM device at the time of the first compilation. In addition, multiple mruby programs cooperatively run in the proposed framework. A RiteVM scheduler makes multitasking processing more easy-to-use than that of mruby on TECS. Synchronization of starting multiple tasks is also implemented with Eventflag. Experimental results demonstrate the advantages of the proposed framework.

Keywords

embedded software; script language; component system;

1. INTRODUCTION

In these days, embedded systems have been required the high-quality and the high-performance. Due to this trend, the complexity of embedded systems also increases and the

scale is larger. For example, IoT (Internet of Things) applications. The low production cost and the short developing period of time are also needed.

An approach of efficient software development is to use component-based techniques. CBD (Component-Based Development) is a design technique for constituting reusable components [13]. Complex and large scale software systems can be developed efficiently using component-based techniques. That is because software componentization provides high-reusability and easy verification. It also makes a system flexible in extensions and specification changes. There are TECS [11], AUTOSAR [1], and SaveCCM [17] as a typical component-based development for embedded systems

In addition, another approach of efficient software development is to develop with script languages. Most of current software are programmed in C language, and the development with C takes a high cost and more time to develop. Script languages make software engineering more efficient and shorten a development period because script languages have high-productivity from their usability. Java script, Perl, Python, Lua and Ruby are well-known as representative script languages.

Although script languages are easy to use and read, their execution time are slower than C language's. For embedded systems, the real-time properties such as estimation of worst-execution time are very important factors. Therefore, it is difficult to apply the script languages to embedded systems.

mruby on TECS is a component-based framework for running script program [9]. It is integrated two technologies. One is mruby, which is a script language for embedded systems [21], [5]. The other is TECS (TOPPERS Embedded Component System), which is a component-based framework for embedded systems [11] [8]. mruby on TECS supports to effectively run mruby script language on embedded systems. mruby on TECS also makes execution time 100 times faster than that of mruby.

mruby on TECS has several problems at present. One of the problems is low software development efficiency. mruby on TECS only supports a storage/ROM device to load mruby programs. For example, an SD card should be inserted and pulled out repeatedly, or a ROM should be rewritten if mruby programs are modified. Developers should also restart an OS on a target device. Moreover, although mruby on TECS has supported the multi-VM, developers need to call OS's function in order to execute multiple tasks. These problem are a big burden on developers.

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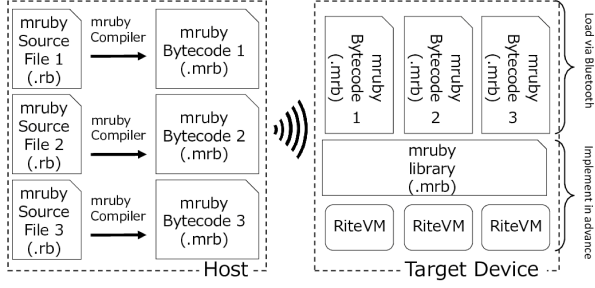


Figure 1: System Model of the proposed framework

This paper proposes an extended framework of mruby on TECS: an mruby bytecode loader using Bluetooth and a RiteVM scheduler for fairly executing mruby programs. To improve the development efficiency, the mruby bytecode loader using Bluetooth enables developers to write the platform in a storage device only once at the beginning and to transfer mruby application codes from a host to a target device. The RiteVM scheduler enables developers to run multitasking programs more easily than mruby on TECS.

Contribution: The proposed framework gives the contribution in the following points.

1. To improve the software development efficiency: Developers do not need to rewrite a storage/ROM device and also to restart an OS. The loader supports the continuous loading, which saves the Bluetooth set-up time. Therefore, the mruby bytecode loader using Bluetooth helps developers develop software.
2. To effectively execute multiple mruby programs in concurrent or/and parallel: Developers can execute multiple tasks without the knowledge of an RTOS because the RiteVM scheduler switches tasks cyclically.
3. To synchronize the execution of multiple RiteVM tasks: The proposed framework provides the synchronization of multiple RiteVM tasks (mruby applications).
4. To focus on the benefits of component-based developments: This paper shows the specific examples for the benefits of component-based development.

Organization: The paper is organized as follows. Section 2 introduces the basic technologies i.e. mruby, TECS and mruby on TECS. Section 3 describes the design and implementation of the proposed framework in detail. Section 4 evaluates the proposed framework, Section 5 discusses related work, and then Section 6 concludes this paper.

2. BACKGROUND

Figure 1 shows the system model of the proposed framework. The bytecodes are transferred from the host to the target device with Bluetooth. The RiteVMs and mruby library are assumed to be prepared in advance. Each bytecode is allocated to a RiteVM, respectively. Developers can run bytecodes transferred with Bluetooth in multitask.

This section describes mruby on TECS on which the proposed framework is based. mruby on TECS is a component-based framework for running script programs. In mruby on

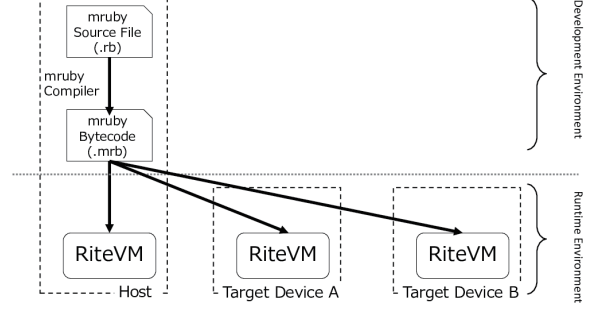


Figure 2: Mechanism of mruby/RiteVM

TECS, two technologies are integrated: mruby and TECS. To explain the system, mruby and TECS are also respectively described in this section.

2.1 mruby

mruby is the light-weight implementation of Ruby programming language complying to part of the ISO standard. Ruby is a object-oriented script language [7]. As the main feature, Ruby is easy-to-use and easy-to-read due to its simple grammar. Ruby can run a program with fewer code lines than C language. Ruby improves the productivity of a software development owing to not only simple grammar but also object-oriented functions such as classes and methods, exceptions, and garbage collection.

mruby is suitable for embedded systems because of faster execution with less amount of resources and takes over the usability and readability of Ruby. In addition, VM (Virtual Machine) mechanism is used in mruby, therefore mruby programs can run on any operating system as long as VM is implemented. A RiteVM is a VM in mruby, that runs mruby programs. The RiteVM mechanism is shown in Figure 2. The mruby compiler translates an mruby code into a bytecode, which is an intermediate code that can be interpreted by a RiteVM. The bytecodes can run on a Rite VM, and thus mruby programs can be executed on any target devices if only RiteVMs are implemented.

2.2 TECS

TECS (TOPPERS Embedded Component System) is a component system suitable for embedded systems. TECS helps increase productivity and reduce development duplication because the reusability of software is improved. TECS also helps decrease complexity of the system because the generated component diagram can visualize the structure of whole software.

The component deployment and composition in TECS are statically performed, which gives optimization. As a result, the overhead of execution time and memory consumption can be reduced. There are other features of TECS, implementation in C language, source level portability, fine-grained component, etc.

2.2.1 Component Model

Figure 3 shows a component diagram. A cell which is an instance of a component in TECS consists of entry ports, call ports, attributes and internal variables. A entry port is an interface to provide functions with other cells, and a call

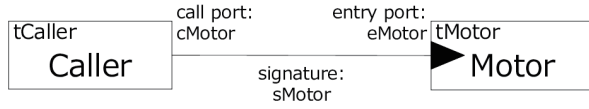


Figure 3: Component Diagram

```

1 signature sMotor{
2   int32_t getCounts( void );
3   ER resetCounts( void );
4   ER setPower( [in]int power );
5   ER stop( [in] bool_t brake );
6   ER rotate( [in] int degrees, [in] uint32_t speed_abs,
7             [in] bool_t blocking );
8   void initializePort( [in]int32_t type );
9 };

```

Figure 4: Signature Description

port is an interface to use functions of other cells. A cell has one or more entry ports and call ports. Functions of a cell are implemented in the C language.

A type of an entry/call port is defined by a signature which is a set of functions. A signature is the interface definition of a cell. The call port of a cell can be connected to the entry port of another cell with the same signature. A celltype is the definition of a cell. Celltype defines one or more call/entry ports, attributes and internal variables.

2.2.2 Component Description

The description of a component in TECS is divided into three parts, signature, celltype, and build description. TECS code is written in .cdl (component description language) file. The component description is mentioned with an example shown in Figure 3 as follow.

Signature Description

The signature description defines an interface of a cell. A signature name is described following the keyword signature. It also has the prefix "s". In this way, a signature is defined such as sMotor shown in Figure 4. To make the definition of an interface clear, specifiers such as in and out are used in TECS. [in] and [out] represent input and output, respectively.

Celltype Description

The celltype description defines entry ports, call ports, attributes, and valuables of a celltype. An example of a celltype description is shown in Figure 5. A celltype name following the keyword celltype with the prefix "t" and elements of a celltype is described. To define entry ports, a signature such as sMotor, and an entry port name such as eMotor follow the keyword entry. In the same way, call ports can be defined. Attributes and valuables follow the keyword attr and var respectively.

Build Description

The build description is used to instantiate cells and connect cells. Figure 6 shows an example of a build description. A celltype name such as tMotor, and a cell

```

1 celltype tCaller{
2   call sMotor cMotor;
3 };
4 celltype tMotor{
5   entry sMotor eMotor;
6   attr{
7     int32_t attribute;
8   };
9   var{
10    int32_t variable;
11  };
12 };

```

Figure 5: Celltype Description

```

1 cell tMotor Motor{
2 };
3 cell tCaller Caller{
4   cMotor = Motor.eMotor;
5 };

```

Figure 6: Build Description

name such as Motor follow the keyword cell. To compose cells, a call port, a signature, an entry port in order are described. In this example, an entry port eMotor in a cell Motor is connected to a call port cMotor in a cell Caller.

2.3 mruby on TECS

mruby on TECS is a component-based framework for running script language. This framework uses two technologies, mruby and TECS.

2.3.1 System Model

The system model of mruby on TECS is shown in Figure 7. Each mruby program, which is bytecode, runs on its own RiteVM as a componentized task of an RTOS. TECS components support various embedded drivers such as motor and sensor drivers.

An mruby-TECS bridge plays a role to call a native program (e.g. C legacy code) from an mruby program. The mruby-TECS bridge provides native libraries for mruby. It also gives TECS components to receive the invocation from an mruby program. The mruby-TECS bridge is described in more detail below.

As the target RTOS, TOPPERS/HRP2 [23], [16] is used in this paper. TOPPERS/HRP2 is an RTOS based on μ ITRON [20] with memory protection. However, mruby on TECS does not depend on the RTOS because TECS supports not only TOPPERS/HRP2 but also the other RTOSs such as OSEK [19] and TOPPERS/ASP [10], [22].

2.3.2 mruby-TECS Bridge

There is a great difference between the execution time of mruby and C language. According to [9], mruby programs are several hundreds times slower than C programs. The execution of mruby bytecode on a RiteVM is not as efficient as that of C language. Thus it is difficult to use mruby for all codes.

The use of Ruby on embedded devices provides the benefit of productivity and maintainability due to the ease to use and read. On the other hands, it is necessary to implement

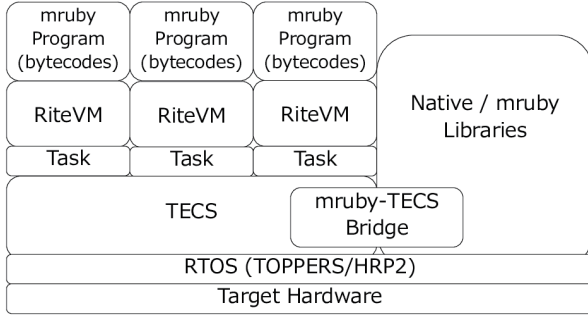


Figure 7: System Model of existing mruby on TECS



Figure 8: mruby-TECS Bridge

parts of applications in C language in order to manipulate actuators and sensors, and also make a critical section of codes run quickly.

Figure 8 shows an example of use of an mruby-TECS bridge for controlling a motor. The left side of BridgeMotor belongs to the mruby program. The right side of BridgeMotor belongs to TECS component.

The mruby-TECS bridge generates two things. One is a celltype to receive invocation from the mruby program. The other is an mruby class that corresponds to the TECS component specified by the developers to invoke a C function from the mruby program.

A code of an mruby-TECS bridge is generated. The generation code supports registration of classes and methods for mruby. The methods in an mruby class are defined by generation codes for an mruby-TECS bridge, such as setPower and stop. Thus, when a method is called in an mruby program, an mruby-TECS bridge calls the function defined in the TECS component such as a Motor cell.

3. DESIGN AND IMPLEMENTATION

Figure 9 shows the detailed system model of the proposed framework. Each mruby application bytecode transferred from the host is received by the loader in the RiteVM. The RiteVM reads the own bytecode and executes it. The mruby applications run at the same time because of synchronization processing. The RiteVM scheduler switches tasks as multiple tasks can run in concurrent.

3.1 mruby Bytecode Loader Using Bluetooth

¹ This section describes an additional functionality of mruby on TECS, mruby bytecode loader using Bluetooth. In the current system, the platform including mruby bytecodes are

¹mruby bytecode loader is intended to improve the development efficiency, therefore software developers should use it for development phase. Complete software should be compiled and linked in the storage/ROM device beforehand.

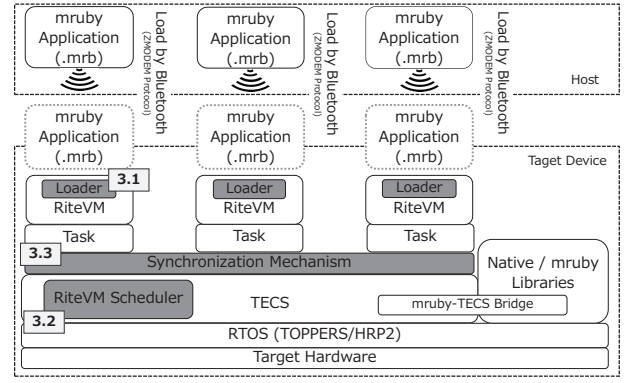


Figure 9: Detailed System Model of the proposed framework



Figure 10: Component Diagram of mruby bytecode loader using Bluetooth

saved in a storage/ROM device. Developers must rewrite the storage/ROM device every time the application programs are modified. The OS on the target device should be also restarted. It causes a low development efficiency to repeat that. An mruby bytecode loader using Bluetooth makes the developers' burden decrease. Developers should just once carry out the work to connect the storage/ROM device and start the OS.

mruby programs are consisted of mruby application and libraries. mruby application is the main code which software developers should program. mruby libraries are the codes defining the functions for application such as Ruby classes. The mruby bytecodes including mruby application and libraries can be sent and run. However, it is also wasteful in terms of the size and time to send bytecodes because libraries are not frequently modified. The proposed framework provides the design that only mruby applications are sent and mruby libraries are preserved with the platform in the storage/ROM device beforehand. Due to this design, RiteVMs can share the mruby libraries. In addition, a RiteVM can use the own library that other RiteVMs should not use.

In the proposed framework, the platform including RiteVMs and mruby library is compiled and copied in the storage/ROM device at the first. In the host, the mruby application programs (.rb) are edited and compiled into the bytecodes (.mrbl) by an mruby compiler. The generated bytecodes are transferred from the host to the target device with Bluetooth.

Moreover, developers can save the time of Bluetooth pairing since the loader can continuously load the bytecode.

3.1.1 Component of RiteVM with mruby bytecode loader using Bluetooth

The proposed framework provides a RiteVM with mruby bytecode loader using Bluetooth as TECS component. The component is an extension of the RiteVM component, which is described in [9]. The component plays a role in receiving bytecodes via Bluetooth, and also manages a RiteVM config-

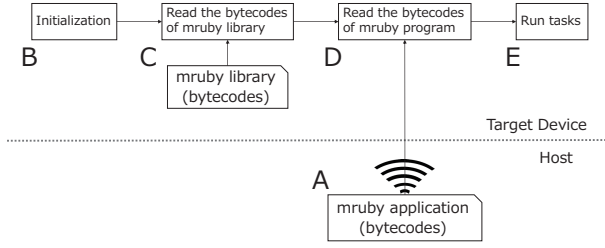


Figure 11: Process Flow of mruby bytecode loader using Bluetooth

```

1 /* tRiteVMBluetooth.cdl */
2 celltype tRiteVMBluetooth{
3     entry sTaskBody eMrubyBody;
4     [optional] call sEventflag cEventflag[];
5     [optional] call sSemaphore cSemaphore;
6     attr{
7         [omit]char_t *mrubyLib;
8         char_t *irepLib =
9             C_EXP("&$cell_global$_irep");
10        uint32_t irepAppSize =
11            C_EXP( BUFFER_SIZE );
12        FLGPTN setptn;
13    };
14    var{
15        mrb_state *mrb;
16        mrbc_context *context;
17        [size_is(irepAppSize)] uint8_t *irepApp;
18    };
19 };

```

Figure 12: Celltype Description for RiteVM with mruby bytecode loader using Bluetooth

uration such as automatically generation the mruby library bytecode. This generated bytecode is prepared beforehand in the storage/ROM device, and different from a bytecode transferred with Bluetooth.

Figure 10 shows a component diagram of MrubyTask1 and RiteVMBluetooth1 cells. The MrubyTask1 cell is a componentized task of the RTOS (TOPPERS/HRP2). TOPPERS/HRP2 is described in [23], [16]. The RiteVMBluetooth1 is the component of RiteVM with mruby bytecode loader using Bluetooth. A bytecode in the host is transferred and received at the top of the component. In this framework, ZMODEM [14] is used as a binary transfer protocol.

Figure 11 shows the process flow of executing mruby program in the component of RiteVM with mruby bytecode loader using Bluetooth such as RiteVMBluetooth1. The concrete main code of tRiteVMBluetooth is shown in Figure 13.

First, the mruby bytecode loader receives the mruby application bytecode transferred from the host (Figure 11:A, Figure 13:6-7). The bytecode is stored in the component variable such as VAR_irepApp shown in Figure 12. This process is exclusively carried out by the semaphore.

Second, pointers of mrb_state and mrbc_context, and mruby-TECS bridges are initialized (Figure 11:B, Figure 13:9-14). mrb_state is a set of states and global variables used in mruby. Synchronization of multiple tasks is performed in this processing phase. The RiteVM that ended

up here waits for the other RiteVM to finish loading and initialization.

Third, the RiteVM reads the bytecode of mruby libraries (Figure 11:C, Figure 13:17-19). mruby libraries are a set of Ruby classes such as motor class and sensor class. For example, motor class defines methods to rotate and stop a motor. The tRiteVMBluetooth cell has attributes as shown in Figure 12. The mrubyLib indicates the program files of mruby libraries. [omit] is only used for the TECS generator, thus the attribute, mrubyLib, does not consume memory. The irepLib is the pointer of the array stored the bytecode of mruby libraries. In short, the bytecode of mruby libraries is stored as an attribute of the component when compiling for the first time.

Fourth, the RiteVM reads the bytecode of the mruby application transferred with Bluetooth (Figure 11:D, Figure 13:20-22). The mruby application bytecode is stored in an array of irepApp. The array is different from that of holding the mruby library bytecode. Two bytecodes are read separately in the RiteVM.

Finally, the mruby task runs (Figure 11:E, Figure 13:20-22). When the mruby application is modified, only the bytecode of the modified application should be transferred. mruby libraries need not be touched because libraries are not normally changed.

The process shown in Figure 13:23-28 is carried out when an exception occurs. When all mruby applications finish, mrb_state and mrbc_context are freed (Figure 13:31-34). The proposed framework supports continuous loading, thus this process loops. After freeing variables, RiteVM will become the state to wait for the next mruby application bytecode.

3.2 RiteVM Scheduler

This section describes implementation of RiteVM scheduler in the proposed framework. mruby on TECS has supported multitasking. However, multitask processing in mruby on TECS requires the knowledge of the RTOS (TOPPERS/HRP2) for developers.

One of approaches for multitasking is co-routine. Co-routine is a cooperative thread, and scheduled by developers with the functions such as resume and yield. (Ruby co-routine is defined in class Fiber [2]) Co-routine is a non-preemptive multitasking, and does not receive the OS's support because developers have to switch tasks manually. Co-routine can not take advantage of multi core processing.

Besides, as another method, delay(), a service call of μ ITRON, can be used for multitasking. This service call delays the execution of the own task for the time of the argument. delay() is needed when scheduling fixed-priority tasks. However, the programming applied to delay() is difficult to use in the case of fair scheduling.

As an approach for multitask processing, the proposed framework provides the RiteVM scheduler which is a fair scheduler and runs multiple tasks equally. The RiteVM scheduler is utilized only when application tasks have the same priority. mruby applications can run in concurrent without developers calling the OS's function. The application programs can also utilize the existing programs since the structures of the programs are not changed.

3.2.1 Design of RiteVM Scheduler

A RiteVM scheduler is a periodic handler, and calls ro-

```

1  /* tRiteVMBluetooth.c */
2  void
3  eMrubyBody_main( CELLIDX idx )
4  {
5      /* Omit: start of exclusive process by semaphore */
6      /* Receive the bytecode via Bluetooth */
7      bluetooth_loader( VAR_irepApp );
8      /* Omit: end of exclusive process by semaphore */
9      /* New interpreter instance */
10     VAR_mrb = mrb_open();
11     /* Omit: error check for mrb_state */
12     /* New mruby context */
13     VAR_context = mrbc_context_new( VAR_mrb );
14     /* Omit: initialization of mruby-TECS bridge */
15     /* Omit: synchronization of
16        starting mruby application */
17     /* Load mruby library bytecode */
18     mrb_load_irep_cxt( VAR_mrb,
19                       ATTR_irepLib, VAR_context );
20     /* Load mruby application bytecode and run */
21     mrb_load_irep_cxt( VAR_mrb,
22                       VAR_irepApp, VAR_context );
23     if ( mrb->exc ) {
24         /* Failure to execute */
25         mrb_p( VAR_mrb,
26               mrb_obj_value( VAR_mrb->exc ) );
27         exit( 0 );
28     }
29     /* Omit: synchronization of
30        finishing mruby application */
31     /* Free mruby context */
32     mrbc_context_free( VAR_mrb, VAR_context );
33     /* Free interpreter instance */
34     mrb_close( VAR_mrb );
35 }

```

Figure 13: Main code for RiteVM with mruby bytecode loader using Bluetooth

tateReadyQueue, a service call of μ ITRON to switch tasks with the same priority. In other words, the RiteVM scheduler calls rotateReadyQueue cyclically. The design of the RiteVM scheduler is shown in Figure 14. rotateReadyQueue is described as follows.

The case is assumed that there are two tasks with the same priority, and both tasks are in an infinite loop. In the current system, when one task is executed first, another task would not be executed. That is because the task with first execution runs in the loop.

When rotateReadyQueue is called, tasks with the same priority are switched as shown in Figure 14. The argument of rotateReadyQueue is the priority.

The rotateReadyQueue can be performed if the number of tasks is more than two. For example, three tasks are in the order: task 1, 2, and 3. In this case, the order is rotated, task 2, 3 and, 1, when the rotateReadyQueue is called.

3.2.2 Component of RiteVM Scheduler

Figure 15 shows the component diagram of RiteVM scheduler. The component of RiteVM scheduler consist of CyclicHandler and RiteVMSchedulerMain. CyclicHandler cell configures the periodic handler based on μ ITRON. Cyclic handlers based on μ ITRON are described in detail [20]. CyclicHandler cell has attributes of the cell. RiteVMSchedulerMain cell is a component to perform the processing body of a periodic handler. rotateReadyQueue is implemented as the

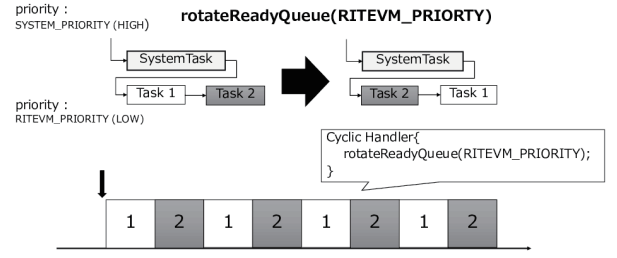


Figure 14: The design of RiteVM scheduler

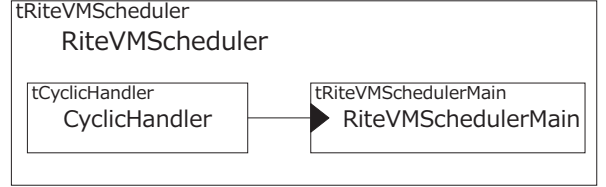


Figure 15: Component Diagram of RiteVM Scheduler

body. Figure 16 shows tRiteVMScheduler celltype, which is the composite cell consisting of two cells. The call port of RiteVMSchedulerMain is connected with the entry port of the Kernel cell (tKernel.eiKernel) to call functions of the kernel. The attribute is used as an arguments of rotateReadyQueue.

Figure 17 shows the build description of RiteVM scheduler. RiteVMScheduler cell has attributes to configure the scheduler such as attribute, cyclicTime, cyclicPhase, and priority. In this case, RiteVM scheduler is executed when it is generated because the attribute is TA_STA that represents the periodic handler is in an operational state after the creation. The scheduler executes every one msec. RITEVM_PRIORITY defines the priority of mruby tasks. In the main of CyclicMain, rotateReadyQueue is implemented and the priority is passed as the argument.

3.3 Synchronization of Multiple RiteVM Tasks

In the proposed framework, RiteVMs read mruby bytecodes, and then execute the applications. Eventflag, one of synchronous processing, is applied to synchronize the starting of multiple mruby applications. Each task sets the flag pattern such as 0x01 (01) and 0x02 (10), and then waits the flag pattern, 0x3 (11), with AND. This process can also apply to the case of the more tasks. For example, in the case of the four RiteVM tasks, each task sets the flag pattern such as 0x01 (0001), 0x02 (0010), 0x04 (0100) and 0x08 (1000), and then waits 0x0f (1111) with AND as shown in Figure 18 (A).

In addition, the end of mruby applications is synchronized to accept the continuous loading. The ending synchronization prevents a RiteVM whose application immediately finishes from waiting the next loading. All of mruby applications finish at the same time, and all RiteVMs wait for receiving the next mruby application bytecodes.

3.4 Utilization of Component-Based Development

This section describes the design applying component-based development.

```

1 /* tRiteVMScheduler.cdl */
2 celltype tCyclicHandler {
3     [inline] entry sCyclic eCyclic;
4     call siHandlerBody ciBody;
5     attr {
6         [omit] ATR attribute = C_EXP("TA_NULL");
7         [omit] RELTIM cyclicTime;
8         [omit] RELTIM cyclicPhase;
9     };
10 };
11 celltype tRiteVMSchedulerMain {
12     require tKernel.eiKernel;
13     entry siHandlerBody eiBody;
14     attr {
15         PRI priority;
16     };
17 };
18
19 composite tRiteVMScheduler {
20     attr {
21         ATR attribute = C_EXP("TA_NULL");
22         RELTIM cyclicTime = 1;
23         RELTIM cyclicPhase = 1;
24         PRI priority;
25     };
26     cell tRiteVMSchedulerMain RiteVMSchedulerMain {
27         priority = composite.priority;
28     };
29     cell tCyclicHandler CyclicHandler {
30         ciBody = RiteVMSchedulerMain.eiBody;
31         attribute = composite.attribute;
32         cyclicTime = composite.cyclicTime;
33         cyclicPhase = composite.cyclicPhase;
34     };
35 };

```

Figure 16: Celltype Description of RiteVM Scheduler

```

1 cell tRiteVMScheduler RiteVMScheduler {
2     attribute = C_EXP("TA_STA");
3     cyclicTime = 1;
4     cyclicPhase = 1;
5     priority =
6         C_EXP("RITEVM_PRIORITY");
7 };

```

Figure 17: Build Description of RiteV Scheduler

In the framework, RiteVMs, the RiteVM scheduler, and Eventflag are implemented as components. Therefore, developers can easily add or remove these components and also reuse them. For example, if RiteVM scheduler is not necessary for software, developers should only comment out the .cdl file such as `//import(<tRiteVMScheduler.cdl>);`. This advantage of CBD saves developers the labor of rewriting a kernel configuration file.

In addition, the code size can decrease by developing with component-based. In the proposed framework, the advantage is applied in the Eventflag component. The set pattern and wait pattern are defined as attributes of the component as shown in Figure 18 (B). This design such as `cEventflag_set(ATTR_setPattern)` enables the program without "if" statements and reuses the identical .c file. Developers do not need to modify the .c file because the .cdl files are prepared in accordance with the number of RiteVMs. In addition, the components of Eventflag are built with [optional]

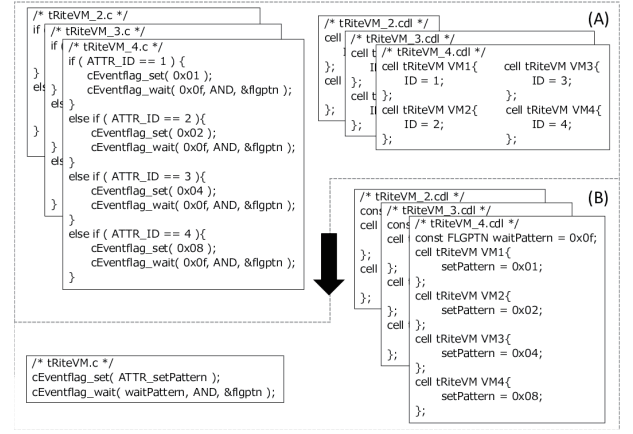


Figure 18: The design for Eventflag using TECS *only differences

in TECS. [optional] means that the codes are run only when the call port is connected. The .c file does not be rewritten even if developers do not use Eventflag.

4. EXPERIMENTAL EVALUATION

This section mentions experimental results and their consideration. To analyze the advantages of the proposed framework, the evaluations are performed as follows.

- Size and time for transferred mruby bytecodes
- Execution time with singletasking, co-routine, and multitasking
- Overhead for periodic time
- Code size utilizing component-based development

These evaluations are performed in order to indicate that an mruby bytecode loader improves the software development efficiency, and that the proposed multitask processing effectively executes compared with singletasking or co-routine, and also the overhead of the cyclic period. This paper demonstrates the proposed system on a LEGO MIND-STORMS EV3 [18] (300MHz ARM9-based Sitara AM1808 system-on-a-chip) compiled with gcc 4.9.3 -O2 and mruby version 1.2.0.

4.1 Evaluation: Improving the software development efficiency by mruby bytecode loader

The size, load process time, and compilation time for mruby application and mruby application including library is shown in Table 1. The overhead of load processing is 50.933 msec, which it takes to load a zero bytes bytecode. Similarly, the overhead of compilation is 46.9 msec, which it takes to compile a zero bytes program. The mruby application bytecode is smaller and faster than that of including mruby libraries in all terms. The difference becomes larger as the number of RiteVMs increases because it takes the 50 msec overhead per one RiteVM. In addition, the design can save the time to rewrite a storage/ROM device, and to restart an OS. These advantages lead improvement of the software development efficiency.

Table 1: Comparison of the size and load process time between an mruby application including mruby libraries and not

	App&Lib	App	App&Lib/App
Bytecode Size	14,044 bytes	199 bytes	$\times 70.6$
Loading Time	305.081 msec	7.774 msec	$\times 39.2$
Compilation Time	8.7 msec	0.3 msec	$\times 29.0$

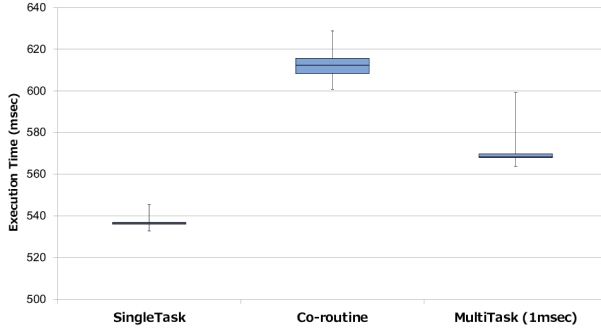


Figure 19: Comparison of the application execution time with singletask, co-routine, and multitask

4.2 Evaluation: RiteVM Scheduler

The comparison of the application execution time with singletasking, co-routine, and multitasking is shown in Figure 19. The 100,000 times loop program is used as mruby application for evaluation of execution time. In detail, the singletask program loops 100,000 times, and the multitask and co-routine programs loop 50,000 times in each task. In Figure 19, the periodic time of the periodic handler for multitasking is one msec. This result shows the proposed design is superior to co-routine in terms of execution time. Moreover, developers can utilize the scheduler practically because the overhead of the RiteVM scheduler is about 5 %. Switching tasks' overhead is also evaluated, that is about three μ sec on average. The scheduler interrupts and switches tasks, which causes this overhead.

Figure 20 shows the execution time of multitasking with the periodic handler. A lower limit of the periodic time is one msec due to the specification of TOPPERS/HRP2, the used RTOS. More than eight msec do not be evaluated in this paper because it is thought the larger periodic time influences applications. The execution time decreases as the periodic time become larger, because the number of switching tasks decreases. The execution time of one msec is only about 1 % larger than that of eight msec. The RiteVM scheduler with small periodic time can effectively execute multiple tasks because the periodic time overhead is not large.

4.3 Evaluation: Synchronization of multiple RiteVM tasks

4.4 Evaluation: Benefits of component-based development

To indicate the superior of component-based development, the comparison of code lines between two .c files is shown in Table 2. (A) and (B) mean the source files in the upper and lower of Figure 18, respectively. In terms of .c, (B)'s lines do not increase even if the number of RiteVMs increases, while (A)'s lines increase as the number increases. (B)'s .c file

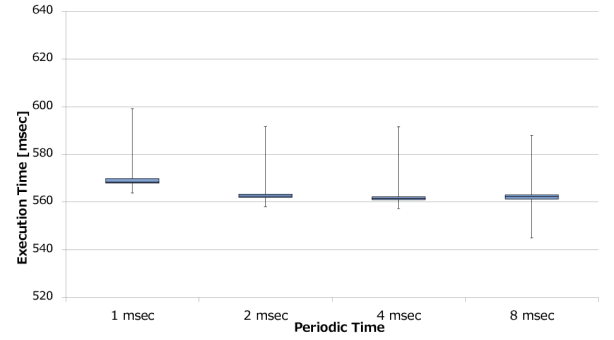


Figure 20: Comparison of the overhead for each cyclic period of calling rotateReadyQueue

Table 2: Lines of .c and .cdl file for the number of RiteVM

	(A)	(B)	Diff
.c (Total)	$8 \times \alpha + 134$	130	$8 \times \alpha + 4$
.c (Modification)	$10 \times \alpha - 2$	0	$10 \times \alpha - 2$
.cdl	$18 \times \alpha + 25$	$18 \times \alpha + 25$	0

α : the number of RiteVM

can be utilized without modification, regardless of the number of RiteVMs. Moreover, lines of two .cdl files are equal. The skillful component-based development brings this advantages such as the decrease of code lines and non-modified code, which leads high productivity and high maintainability.

5. RELATED WORK

The open-source run-time systems for scripting languages have been proposed such as follow: python-on-a-chip [6], the Owl system [12], eLua [3], mruby [21], [5], and mruby on TECS [9].

python-on-a-chip (p14p) is a Python run-time system that uses a reduced Python VM called PyMite. The VM runs a significant subset of Python language with few resources on a micro-controller. p14p can run multiple stackless green threads.

The Owl system is an embedded Python run-time system. The Owl is a complete system for ARM Cortex-M3 micro-controllers. The Owl toolchain produces relocatable memory images, that are directly runnable on the micro-controller, from Python code objects. The interpreter of the Owl system is the same as that of python-on-a-chip.

eLua offers the full implementation of Lua programming language to the embedded systems. Lua is one of the most popular script languages for embedded systems [4], [15]. Lua supports co-routine, referred to collaborative multitasking. A co-routine in Lua is used as an independently executed thread. A co-routine can just suspend and resume multiple routines. Thus, a Lua co-routine is not like multitasks in multitask systems.

mruby, the lightweight implementation of the Ruby language, has been proposed for embedded systems. mruby programs can run on a RiteVM, which is the VM for mruby and reads the mruby bytecode. mruby has supported co-routine, but not supported multitasking for RTOSs.

mruby on TECS is a component-based framework for running mruby programs. The programs on mruby on TECS can execute about 100 times faster than the mruby pro-

Table 3: Comparison of the proposed and previous work

	Bluetooth Loader	Call C Function	Legacy Code of Embedded System	VM Management	VM Scheduler	Synchronization of Application	Co-routine
python-on-a-chip [6]							✓
Owl system [12]		✓	Partially				✓
eLua [3]		✓	Partially				✓
mruby [21]		✓					✓
mruby on TECS [9]		✓	✓	✓			✓
Proposed framework	✓	✓	✓	✓	✓	✓	✓

grams. Software can be also developed with component base by mruby on TECS. Although multitasking has been supported in the current mruby on TECS, developers need to be familiar with functions of an RTOS to use multitasking. The co-routine is supported as same as mruby.

Table 3 shows a comparison between the proposed framework and previous work. The proposed framework supports the loader, the VM scheduler, and synchronization of application.

In addition, there are component systems for embedded systems. AUTOSAR (AUTomotive Open System ARchitecture) [1] is a worldwide development partnership of vehicle manufactures.

SaveCCM [17] is a component model for embedded control application. SaveCCM is a simple model which is flexible to facilitate analysis of real-time and dependability.

Koala [24] is a component model that helps to manage the growing complexity and diversity of software in consumer electronics products.

6. CONCLUSION

This paper has presented an extended framework of mruby on TECS: mruby bytecode loader using Bluetooth and RiteVM scheduler. The loader provides developers with the software development efficiency without rewriting a storage/ROM device and restarting an OS. The proposed framework can be applied to many kinds of embedded systems because the loader can use not only Bluetooth also wired serial connection. The RiteVM scheduler makes multitasking more easily than the current mruby on TECS. In the evaluation, experimental results of the loader and the RiteVM scheduler show their advantages. The loader can improve the software development efficiency on mruby on TECS. The RiteVM scheduler has the effectiveness in terms of execution time and ease of use compared with singletasking and co-routine. In addition, synchronization of multiple RiteVM tasks is implemented.

The proposed framework is developed in component-base by TECS. The facilities such as RiteVMs, the RiteVM scheduler, and Eventflag are implemented as components. Therefore, developers can easily add or remove the functionalities as necessary, and also reuse them. Developers can choose fair scheduling or fixed-priority scheduling since the RiteVM scheduler can be easily removed. Component-based development can increase productivity.

In the future, the .cdl files for RiteVM and mruby-TECS bridge are generated automatically using a plugin, and developers can send a bytecode with ZMODEM protocol on the command line.

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