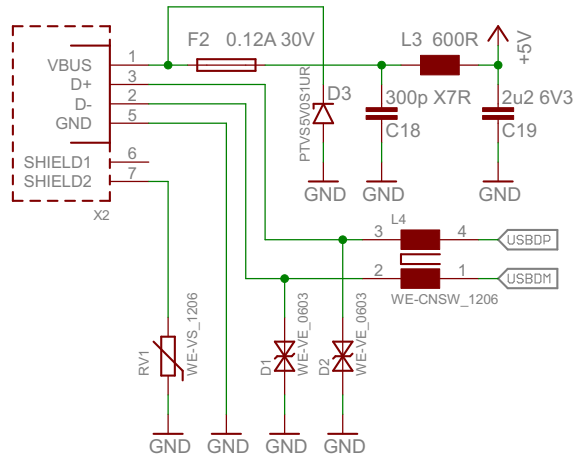
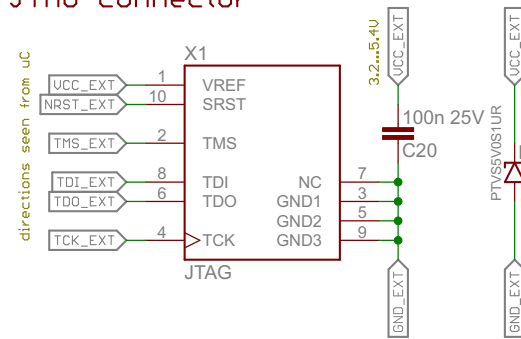


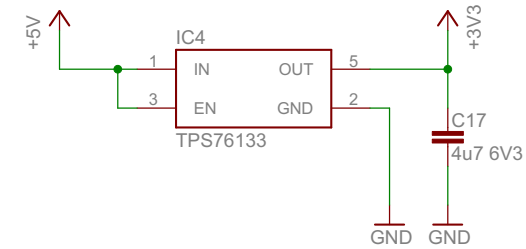
## USB with EMC-protection



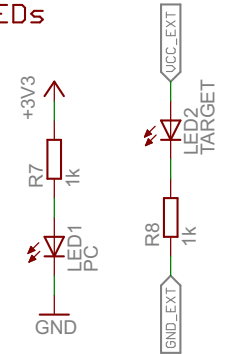
## JTAG-connector



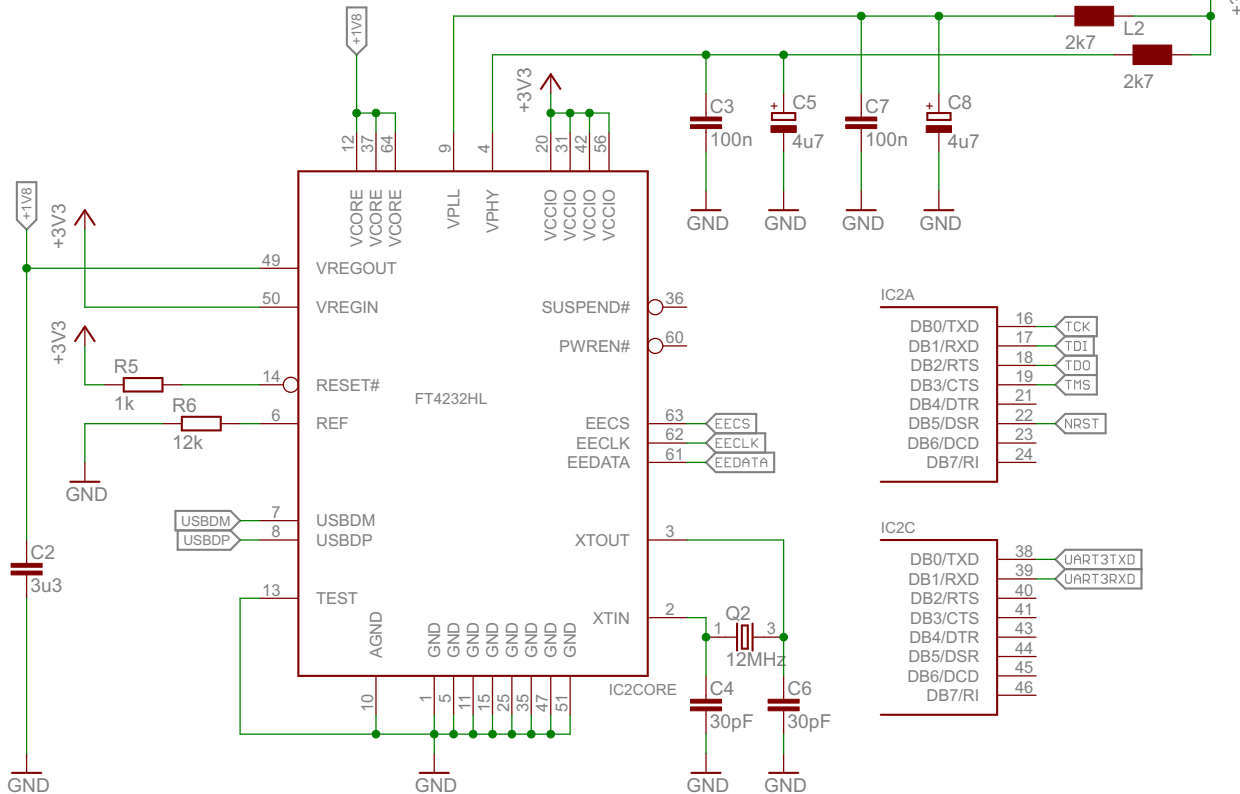
## +3V3 LDO



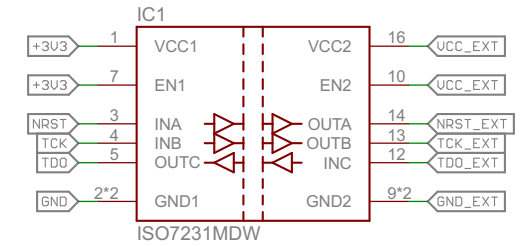
## LEDs



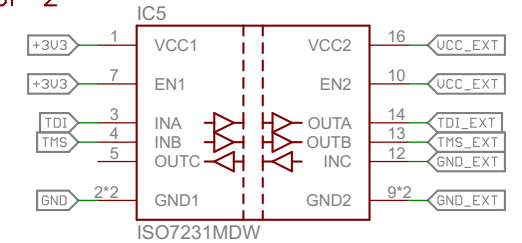
## FTDI-Chip



## Isolator 1



## Isolator 2



# ERPROG

JTAG-Pogammer with target isolation

TITLE: erprog\_1504-1

Drawing: Markus Hoffmann

Document Number:-

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