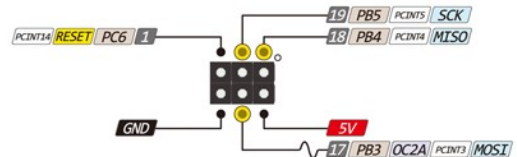
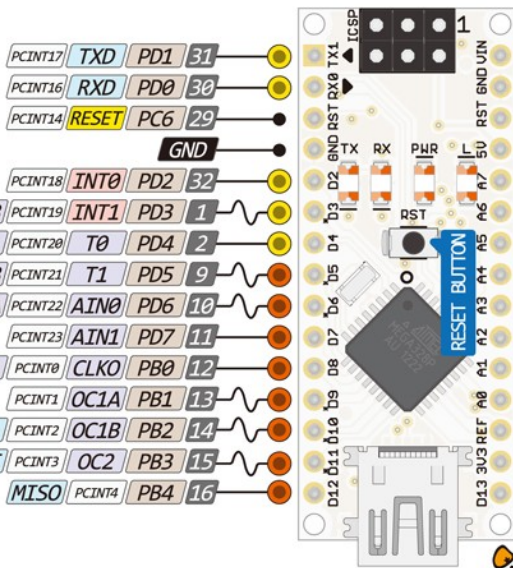


NANO PINOUT



1
0

2
3
4
5
6
7
8
9
10
11
12



VIN The input voltage to the board when it is running from external power. Not USB bus power.

GND

29 PC6 RESET PCINT14

5V

22 ADC7

19 ADC6

28 PC5 PCINT13 ADC5 SCL

27 PC4 PCINT12 ADC4 SDA

26 PC3 PCINT11 ADC3

25 PC2 PCINT10 ADC2

24 PC1 PCINT9 ADC1

23 PC0 PCINT8 ADC0

21 AREF

3V3

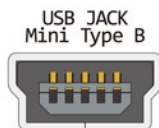
17 PB5 PCINT5 SCK

A7
A6
A5
A4
A3
A2
A1
A0

- Power
- GND
- Serial Pin
- Analog Pin
- Control
- INT
- Physical Pin
- Port Pin
- Pin function
- Interrupt Pin
- PWM Pin
- Port Power

Absolute MAX per pin 40mA recommended 20mA

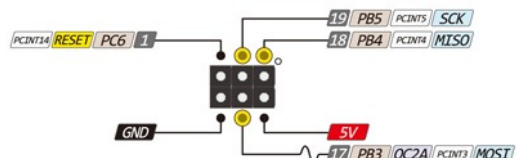
Absolute MAX 200mA for entire package



Analog exclusively Pins

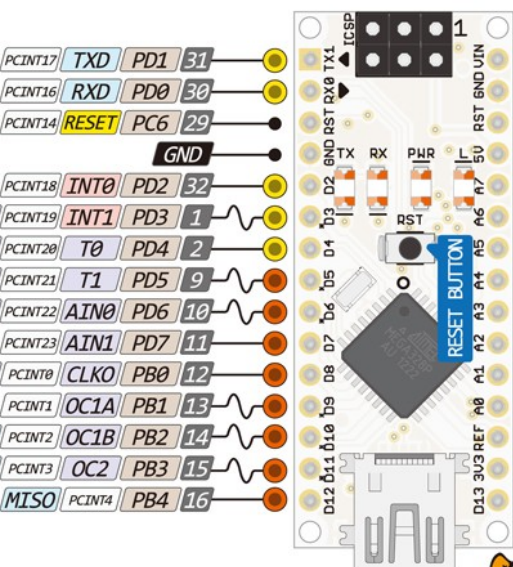
The power sum for each pin's group should not exceed 100mA

NANO PINOUT



1
0

2
3
4
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7
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11
12



VIN The input voltage to the board when it is running from external power. Not USB bus power.

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21 AREF

3V3

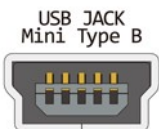
17 PB5 PCINT5 SCK

A7
A6
A5
A4
A3
A2
A1
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- Power
- GND
- Serial Pin
- Analog Pin
- Control
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- Physical Pin
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Absolute MAX per pin 40mA recommended 20mA

Absolute MAX 200mA for entire package



Analog exclusively Pins

The power sum for each pin's group should not exceed 100mA