

A

B

C

D

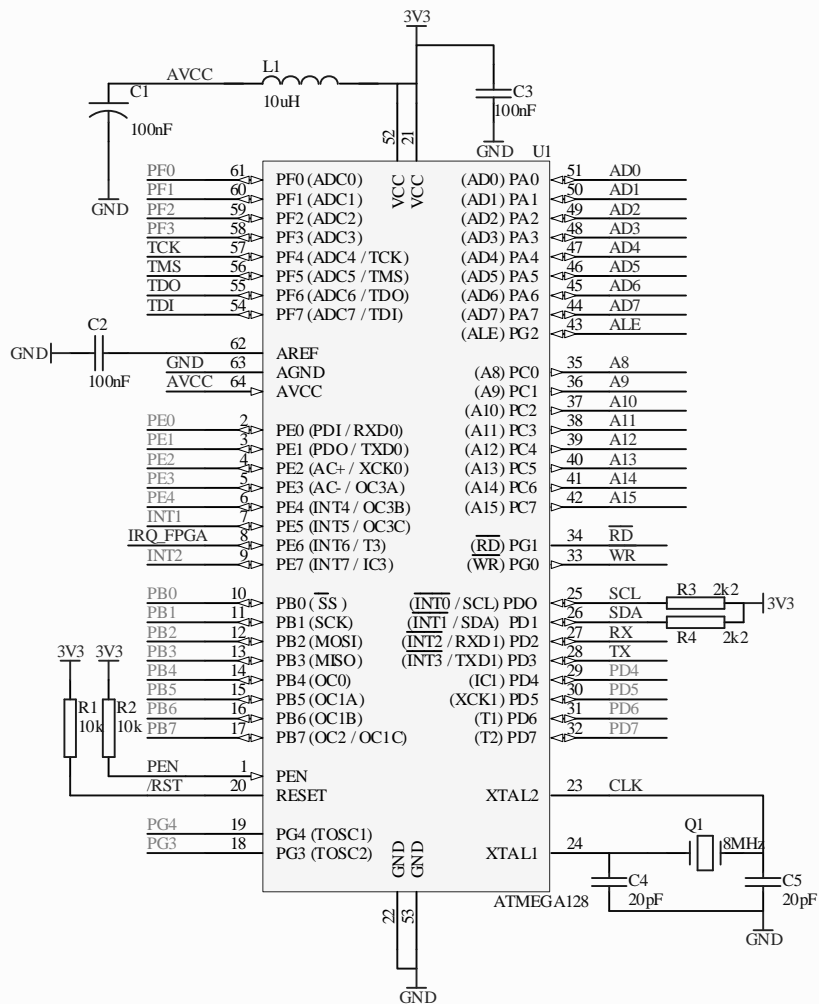
A

B

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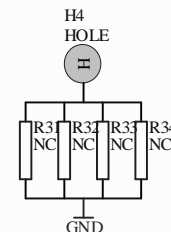
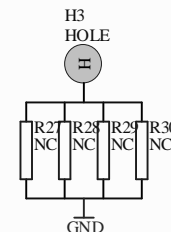
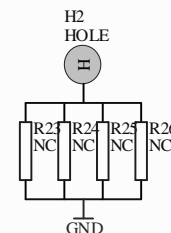
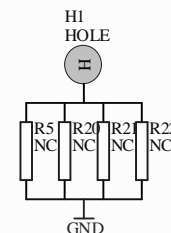
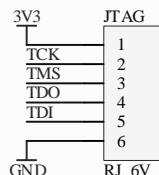
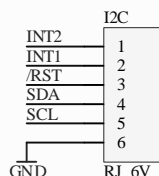
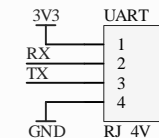
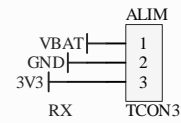
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FPGA_UNIOC-NG
FPGA_UNIOC-NG.Sch



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120	7	125	8
126	9	129	10
132	11	133	12
134	13	135	14
136	15	137	16
139	17	142	18
143	19	144	20
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7	23	8	24
9	25	6	26

UNIOC_NG_FPGA1



UNIOC_uC			
PD7	1	PD7	PD6
PD5	3	PD5	PD4
TX	5	TX	RX
SDA	7	SDA	SCL
/RST	9	RST	PG3
PB7	11	PB7	PB6
PB5	13	PB5	PB4
PB3	15	PB3	PB2
PB1	17	PB1	PB0
INT2	19	INT2	IRQ_FPGA
INT1	21	PE7	PE6
PE3	23	PE5	PE4
PE1	25	PE3	PE2
PEN	27	PE1	PE0
PF1	29	PEN	PF0
PF3	31	PF1	PF2
3V3	33	PF3	GND
		3V3VBAT	VBAT

UNIOC_NG_uC

UNIOC_F2			
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104	7	104	NC
101	9	101	103
99	11	99	100
96	13	96	102
93	15	93	104
91	17	91	106
89	19	89	108
87	21	87	110
79	23	79	112
74	25	74	114
		NC	116

UNIOC_NG_FPGA2

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