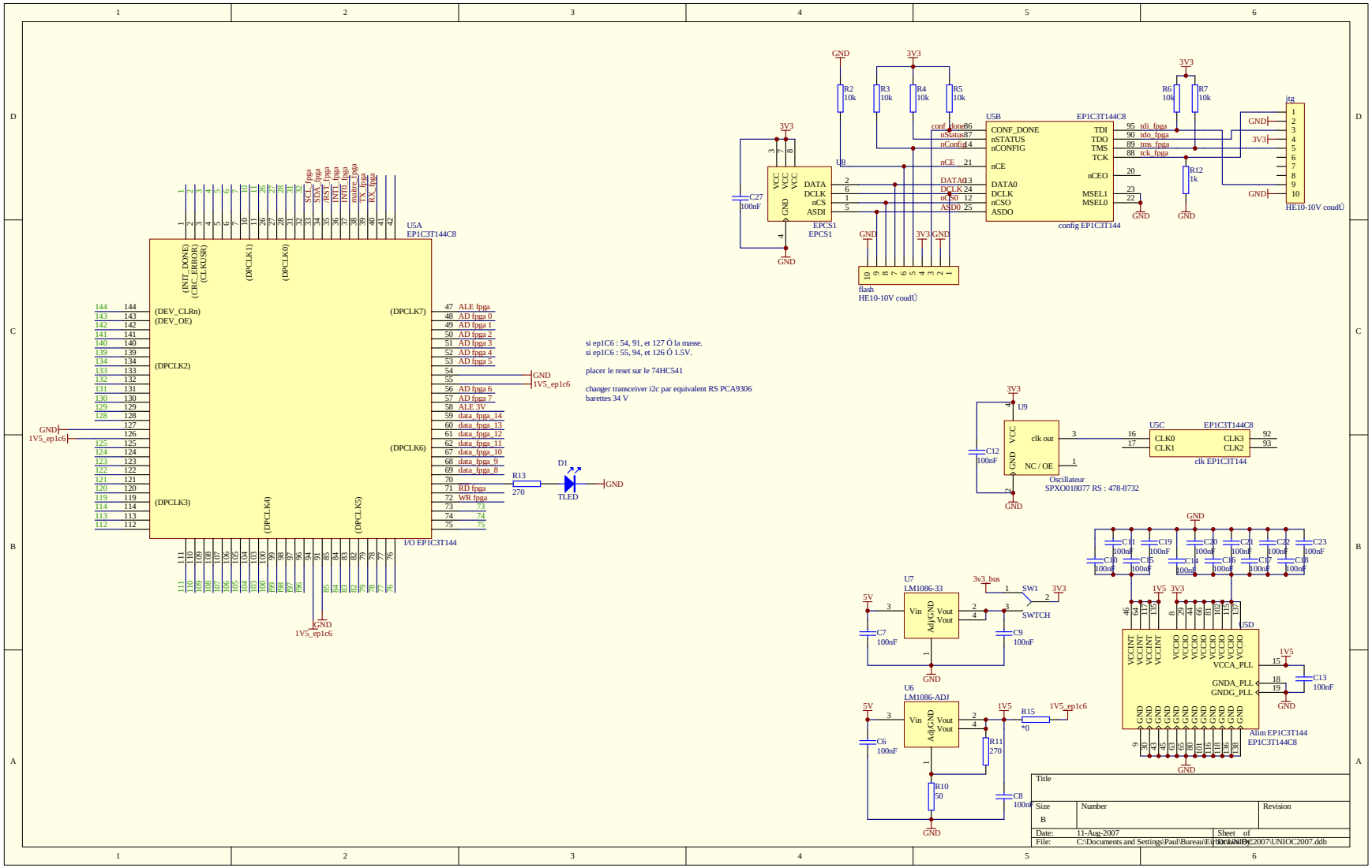
U5A
EP1C3T144C8
I/O EP1C3T144
(INIT_DONE)
(CRC_ERROR)
(CLKUSR)
(DPCLK1)
(DPCLK0)
(DPCLK7)
(DPCLK6)
(DPCLK5)
(DPCLK4)
(DPCLK3)
(DPCLK2)
(DEV_CLRn)
(DEV_OE)
SCL_fpga
SDA_fpga
INT2_fpga
INT1_fpga
INT0_fpga
maitre_fpga
TX_fpga
RX_fpga
ALE fpga
AD fpga 0
AD fpga 1
AD fpga 2
AD fpga 3
AD fpga 4
AD fpga 5
AD fpga 6
AD fpga 7
ALE 3V
data_fpga_14
data_fpga_13
data_fpga_12
data_fpga_11
data_fpga_10
data_fpga_9
data_fpga_8
RD fpga
WR fpga
GND
1V5_ep1c6
R13
270
D1
TLED
si ep1C6 : 54, 91, et 127 Ó la masse.
si ep1C6 : 55, 94, et 126 Ó 1.5V.
placer le reset sur le 74HC541
changer transceiver i2c par equivalent RS PCA9306
barettes 34 V
U8
EPCS1
EPCS1
C27
100nF
R2
10k
R3
10k
R4
10k
R5
10k
U5B
EP1C3T144C8
CONF_DONE
nSTATUS
nCONFIG
nCE
DATA0
DCLK
nCSO
ASDO
TDI
TDO
TMS
TCK
nCEO
MSEL1
MSEL0
config EP1C3T144
tdi_fpga
tdo_fpga
tms_fpga
tck_fpga
R6
10k
R7
10k
R12
1k
jtg
HE10-10V coudÚ
flash
HE10-10V coudÚ
3V3
GND
U9
Oscillateur
SPXO018077 RS : 478-8732
C12
100nF
U5C
EP1C3T144C8
CLK0
CLK1
CLK3
CLK2
clk EP1C3T144
U7
LM1086-33
U6
LM1086-ADJ
Vin
Vout
Adj/GND
5V
C7
100nF
C9
100nF
C6
100nF
3v3_bus
SW1
SWTCH
R11
270
R10
50
C8
100nF
R15
*0
1V5
U5D
VCCA_PLL
GNDA_PLL
GNDG_PLL
C13
100nF
Alim EP1C3T144
EP1C3T144C8
Title
Size
B
Number
Revision
Date: 11-Aug-2007
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