CPE301 – SPRING 2019

Design Assignment 1B

Student Name: Rocky Yasuaki Gonzalez

Student #: 5003229733

Student Email: gonzar14@unlv.nevada.edu

Primary Github address: https://github.com/rockyg1995/ihswppdar.git

Directory: C:\Users\rocky\Documents\CpE 301+L - Embedded Systems Design\CpE

301\Repository\DesignAssignments\DA1B

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.

- 2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
- 3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
- 4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

N/A, (Atmel Studio 7 Project Only)

2. INITIAL/DEVELOPED CODE OF TASK 1/A

```
; DA1B - Rocky Gonzalez.asm
; Created: 2/17/2019 8:17:42 PM
; Author: rocky
.include <m328pdef.inc>
                               ; Include library for .SET and .ORG directives
.SET STARTADDS = 0x0200 ; Create 'STARTADDS' to begin at address '0x0200'
.SET DIV = 3
                               ; Create 'DIV' to be a value of '3'
                                       ; Store the value '99' into Counter (register R19)
        LDI R23, 99
       LDI XL, LOW(STARTADDS)

LDI XH, HIGH(STARTADDS)

LDI YL, LOW(0x0400)

LDI YH, HIGH(0x0400)

LDI YH, HIGH(0x0400)

LDI ZL, LOW(0x0600)

LDI ZL, LOW(0x0600)

Store address in Lower Y-pointer

LDI ZH, HIGH(0x0600)

Store address in Lower Z-pointer

LDI ZH, HIGH(0x0600)

Store address in Lower Z-pointer

LDI ZH, HIGH(0x0600)

Store address in Upper Z-pointer

LDI R20, 0x07

Arbitrary Value, consecutive addition executed

MOV R21 R20
        LDI R20, 0x07
                                       ; Retain Arbitrary Value (Since 'R16' will Change)
        MOV R21, R20
                ; Loop to Store '7, 14, ..., n' Values Starting at Address 0x0200 (Using X-Pointer)
Cont1: CPI R20, 11 ; Check if 'R20 > 10'
BRLO L1 ; If 'R20 <= 10', Add '7' More to 'R20'
RJMP LoadY ; Load Value into Y or Z Address depending on Divisibility
Cont2: ST X+, R20 ; Store 'R20' into 'X', then increment address location
        ADD R20, R21 ; Increment 'R20' with itself (consecutive addition)
                   ; Else, Continue to Decrement Counter Value (register R23)
        DEC R23
        BRNE Cont1 ; Repeat 'Cont1' until all values are in address starting at 0x0200
        RJMP end ; Finish Program after storing all Values
        ADD R20, R21 ; Add '7' more so that 'R20 > 10'
        RJMP Cont1 ; Recheck
; Store Divisible Values by 3 into 'Y', and Non-Divisible Values into 'Z'
LoadY: MOV R22, R20 ; Store 'R20' into temporary register 'R22'
Repeat:
        SUBI R22, DIV ; Subtract the value '3' from 'R22'
                               ; Check if 'R22 > 3', Set 'Carry' for SREG when 'R22 <= 3'
        CPI R22, DIV+1
                               ; If 'R22 > 3', Repeat subtracting '3'
        BRSH Repeat
                               ; Otherwise, Subtract '3' Once More to Check Divisible by '3'
        SUBI R22, DIV
                                ; If Not Divisible, Load Value into Z Address
        BRNE LoadZ
                               ; Else, Load Divisible Value into Y Address
        ST Y+, R20
        Add R16, R20
                        ; If Overflow in R16, increment value into R17; Continue back to Opicinal C
                               ; Add value into R17:R16
        BRCS ext1
        RJMP Cont2
```

```
ST Z+, R20 ; Load Non-Divisible Value into Z-Address
Add R18, R20 ; Add value into R19:R18
BRCS ext2 ; If Overflow in R18, increment value into R19
RJMP Cont2 ; Continue back to Original Operation
LoadZ: ST Z+, R20
; ------
; Incrementing R17:R16 (High Register)
ext1: CLC ; Clear Carry in Status Register
INC R17 ; Increment R!7:R16 (High Register)
RJMP Cont2 ; Continue Original Loop
;
; Incrementing R19:R18 (High Register)
ext2: CLC ; Clear Carry in Status Register
INC R19 ; Increment R!9:R18 (High Register)
RJMP Cont2 ; Continue Original Loop
; End of Program
end:
      RJMP end
                            ; End Program/Loop Forever
3.
       DEVELOPED (VERIFICATION) CODE OF TASK 2/A from TASK 1/A
/// @author Rocky Gonzalez
/// @note da1b arrays
/// @file da1b_arrays.cpp
/// @version 2019-02-23
/// @brief The program is used to verify values for DA1B.
#include <iostream>
using namespace std;
int main() {
    unsigned int array_x[100];  // Create an array to store X-Pointer Values
    unsigned int array_y[100];  // Create an array to store Y-pointer Values
unsigned int array_z[100];  // Create an array to store Z-Pointer Values
    unsigned char store = 0;  // Values that range from "0-255" (Byte)
unsigned char i;  // Iteration for Array X
int total_y = 0;  // Total Value of Y Array
int total_x = 0;  // Total Value of T Array
    int total z = 0;
                                     // Total Value of Z Array
    cout << "Array X" << endl; // Display "Array X" into Monitor</pre>
    if (store <= 10) {
            // Store Values into Array X
        array_x[i] = store;
        }
    cout << endl << endl;</pre>
                                    // Line Break
    unsigned char j = 0;
                                    // Iteration for Array Y
```

```
unsigned char k = 0;
                   // Iteration for Array Z
cout << "Array Y" << endl;  // Display "Array Y" into Monitor</pre>
// Store into Y Data
       array_y[j] = array_x[i];
       total_y = total_y + array_x[i];  // Add Value (total)
                                    // Increment Y Array
      i = i++;
      cout << array_y[j] << " ";
                                    // Create List of Y Values
   }
}
cout << endl << endl;</pre>
cout << "Array Z" << endl; // Display "Array Z" into Monitor</pre>
for (i = 0; i < 99; i++) { // Loop X Data if(array_x[i]%3 != 0) { // If not divisible by 3, Store in Array Z and Display
       array_z[k] = array_x[i];
                               // Store into Z Data
       total_z = total_z + array_x[i];  // Add Value (total)
                                     // Increment Z Array
       k = k++;
       cout << array_z[k] << " ";
                                   // Create List of Z Values
}
                                           // Line Break
cout << endl << endl;</pre>
cout << "Total Y: " << total_y << endl << endl; // Totality of Y Values</pre>
```

4. SCHEMATICS

}

N/A (Assembly Coding Only)

5. SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)

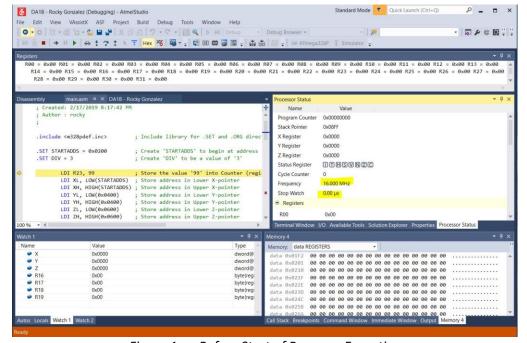


Figure 1a – Before Start of Program Execution

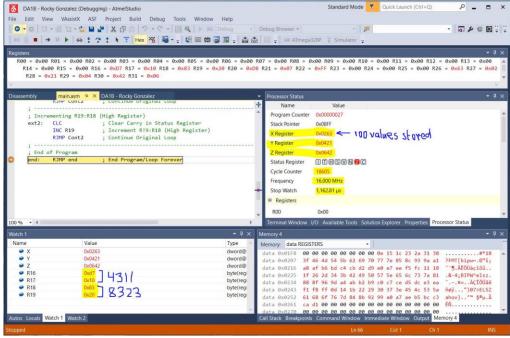


Figure 1b – Total Values from R17:R16 (4,311) and R19:R18 (8,323)/Total Time of Execution (1,162.61μs)

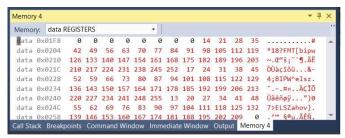


Figure 1c – All Values Stored from X-Pointer (Starting at 0x0200)

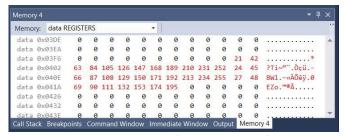


Figure 1d – Divisible Values Stored from Y-Pointer (Starting at 0x0400)

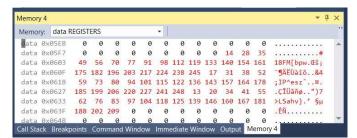


Figure 1e – Non-divisible Values Stored from Z-Pointer (Starting at 0x0600)

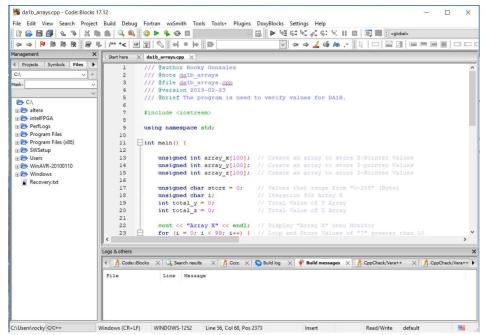


Figure 1f – Verification using C++ through CodeBlocks

```
"C:\Users\rocky\Documents\CpE 301+L - Embedded System...
4 21 28 35 42 49 56 63 70 77 84 91 98 105 112 119 126 133 140 147
154 161 168 175 182 189 196 203 210 217 224 231 238 245 252 17 24
31 38 45 52 59 66 73 80 87 94 101 108 115 122 129 136 143 150 157
164 171 178 185 192 199 206 213 220 227 234 241 248 255 13 20 27
4 41 48 55 62 69 76 83 90 97 104 111 118 125 132 139 146 153 160
167 174 181 188 195 202 209
1 42 63 84 105 126 147 168 189 210 231 252 24 45 66 87 108 129 15
171 192 213 234 255 27 48 69 90 111 132 153 174 195
14 28 35 49 56 70 77 91 98 112 119 133 140 154 161 175 182 196 203
217 224 238 245 17 31 38 52 59 73 80 94 101 115 122 136 143 157 1
4 178 185 199 206 220 227 241 248 13 20 34 41 55 62 76 83 97 104
118 125 139 146 160 167 181 188 202 209
Total Y: 4311
otal Z: 8323
Process returned 0 (0x0) execution time : 0.348 s
ress any key to continue.
```

Figure 1g – Output Monitor from C++ Coding

6. SCREENSHOT OF EACH DEMO (BOARD SETUP)

N/A (Assembly Coding Only)

7. VIDEO LINKS OF EACH DEMO

https://youtu.be/IM5o84oR52E

8. GITHUB LINK OF THIS DA

 $\label{lem:cpe} C:\Users\rocky\Documents\CpE\ 301+L-Embedded\ Systems\ Design\CpE\ 301\Repository\Design\Assignments\DA1B$

Student Academic Misconduct Policy

http://studentconduct.unlv.edu/misconduct/policy.html

"This assignment submission is my own, original work".

Rocky Gonzalez