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| **Rod Woodman** | **777 Hollenbeck Ave #13N, Sunnyvale, CA 94087**  **roderickwoodman@gmail.com | 1-408-881-2234**  **rodwoodman.com** |

**BACKGROUND:** I am looking to transition into a Web or mobile front-end developer or UI/UX role to match my

most recent studies and my true passion for solving problems on the human-computer interface. I

have recently discovered that while my current embedded software role does exercise my technical

mind and my thoroughness in coding, it does not touch on my love of information (IA) and

writing and communication in general. I am most proud of my techical work when I can also

incorporate elements of Tufte’s visualizations and Krug’s usability messages. I am a U.S. Citizen.

**PROJECTS: Web Design Certificate (UCSC Extension)**: completed 2013; front-end developer focus

including HTML, CSS, PHP, Dreamweaver, User Experience Design, Visual Design, Photoshop;

techniques including User-Centered Design (UCD), wireframes, card sorting

**other coursework:** JavaScript, jQuery, HTML5 Fast Track, HTML5 Mobile Mastery, iPhone

**website redesign**: client redesign using HTML5 & CSS3; http://dinsmorelandscape.com

**personal website and portfolio**: personal site using Bootstrap3; http://www.rodwoodman.com

**EMPLOYMENT HISTORY:**

2005 to current **CISCO SYSTEMS INC. (Catalyst 6000 Diagnostics Group) San Jose, CA**

**Diagnostic Firmware Engineer (C, Linux, TCL, Eclipse, wikis)**

I reported to the Diagnostics Manager with diagnostics design, bringup, and support responsibilities

for a flagship IP-forwarding card within a modular blade chassis. Our customers were first

the hardware bringup team and later the manufacturing test team. Our deliverables were

Command Line Interface (CLI) offline diagnostic tests and utilities running on a flash image.

* ***Layer3 forwarding card bringup project:*** Implemented and debugged chip-level, board-level, and test-level drivers for the company’s highest-profile forwarding ASIC chipset. Documented the complex internal packet flow with a single reference diagram loved by all!
* ***IP traffic stress test design project:*** Designed and coded a system-level test suite to maximize hardware stresses of IP traffic. Improved existing APIs. Added user trace configurability and system visualization CLIs.
* ***40G system bringup project:*** Designed and implemented a new fabric-steering scheme for IP traffic within the switch hardware. Supported the manufacturing team’s CLI modeling and environmental chamber failures. Trained the China development team on the forwarding engine architecture and testing and received outstanding reviews.

2001 to 2005 **COMPAQ/HP CORPORATION (Tandem/NonStop Division) Cupertino, CA**

**Diagnostic Firmware Engineer (C, Linux, Perl, cvs)**

**System Hardware Engineer**

1996 to 2001 **APPLIED SIGNAL TECHNOLOGY, INC. Sunnyvale, CA**

**Lead System Hardware Engineer (1999 to 2001)**

**System Test and Integration Engineer (1998 to 2001)**

**Board Design Engineer (1996 to 1999)**

**FORMAL** *Certificate, Web Design, UCSC Extension, Silicon Valley (2013)*

**EDUCATION:** *M.S., Computer Engineering, Santa Clara University (2002)*

*B.S., Electronic Engineering, Cal Poly San Luis Obispo (1996)*

**INTERESTS:** photography, games, trail running, CrossFit, real food