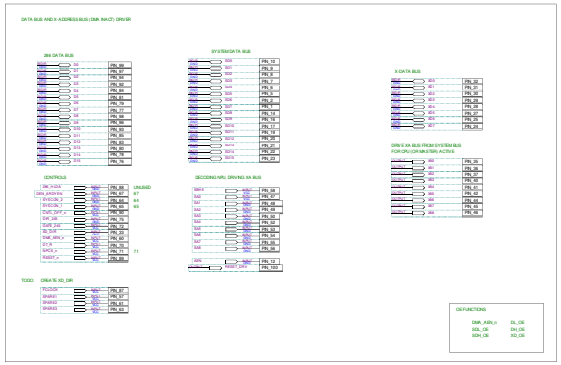
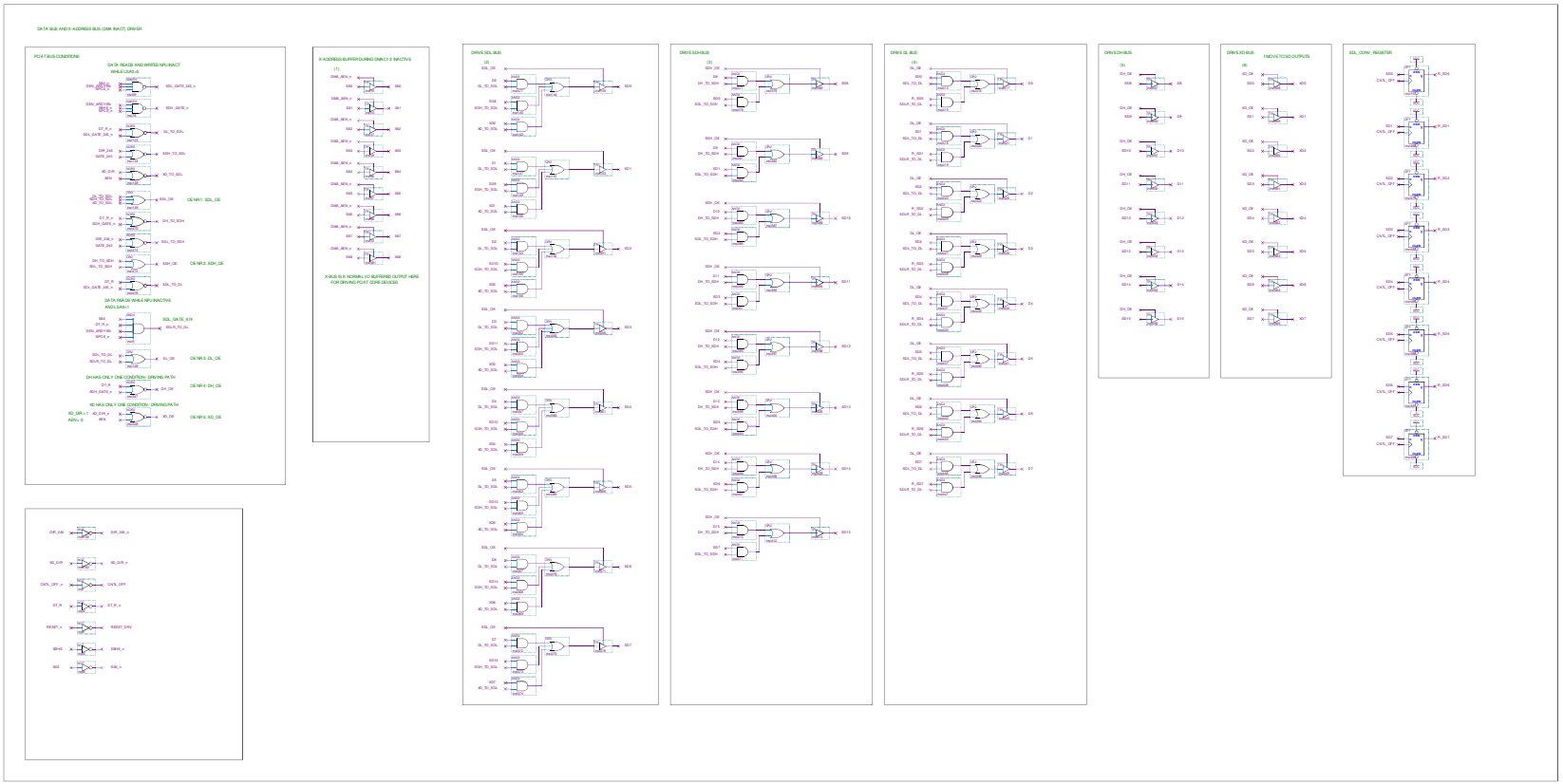

DATA BUS AND X-ADDRESS BUS (DMA INACT) DRIVER
Z80 DATA BUS
SYSTEM DATA BUS
X DATA BUS
DRIVE XA BUS FROM SYSTEM BUS FOR CPU (DRAM BSTER) ACTIVE
CONTROLS
DECODING NPU, DRIVING XA BUS
UNUSED
TODO: CREATE XD_DIR
CE FUNCTIONS
DMA_ARN_n DL_CE
SDL_CE DH_CE
SDH_CE XD_CE


DATA BUS AND X-ADDRESS BUS (DMA INACT) DRIVER
PCAT BUS CONDITIONS
DATA READS AND WRITES NPU INACT WHILE LSRA=0
DATA READS AND WRITES NPU INACT AND LSRA=1
DH HAS ONLY ONE CONDITION: DRIVING PATH
XD HAS ONLY ONE CONDITION: DRIVING PATH
X-ADDRESS BUFFER DURING DMA CY-2 INACTIVE
X-BUS IS A NORMAL I/O BUFFERED OUTPUT HERE FOR DRIVING PCAT DURE DEVICES
DRIVES SDL BUS
DRIVES SDH BUS
DRIVES DL BUS
DRIVES DH BUS
DRIVES XD BUS
MOVE TO SD OUTPUTS
SDL_CONV_REGISTER