



Fast analog outputs

125 MS/s, 14 bits, ± 1 V

Fast analog inputs

125 MS/s, 14 bits, ± 1 V

IN2

Digital extension

16 I/O ch., 125 MS/s, 3.3 V

OUTVolt 2

Analog extension

~100 kS/s, ~12 bits, 0-1.8 V
4 SAR ADC ch., 4 PWM DAC ch.,

Microprocessor + FPGA

Dual core ARM-Cortex A9 Zync SoC

DDR3 RAM

512 MB

Daisy chain

microSD

OS drive & FPGA design

Ethernet

1 Gb/s

USB

OTG

micro USB

SO console

micro USB

Power in
5 V, 2 A