REG OFFSET	
0x0	FIFO TX
0x4	CONTROL TX
0x8	FIFO RX
0xC	CONTROL RX

CONTROL TX	
BIT 0	FIFO TX EMPTY = 1
BIT 1	FIFO TX FULL = 1

CONTROL RX	
BIT 0	FIFO RX EMPTY = 1
BIT 1	FIFO RX FULL = 1
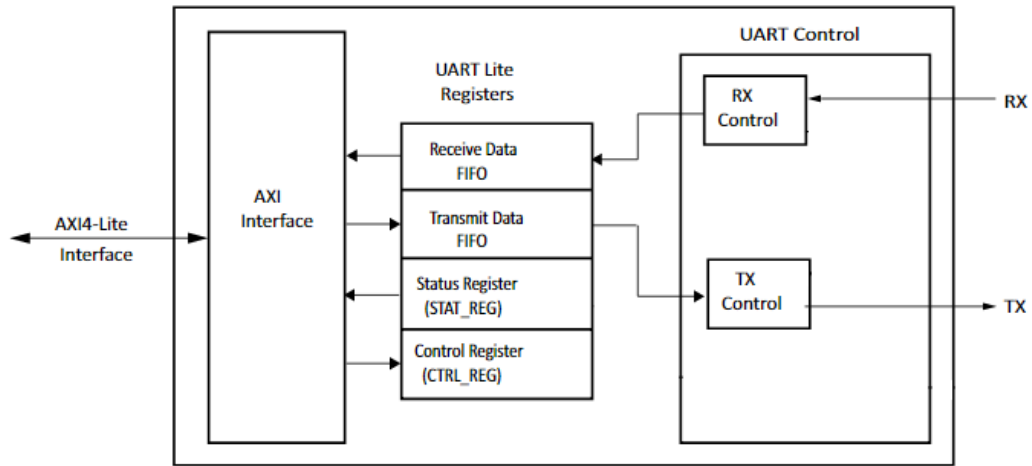
Block Diagram of AXI UART Lite