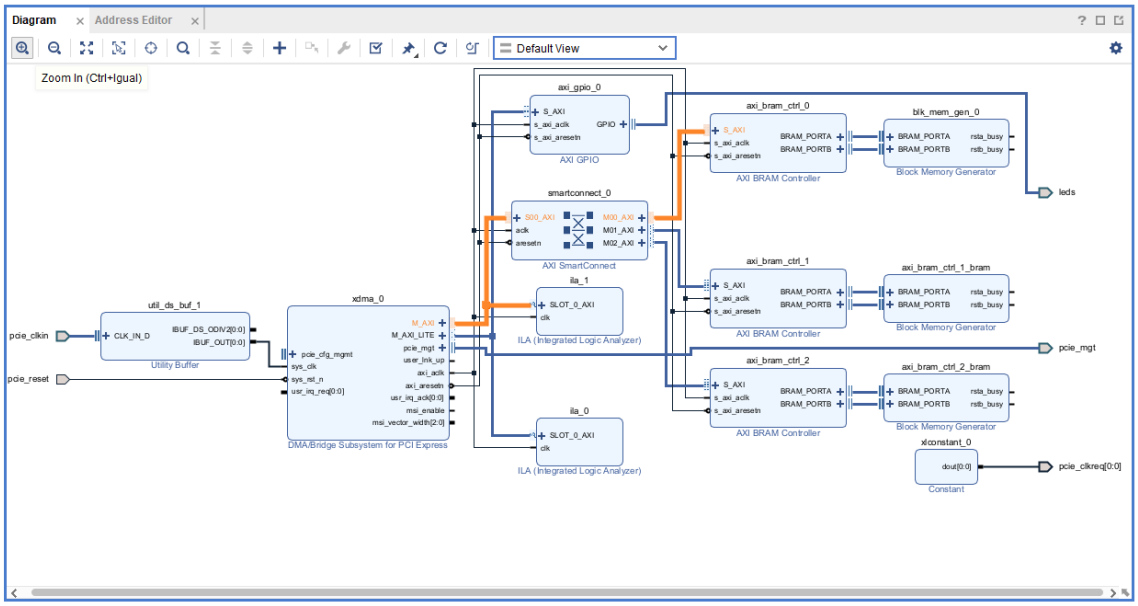
Diagram | Address Editor

Assigned (3) | Unassigned (1) | Excluded (0) | Hide All

Name	Interface	Slave Segment	Master Base Address	Range	Master High Address
Network 0					
/xdma_0					
/xdma_0/M_AXI (64 address bits : 16E)					
/axi_bram_ctrl_0/S_AXI	S_AXI	Mem0	0xC000_0000	8K	0xC000_1FFF
/axi_bram_ctrl_1/S_AXI	S_AXI	Mem0	0xC200_0000	4K	0xC200_0FFF
/axi_bram_ctrl_2/S_AXI	S_AXI	Mem0	0xC400_0000	16K	0xC400_3FFF
Network 1					
/xdma_0					
/xdma_0/M_AXI_LITE (32 address bits : 4G)					
Unassigned (1)					
/axi_gpio_0/S_AXI	S_AXI	Reg