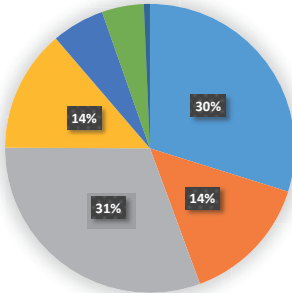


High-precision mode



Low-precision mode

- Buffers of the SAR Logic
- Comparators
- Bias Circuits
- CDS Circuits
- Peripheral Circuit
- Output Buffer of the Ramp Generator
- Input Buffer of the Ramp generator