

Certificate of Training

Sreeram K R

has successfully completed a 6-week online training on **VLSI Design**. The training consisted of Introduction to VLSI, HDL Coding Concepts, Combinational Circuits, Sequential Circuit Design, Finite State Machines, System Design using FPGA, and Case Studies & Final Project modules.

We wish Sreeram all the best for future endeavours.

A handwritten signature in black ink, appearing to read "Sarvesh".

Sarvesh Agrawal
FOUNDER & CEO, INTERNSHALA



Dr. Shankar Raman
CEO, IITM PRAVARTAK TECHNOLOGIES FOUNDATION

Date of certification: 2024-12-17

Certificate no. : 15mk77bn60p

For certificate authentication, please visit https://trainings.internshala.com/verify_certificate