No	Peripheral	Pin	Function
1	LTDC_CLK	PG7	LCD CLK
2	LTDC_DE	PF10	LCD Data Enable
3	LTDC_VSYNC	PA4	LCD Vertical Sync
4	LTDC_HSYNC	PC6	LCD Horizontal Sync
5	LTDC_R3	PB0	LCD Red Channel 3
6	LTDC_R4	PA5	LCD Red Channel 4
7	LTDC_R5	PA9	LCD Red Channel 5
8	LTDC_R6	PB1	LCD Red Channel 6
9	LTDC_R7	PG6	LCD Red Channel 7
10	LTDC_G2	PA6	LCD Green Channel 2
11	LTDC_G3	PG10	LCD Green Channel 3
12	LTDC_G4	PB10	LCD Green Channel 4
13	LTDC_G5	PB11	LCD Green Channel 5
14	LTDC_G6	PC7	LCD Green Channel 6
15	LTDC_G7	PD3	LCD Green Channel 7
16	LTDC_B3	PG11	LCD Blue Channel 3
17	LTDC_B4	PA10	LCD Blue Channel 4
18	LTDC_B5	PA3	LCD Blue Channel 5
19	LTDC_B6	PB8	LCD Blue Channel 6
20	LTDC_B7	PB9	LCD Blue Channel 7
21	LCD_RESET	PE4	LCD Reset
22	LCD_SPI_CLK	PE2	LCD SPI Clock
23	LCD_SPI_DATA	PE6	LCD SPI Data
24	LCD_SPI_CS	PE3	LCD SPI Chip Select
25	CTP_INT	PC8	CTP Interrupt
26	CTP_RESET	PB7	CTP Reset
27	CTP_SCL	PA8	CTP I2C SCL
28	CTP_SDA	PC9	CTP I2C SDA
29	LCD_BL	PA7	LCD Backlight

No	Peripheral	Pin	Function
1	SDRAM_CLK	PG8	SDRAM Clock
2	SDRAM_CKE	PC5	SDRAM Clock Enable
3	SDRAM_CS	PC4	SDRAM Chip Select
4	SDRAM_WE	PC0	SDRAM Write Enable
5	SDRAM_CAS	PG15	SDRAM Column Strobe
6	SDRAM_RAS	PF11	SDRAM Row Strobe
7	SDRAM_NBL0	PE0	SDRAM Low Nibble
8	SDRAM_NBL1	PE1	SDRAM High Nibble
9	SDRAM_BA0	PG4	SDRAM Bank Address 0
10	SDRAM_BA1	PG5	SDRAM Bank Address 1
11	SDRAM_A0	PF0	SDRAM Address 0
12	SDRAM_A1	PF1	SDRAM Address 1
13	SDRAM_A2	PF2	SDRAM Address 2
14	SDRAM_A3	PF3	SDRAM Address 3
15	SDRAM_A4	PF4	SDRAM Address 4
16	SDRAM_A5	PF5	SDRAM Address 5
17	SDRAM_A6	PF12	SDRAM Address 6
18	SDRAM_A7	PF13	SDRAM Address 7
19	SDRAM_A8	PF14	SDRAM Address 8
20	SDRAM_A9	PF15	SDRAM Address 9
21	SDRAM_A10	PG0	SDRAM Address 10
22	SDRAM_A11	PG1	SDRAM Address 11
23	SDRAM_A12	PG2	SDRAM Address 12
24	SDRAM_DQ0	PD14	SDRAM Data 0
25	SDRAM_DQ1	PD15	SDRAM Data 1
26	SDRAM_DQ2	PD0	SDRAM Data 2
27	SDRAM_DQ3	PD1	SDRAM Data 3
28	SDRAM_DQ4	PE7	SDRAM Data 4
29	SDRAM_DQ5	PE8	SDRAM Data 5
30	SDRAM_DQ6	PE9	SDRAM Data 6
31	SDRAM_DQ7	PE10	SDRAM Data 7
32	SDRAM_DQ8	PE11	SDRAM Data 8
33	SDRAM_DQ9	PE12	SDRAM Data 9
34	SDRAM_DQ10	PE13	SDRAM Data 10
35	SDRAM_DQ11	PE14	SDRAM Data 11
36	SDRAM_DQ12	PE15	SDRAM Data 12
37	SDRAM_DQ13	PD8	SDRAM Data 13
38	SDRAM_DQ14	PD9	SDRAM Data 14
39	SDRAM_DQ15	PD10	SDRAM Data 15

No	Peripheral	Pin	Function
1	QSPI_FLASH_CLK	PB2	QuadSPI Flash Clock
2	QSPI_FLASH_CS	PB6	QuadSPI Flash Chip Select
3	QSPI_FLASH_D0	PF8	QuadSPI Flash Data 0
4	QSPI_FLASH_D1	PF9	QuadSPI Flash Data 1
5	QSPI_FLASH_D2	PF7	QuadSPI Flash Data 2
6	QSPI_FLASH_D3	PF6	QuadSPI Flash Data 3

No	Peripheral	Pin	Function
1	EEPROM SCL	PD12	EEPROM I2C SCL
2	EEPROM SDA	PD13	EEPROM I2C SDA

No	Peripheral	Pin	Function
1	SDCARD_CLK	PC1	SDCARD Clock
2	SDCARD_CMD	PD7	SDCARD Data/Command
3	SDCARD_DAT0	PB14	SDCARD Data 0
4	SDCARD_DAT1	PB15	SDCARD Data 1
5	SDCARD_DAT2	PB3	SDCARD Data 2
6	SDCARD_DAT3	PB4	SDCARD Data 3
7	SDCARD_DET	PB5	SDCARD Detection

No	Peripheral	Pin	Function
1	I2S_CLK	PB13	
2	I2S_SDO	PC3	
3	I2S_WS	PB12	
4	I2S_SD_MODE	PC2	

No	Peripheral	Pin	Function
1	LED_0	PG12	LED Onboard 0
2	LED_1	PG13	LED Onboard 1

No	Peripheral	Pin	Function
1	BUTTON_0	PA0	Button 0
2	BUTTON_1	PA1	Button 1

No	Peripheral	Pin	Function
1	STEMMA_QT_SCL	PD12	STEMMA QT I2C SCL
2	STEMMA_QT_SDA	PD13	STEMMA QT I2C SDA

No	Peripheral	Pin	Function
1	UART_EXT_RX	PG9	External UART RX
2	UART_EXT_TX	PG14	External UART TX

No	Peripheral	Pin	Function
1	STLINK_UART_RX	PD6	STLINK UART RX
2	STLINK_UART_TX	PD5	STLINK UART TX