

# 2.47 inch Round DSI Screen 480x480 with CTP

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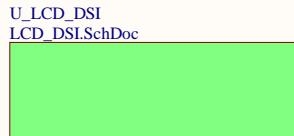
MB1835

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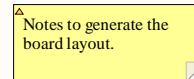
## Legend

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General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.



## Open Platform License Agreement

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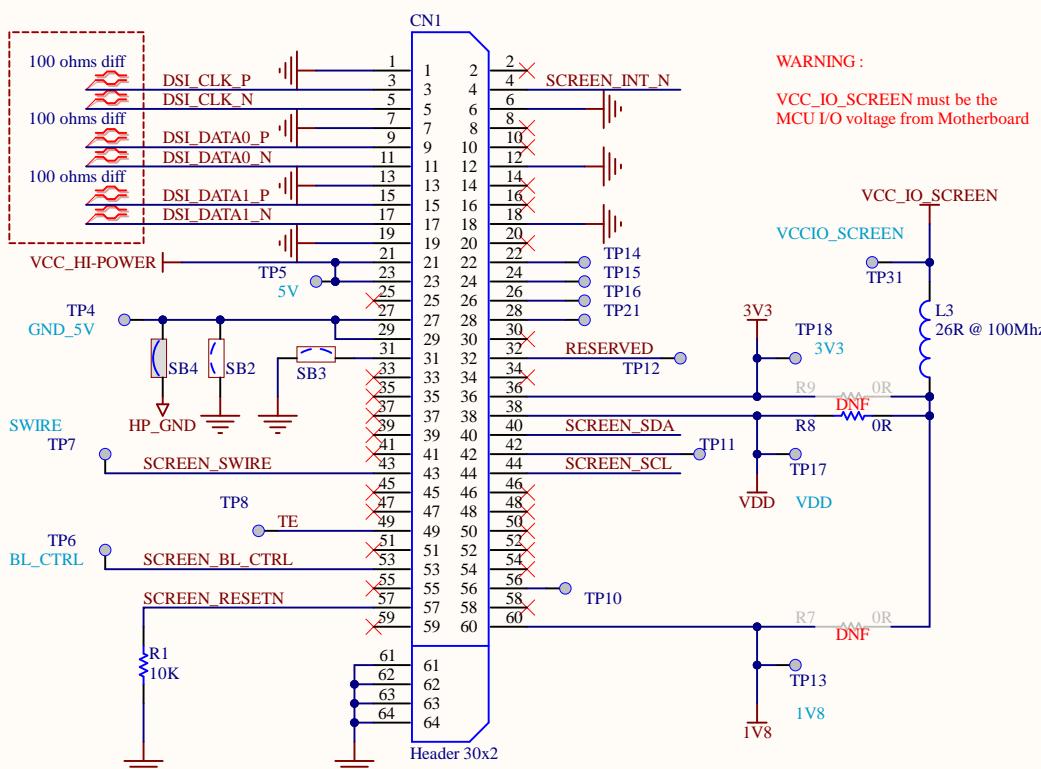
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Title: <a href="#">Project overview</a>	Project: <a href="#">2.47 inch Round DSI Screen 480x480 with CTP</a>
Variant: <a href="#">VDDIV8</a>	
Revision: <a href="#">B-01</a>	Reference: <a href="#">MB1835</a>
Size: A4	Date: 14 FEB 2023
	Sheet: 1 of 2



## **DAUGHTER BOARD CONNECTOR**

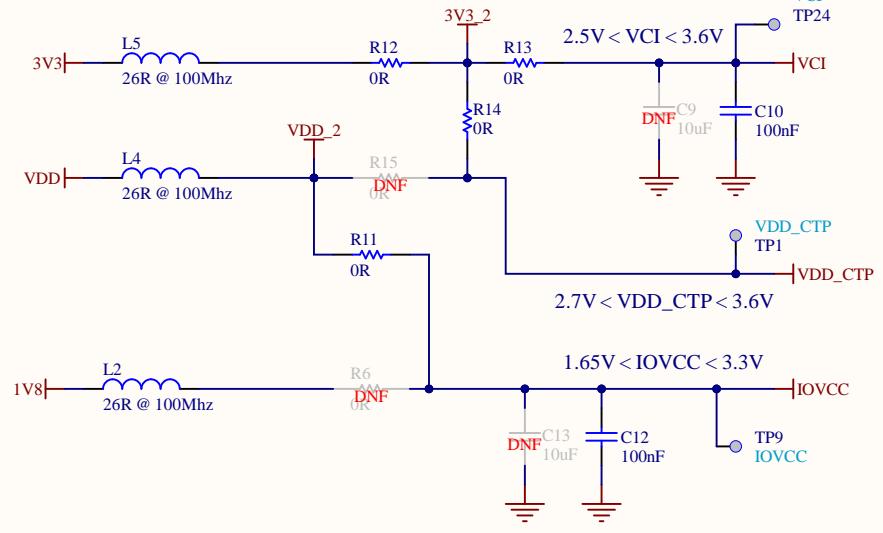
The N/P signals shall be length equalized, and should not include any PTH vias.



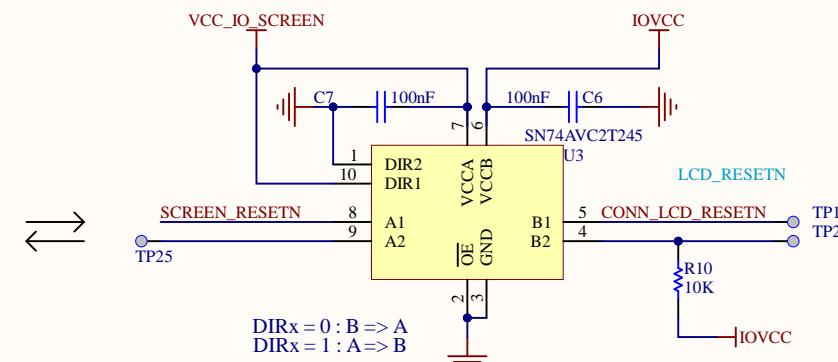
**WARNING :** VDD may be 1.8V or 3.3V on Motherboard  
By default, VDD must be 1.8V to comply with LCD module

## POWER supply Options

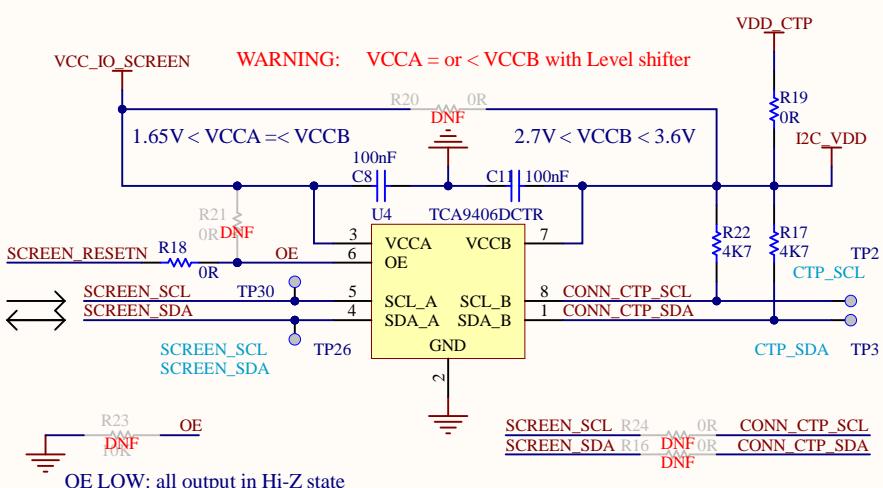
Ensure Power supplies from Motherboard respect below voltage limits



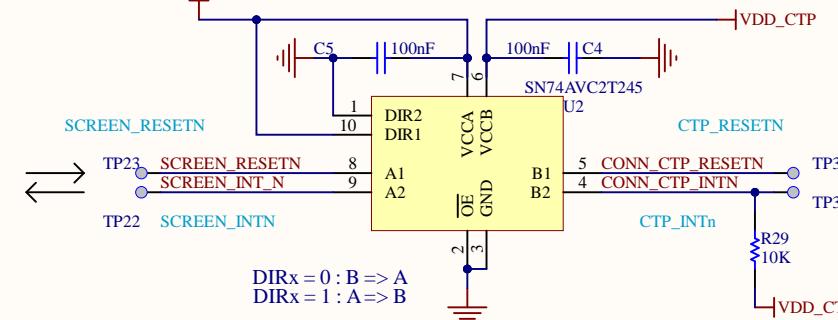
## LCD Level shifter



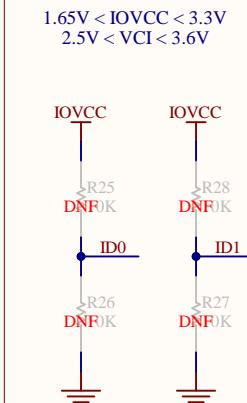
## CTP Level shifters



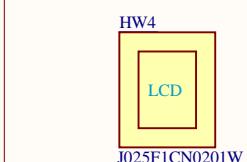
VCC IQ SCREEN



## LCD CONNECTOR



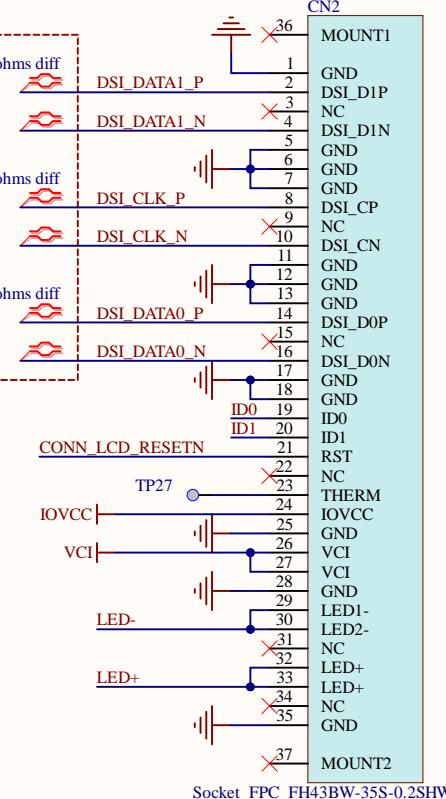
LCD Module J025F1CN0201W



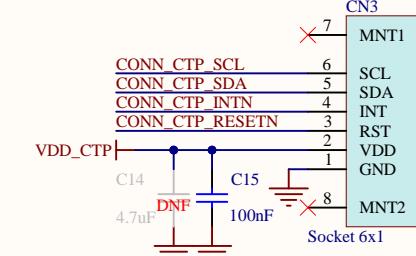
CTP CONNECTOR

CTP Driver IC is ST1633i  
I2C address : 0xE0/0xE1 (W/R)

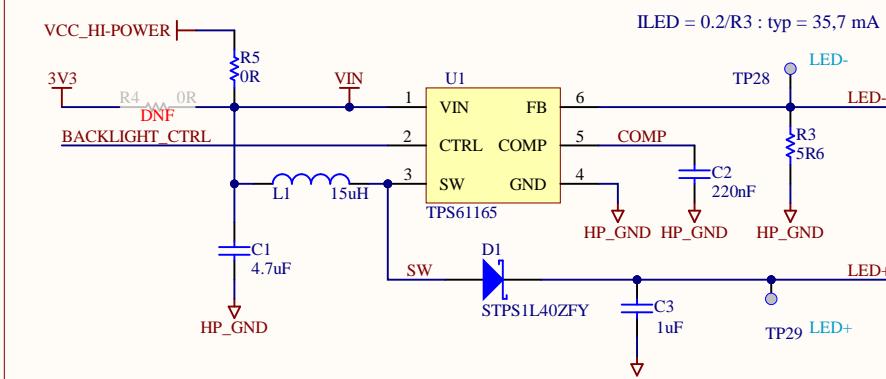
$2.7V < VDD (\text{CTP}) < 3.6$



The pinout is inverted vs FPC Ma



**BACKLIGHT POWER : 9.2V typ. / 36mA**

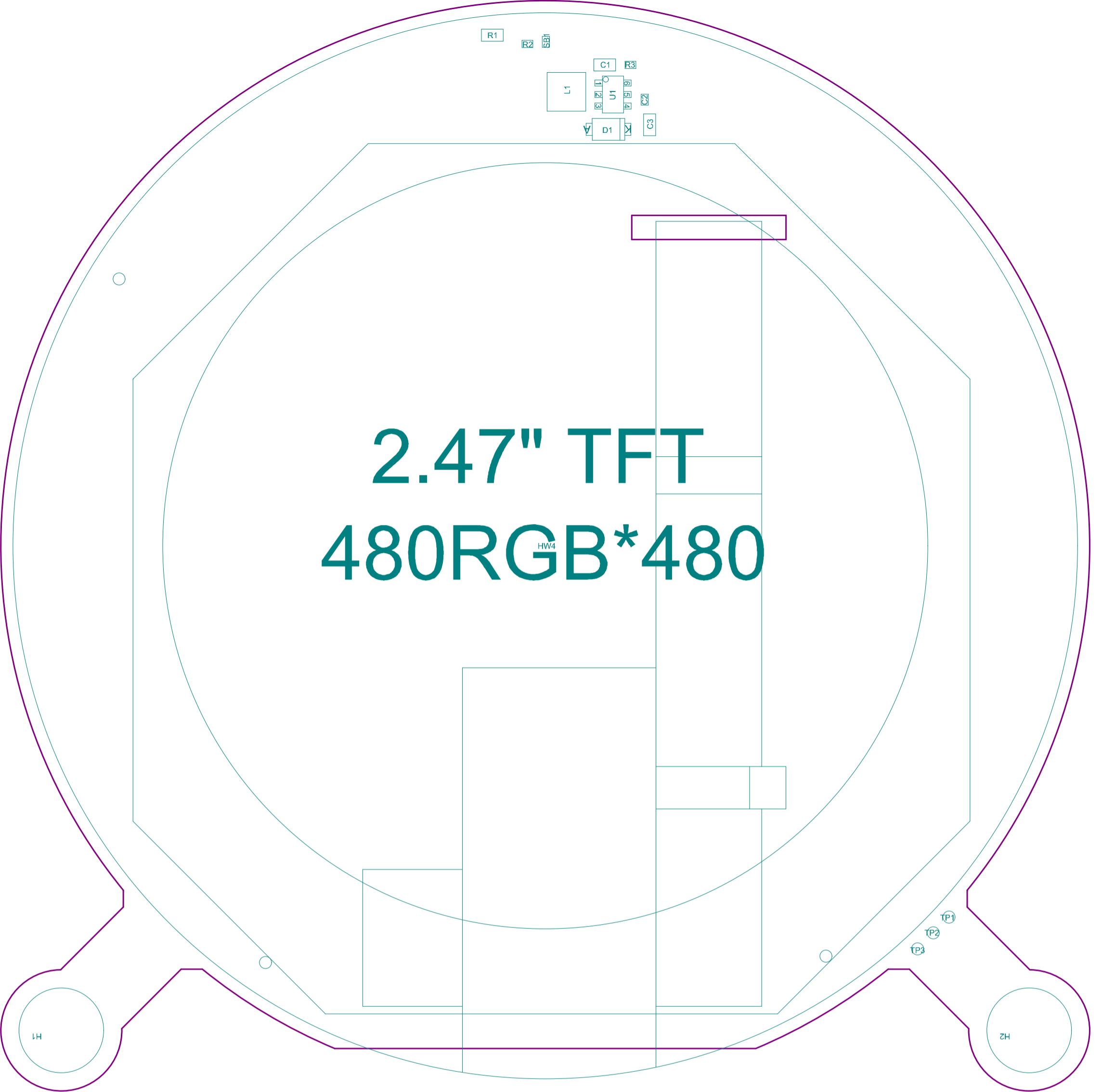


SCREEN\_BL\_CTRL R2 0R SCREEN\_BL\_CTRL supports PWM and SWIRE Backlight mode



The local HP\_GND plan shall be large enough below U10, and connected to Ground close to CN1 with a wide trace.

le: **LCD\_DSI**  
 Object: **2.47 inch Round DSI Screen 480x480 with CTP**  
 Variant: **VDD1V8**  
 Revision: **B-01** Reference: **MB1835**  
 Date: **14 FEB 2023** Sheet: **2 of 3**



Project: 2.47 inch Round DS1 Screen 480x480 with CTP

Layer: M14-Top Assembly

Gerber: .GM14

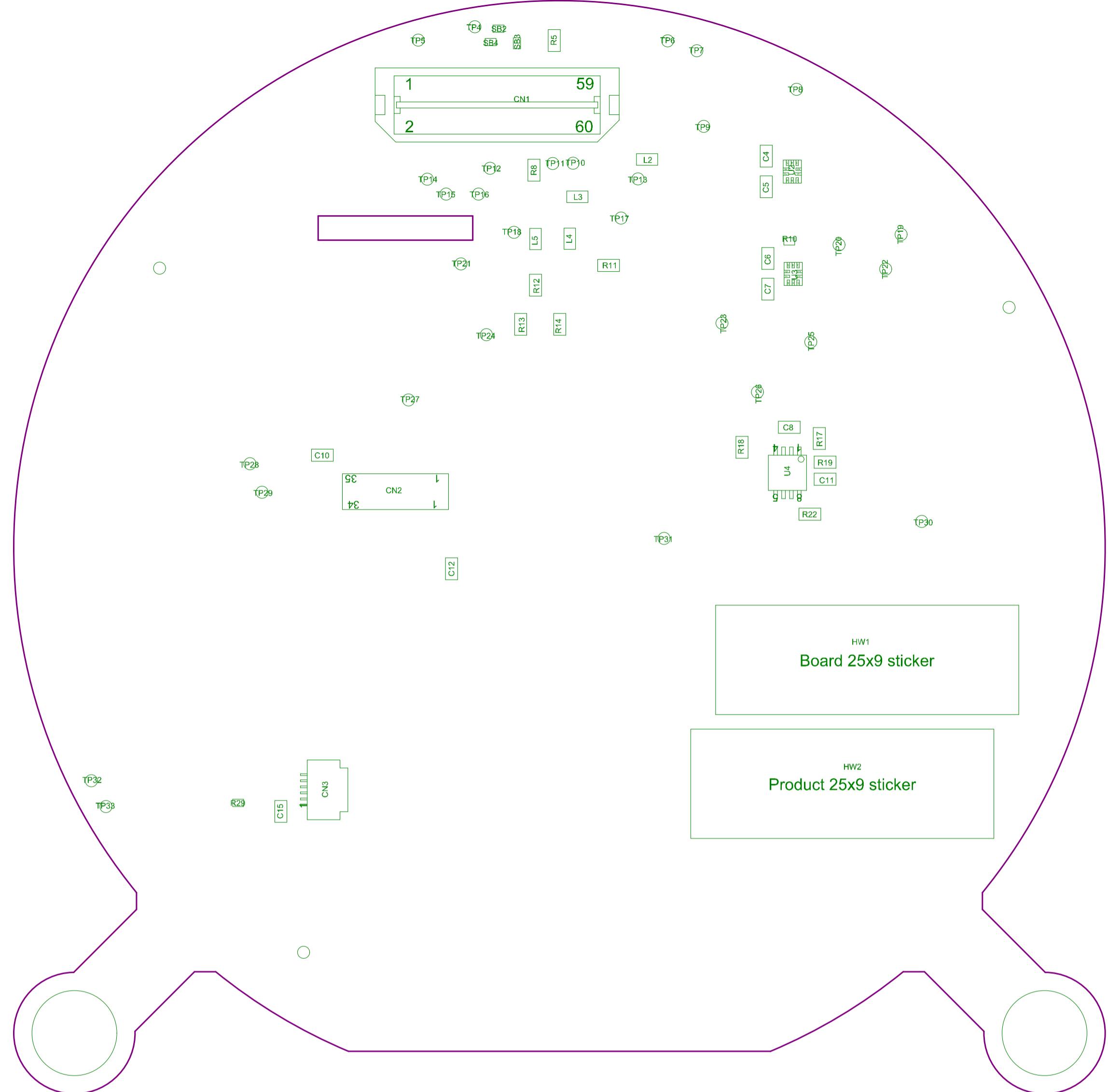
Variant: VDD1V8

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Project: 2.47 inch Round DS1 Screen 480x480 with CTP

Layer: M15-Bottom Assembly

Gerber:.GM15

Variant: VDD1V8

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Date: 14 FEB 2023

Rev: B





