No	Peripheral	Pin	Function
1	LTDC_CLK	PG7	LCD CLK
2	LTDC_DE	PF10	LCD Data Enable
3	LTDC_VSYNC	PA4	LCD Vertical Sync
4	LTDC_HSYNC	PC6	LCD Horizontal Sync
5	LTDC_R0	PG13	LCD Red Channel 0
6	LTDC_R1	PA2	LCD Red Channel 1
7	LTDC_R2	PA1	LCD Red Channel 2
8	LTDC_R3	PB0	LCD Red Channel 3
9	LTDC_R4	PA5	LCD Red Channel 4
10	LTDC_R5	PC0	LCD Red Channel 5
11	LTDC_R6	PB1	LCD Red Channel 6
12	LTDC_R7	PG6	LCD Red Channel 7
13	LTDC_G0	PE5	LCD Green Channel 0
14	LTDC_G1	PE6	LCD Green Channel 1
15	LTDC_G2	PA6	LCD Green Channel 2
16	LTDC_G3	PG10	LCD Green Channel 3
17	LTDC_G4	PB10	LCD Green Channel 4
18	LTDC_G5	PB11	LCD Green Channel 5
19	LTDC_G6	PC7	LCD Green Channel 6
20	LTDC_G7	PD3	LCD Green Channel 7
21	LTDC_B0	PE4	LCD Blue Channel 0
22	LTDC_B1	PG12	LCD Blue Channel 1
23	LTDC_B2	PD6	LCD Blue Channel 2
24	LTDC_B3	PG11	LCD Blue Channel 3
25	LTDC_B4	PA10	LCD Blue Channel 4
26	LTDC_B5	PA3	LCD Blue Channel 5
27	LTDC_B6	PB8	LCD Blue Channel 6
28	LTDC_B7	PB9	LCD Blue Channel 7
29	CTP_SCL	PA8	Capacitive Touch I2C SCL
30	CTP_SDA	PC9	Capacitive Touch I2C SDA
31	CTP_RST	PC8	Capacitive Touch Reset Pin
32	CTP_INT	PD11	Capacitive Touch Interrupt Pin
33	CTP_WAKE	PG3	Capacitive Touch Wake Up Pin
34	RTP_CS	PD5	Resistive Touch Chip Select
35	RTP_CLK	PC10	Resistive Touch Clock
36	RTP_MOSI	PC12	Resistive Touch MOSI
37	RTP_MISO	PC11	Resistive Touch MISO
38	RTP_BUSY	PD2	Resistive Touch Busy
39	RTP_IRQ	PD4	Resistive Touch INT
40	LCD_BL	PA15	LCD Backlight Control

No	Peripheral	Pin	Function
1	SDRAM_CLK	PG8	SDRAM Clock
2	SDRAM_CKE	PC5	SDRAM Clock Enable
3	SDRAM_CS	PC4	SDRAM Chip Select
4	SDRAM_WE	PA7	SDRAM Write Enable
5	SDRAM_CAS	PG15	SDRAM Column Strobe
6	SDRAM_RAS	PF11	SDRAM Row Strobe
7	SDRAM_NBL0	PE0	SDRAM Low Nibble
8	SDRAM_NBL1	PE1	SDRAM High Nibble
9	SDRAM_BA0	PG4	SDRAM Bank Address 0
10	SDRAM_BA1	PG5	SDRAM Bank Address 1
11	SDRAM_A0	PF0	SDRAM Address 0
12	SDRAM_A1	PF1	SDRAM Address 1
13	SDRAM_A2	PF2	SDRAM Address 2
14	SDRAM_A3	PF3	SDRAM Address 3
15	SDRAM_A4	PF4	SDRAM Address 4
16	SDRAM_A5	PF5	SDRAM Address 5
17	SDRAM_A6	PF12	SDRAM Address 6
18	SDRAM_A7	PF13	SDRAM Address 7
19	SDRAM_A8	PF14	SDRAM Address 8
20	SDRAM_A9	PF15	SDRAM Address 9
21	SDRAM_A10	PG0	SDRAM Address 10
22	SDRAM_A11	PG1	SDRAM Address 11
23	SDRAM_A12	PG2	SDRAM Address 12
24	SDRAM_DQ0	PD14	SDRAM Data 0
25	SDRAM_DQ1	PD15	SDRAM Data 1
26	SDRAM_DQ2	PD0	SDRAM Data 2
27	SDRAM_DQ3	PD1	SDRAM Data 3
28	SDRAM_DQ4	PE7	SDRAM Data 4
29	SDRAM_DQ5	PE8	SDRAM Data 5
30	SDRAM_DQ6	PE9	SDRAM Data 6
31	SDRAM_DQ7	PE10	SDRAM Data 7
32	SDRAM_DQ8	PE11	SDRAM Data 8
33	SDRAM_DQ9	PE12	SDRAM Data 9
34	SDRAM_DQ10	PE13	SDRAM Data 10
35	SDRAM_DQ11	PE14	SDRAM Data 11
36	SDRAM_DQ12	PE15	SDRAM Data 12
37	SDRAM_DQ13	PD8	SDRAM Data 13
38	SDRAM_DQ14	PD9	SDRAM Data 14
39	SDRAM_DQ15	PD10	SDRAM Data 15

No	Peripheral	Pin	Function
1	QSPI_CLK	PB2	QSPI Flash Clock
2	QSPI_CS	PB6	QSPI Flash Chip Select
3	QSPI_DIO0	PF8	QSPI Flash Data 0
4	QSPI_DIO1	PF9	QSPI Flash Data 1
5	QSPI_DIO2	PF7	QSPI Flash Data 2
6	QSPI_DIO3	PF6	QSPI Flash Data 3

No	Peripheral	Pin	Function
1	EE_SCL	PD12	I2C EEPROM SCL
2	EE_SDA	PD13	I2C EEPROM SDA

No	Peripheral	Pin	Function
1	SD_DET	PC2	SD CARD Detect Pin
2	SDCARD_CLK	PC1	SD CARD Clock
3	SDCARD_CMD	PA0	SD CARD Command
4	SDCARD_D0	PB14	SD CARD Data 0
5	SDCARD_D1	PB15	SD CARD Data 1
6	SDCARD_D2	PB3	SD CARD Data 2
7	SDCARD_D3	PB4	SD CARD Data 3

No	Peripheral	Pin	Function
1	I2S_WS	PB12	I2S Word Select
2	I2S_CLK	PB13	I2S Clock
3	I2S_SDO	PC3	I2S Serial Data Out
4	I2S_SD_MODE	PE3	I2S SD Mode / Share with LED 1

No	Peripheral	Pin	Function
1	LED_0	PE2	LED Out 0
2	LED_1	PE3	LED Out 1 / Share with I2S SD_MODE
3	BTN_0	PC13	Button Input 0
4	BTN_1	PD7	Button Input 1
5	BUZZER	PB5	Buzzer Output

No	Peripheral	Pin	Function
1	Serial Debug TX	PA9	ST-LINK Serial TX
2	Serial Debug RX	PB7	ST-LINK Serial TX
3	Serial Comm TX	PG14	Serial Comm TX
4	Serial Comm RX	PG9	Serial Comm RX