

Rohan Gandhi

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CAREER SUMMARY

Driven and ambitious Engineer with hands-on Quality Assurance experience at Hudl, where I contributed to product quality through rigorous testing, automation, and user-centered feedback. A quick learner, I thrive in collaborative environments and enjoy solving complex problems with a product-first mindset. Earned a degree in Computer Science & Engineering from UCLA, where I built a strong technical foundation and led cross-functional student engineering teams from ideation to launch. Now seeking an entry-level role in software engineering or product management to drive meaningful product improvements.

EDUCATION

University of California, Los Angeles | Bachelor of Science in Computer Science and Engineering **June 2025**

- **Relevant Courses:** Computing Theory, Algorithms and Complexity, Computer Imaging, Computer Vision

WORK EXPERIENCE/INTERNSHIPS

Hudl **June 2023 - May 2025**

The pioneer in performance analysis technology, helping more than 230K teams globally stay ahead of the competition.

Hardware Quality Assurance Engineering Intern, June 2024 - May 2025

- Spearheaded quality assurance process for the deployment of Focus Point cameras for thousands of users in July 2024.
- Contributed to product roadmap discussions by aligning testing efforts with customer expectations for 6 products.
- Collaborated with cross-functional teams every 2 weeks to validate updates to the Hudl Focus app.
- Designed Detox and Antioch automation tests in TypeScript, to reach our goal of 90% hit rate for API tests.
- Conducted in-depth analysis of test results, identifying and resolving issues to improve product quality.

Software Quality Assurance Engineering Intern, June 2023 - September 2023

- Managed end-to-end quality assurance for Auth0 and Hudl Team Profiles, ensuring functionality for millions of users.
- Authored and executed 30+ UI/API tests using Gherkin and Cucumber to deliver a seamless user experience.
- Maintained daily production deployments via GitHub and Alyx3 configuration management systems.
- Participated in planning meetings to provide 3 quality assurance best practices and contribute to product decisions.

TechFabric **June 2022 - September 2022**

TechFabric, turning complex AI technology into powerful, practical solutions that drive real business value.

Product Manager Intern

- Collaborated with 2 cross-functional teams and 3 stakeholders to define product requirements and plan delivery.
- Coordinated the Discovery phase to understand client requirements and developed a comprehensive one-year roadmap.
- Acquired hands-on experience in utilizing key tools including Azure DevOps, Teamwork, Jira, and Miro.

LEADERSHIP EXPERIENCES

Projects Director | Creative Labs | University of California, Los Angeles **May 2024 - June 2025**

- Directed 6 project teams to successfully deliver a cutting-edge software development project within an 8-week timeframe.
- Orchestrated the presentation of over 60 project members, engaging 100+ students through interactive demonstrations.

Corporate Director | Engineering Society at UCLA | University of California, Los Angeles **June 2023 - June 2025**

- Organized a high-impact networking event, connecting ESUC's 4 corporate sponsors with 50+ Engineering students.
- Oversaw the Industry Liaison Committee, serving as a pivotal link between UCLA faculty and 40 student leaders.
- Raised over \$20,000 from Corporate Sponsors to fund events such as Engineers Week and Engineering Welcome Day.

PROJECTS

Basketball Stats Data Pipeline Project **July 2025**

- Built a data pipeline in Python to scrape NBA stats using BeautifulSoup and pandas, enabling analysis of live web data
- Transformed HTML tables into DataFrames by handling missing values and formatting fields for downstream modeling

Energy Delay Product Optimization | University of California, Los Angeles **April 2025 - June 2025**

- Optimized a high-speed circuit for a 4-bit Carry Look-Ahead Adder, reducing propagation delay to the final carry output.
- Employed logical effort analysis and transistor sizing to minimize delay by simulating EDP across varying VDD values.

Morse Code Translator | University of California, Los Angeles **April 2025 - June 2025**

- Created a Morse Code communication system on FPGA boards to enable real-time message encoding and decoding.
- Developed an interface using buttons, switches, and a terminal connection to allow input, transmission, and display of messages between FPGA boards in both Morse and alphanumeric formats via UART.