

# FPGA Speaks

Level-2  
Samuel Rohan

## Contents

|   |                                 |   |
|---|---------------------------------|---|
| 1 | Introduction                    | 1 |
| 2 | Pre-requisites                  | 1 |
| 3 | Tools/Requirements              | 1 |
| 4 | What have I done in this level? | 1 |
| 5 | Links                           | 1 |

## 1 Introduction

In this level you will learn System Design in Verilog. This level is divided into two parts namely **Level-2.A** and **Level-2.B**. Test-Benches are also provided in this Level.

## 2 Pre-requisites

Basic knowledge of Digital System Design (Combinational and Sequential circuits, Memory elements, State diagrams and state tables etc) and Verilog Programming.

## 3 Tools/Requirements

Xilinx Vivado

## 4 What have I done in this level?

I have implemented system design for various systems(Combinational and Sequential) such as Adders, Multiplexers, encoders, decoders, Flipflops, counters, registers etc. **Level -2A** is of Basic standard and **Level-2B** is of Moderate standard.

## 5 Links

Link to my Github repo