

Project Final Evaluation Instructions

Greetings Students,

Hope you all are doing well. We are excited to see interesting ideas being realized and celebrate application of logic design in its purest form. By now you all should have gone through feedback of lab grade and theory grade of all milestones so far and getting ready for final milestone. We do realize that milestone 2 evaluation was purposefully harsh and its main purpose was to provide you feedback on where you and your group stands in meeting the goals. We also realize that theory grade costs more than lab grade in milestones whereas lab grade is equally important as being *enabler* for theory grade. Thus, we would like to encourage you all to reach out to respective Instructors for theory grade concerns and RAs of your lab for lab grade concerns to understand the expectations clearly for Final Milestone to avoid last minute surprises. Since, final report will include content from milestone 2 beside additional content (as identified **Checklist for Grading V1.0 Fall 2022- DLD.pdf**), so here is last favor: If you improve parts of your report related to milestone 2 rubrics, you 'll receive improved grade for theory component. Same does not apply for lab grade.

Please take a note of following deadlines to plan your days and distribute teamwork.

Deadlines:

- Final Evaluation will be on December 14. (See **Checklist for Grading V1.0 Fall 2022- DLD.pdf**), for expectations.)
{ You 'll receive scheduled time slot for evaluation for your group by next week.}
- Final Report will be due at **8:00 AM on December 14**. You are expected to appear in Final Evaluation with printout of report.
- Project Poster will be due at **11:59 PM on December 12**. {Find *Instructions for poster below*}
- 05 min Video will be due on **11:59 PM on December 13**. {Find *Instructions for video below*}
- DLD Project Exhibition & Competition (DPEC) to be held on December 14 in Playground. Nominations due by **December 1st, 11:59 PM**. {See details *below*}

DLD Fall 22 Team
Instructors, RAs, TAs

Video Guidelines:

Video is narrative of project idea, motivation, design and demonstration of logic design for digital systems practiced in your project.

Video details are as follows:

Video Audience: You should consider audience to be a “technically literate individual who have understanding of FSM”.

Video Goals:

- To introduce project idea and motivation
- To demonstrate project working
- To showcase the application of logic design ideas.
- To showcase logic design on FPGA using Verilog HDL
- To instill the motivation of logic design (Don't give motivation speech 😊)

Video Requirements:

- Total duration of video **must** not exceed 05 min. Though its duration can be less than 05 min.
- Video **must** start with 30 to 60 sec teaser demonstrating project in action creatively. See first 40 seconds of [this](#) as an example of teaser.
- Above two are only requirement and following are suggestions and left as choice of creativity.
- Suggested points to add:
 - Design Diagrams like System Overview (like Vending machine Block Diagram shown in lecture slides)
 - State Diagram of one at least FSM
 - Sample Simulation/Vivado results which may include showing screenshots or short video clip
 - Discuss Challenges: what was most challenging part and how your solved it. You can consider displaying on screen, taking input from xyz sensor, maintaining synchronization in modules, teamwork or any other challenge specific to your group.
- Suggested points NOT to add:
 - Design procedure like how state table was made and how equations were derived. *This will take a lot of time and is essentially not goal of video rather it is part for report.*
 - Details on FPGA board like what is BASYS 03 etc. *This video is not FPGA commercial.*
- Try to target following keywords in narrative:
 - Combinational Logic Design
 - Sequential Logic Design
 - Finite state machine (Moore/Mealy)

- Logic Gates
- Flip flops
- Binary number
- Binary Codes
- Verilog
- Basys 03
- FPGA

Have fun, be creative! Keep philosophy of Yoshin (Excellence, Aesthetics, Passion, Respect, Service) in mind when making videos.

Video FAQs

- Should video convey all design details? *No*
- Should someone narrate in video? *Yes*
- Is it necessary to narrate in real time during video? *No*
- Can we use text to speech tools to narrate the text rather than human voice? *No- it becomes robotic*
- Is it necessary to make power point presentation? *No, but it is necessary to rehearse and plan on how you will achieve all goals within 05 min time window. Presentation docks help in time management.*
- Will these videos be accessible to all class? *Yes*
- Will these videos be accessible to all HU? *Yes*
- Will these videos be accessible publicly outside HU? *All students in group will be asked for consent for selected videos to be posted publicly.*
- Post more questions in DLD Teams group or below.

How to submit: Submit your video [here](#)

Poster Guidelines

Poster is your single slide that you need to explain your work to wider group of audience. PFA poster template to be followed. Instructions regarding each section are given in template. Generally, poster has graphics and text. Keep text to be very brief and to the point. Pay attention to graphics visibility. If you are adding a flow chart, make sure the letters are big enough for printing, if you are adding graph make sure axes and labels are visible. Assume that poster will be printed on A-3 sized sheet.

FAQs

- Can we modify the template or do it differently?
Yes- as long as you address the points highlighted in template.

Participation in DPEC

DLD Project Exhibition and Competition (DPEC) is your opportunity to showcase your work to broader community and get recognition. Since we have limited space in exhibition therefore participation is based on nomination only. Participation is optional for groups. Course grade is not associated with participation in DPEC. Grade evaluation of participants in DPEC will be same as non-participants, it will be just in different physical space.

Why you should participate?

- It is free to participate.
- It is an opportunity to present your work to diverse population.
- All DPEC participants will get participation certificates.
- It is opportunity to win the competition and get recognition. Winners will be chosen by Judges based on Instructor inputs and public votes.
- {*Tentative*} We are planning to invite industry people as well. So, this could be opportunity for getting internship.

How to get Nominated?

- Interested groups should reach out to RAs/ Instructors for nomination for DPEC.
- Each Instructor and RA can nominate 02 groups only. Each TA can nominate one group only.
- Nominations will be based on following:
 - Progress of each group in milestone-2
 - Availability of team throughout the day
 - Any other requirement set by nominator.
- Nominations will be on rolling basis till all slots are full.