Design of
Two Stage CMOS OP AMP

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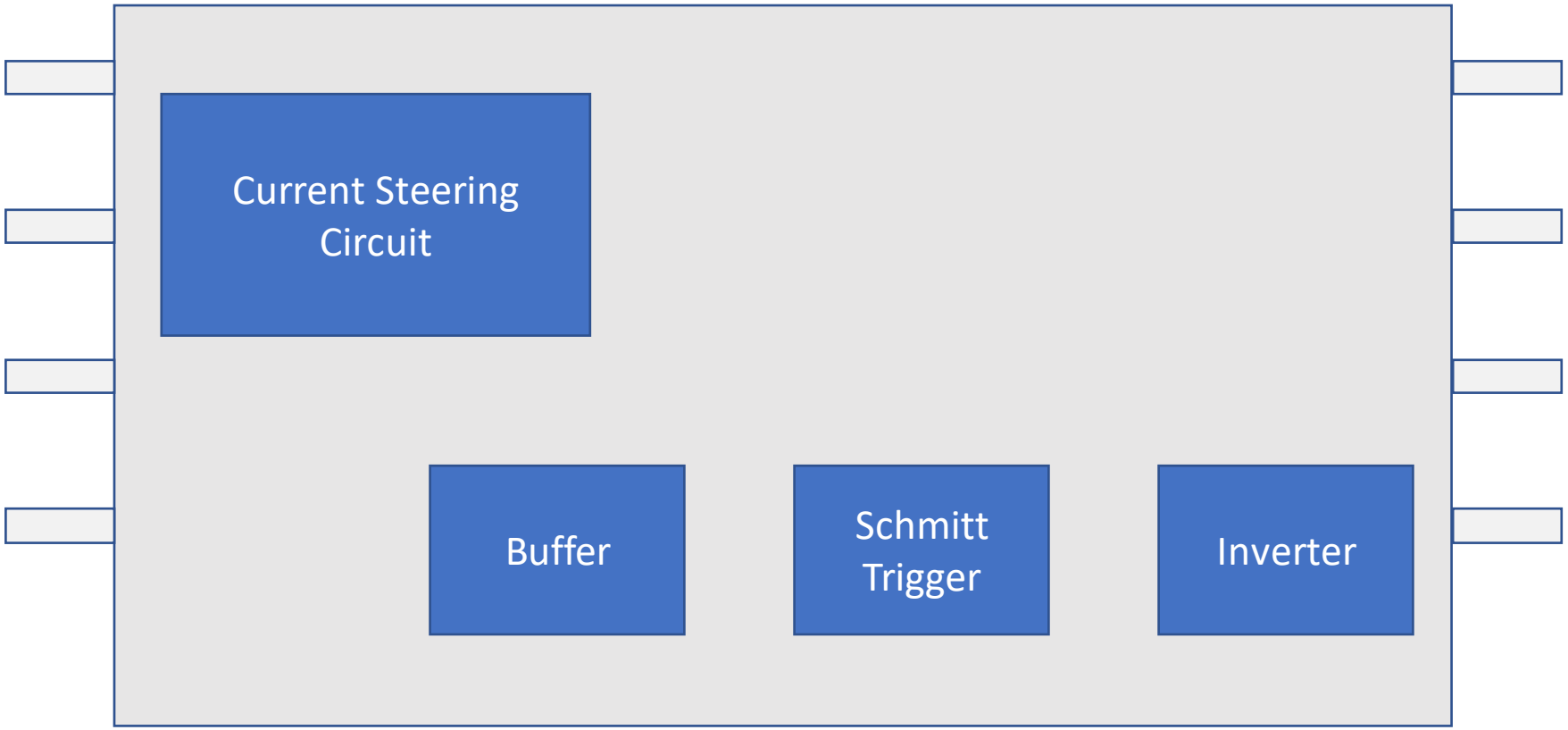
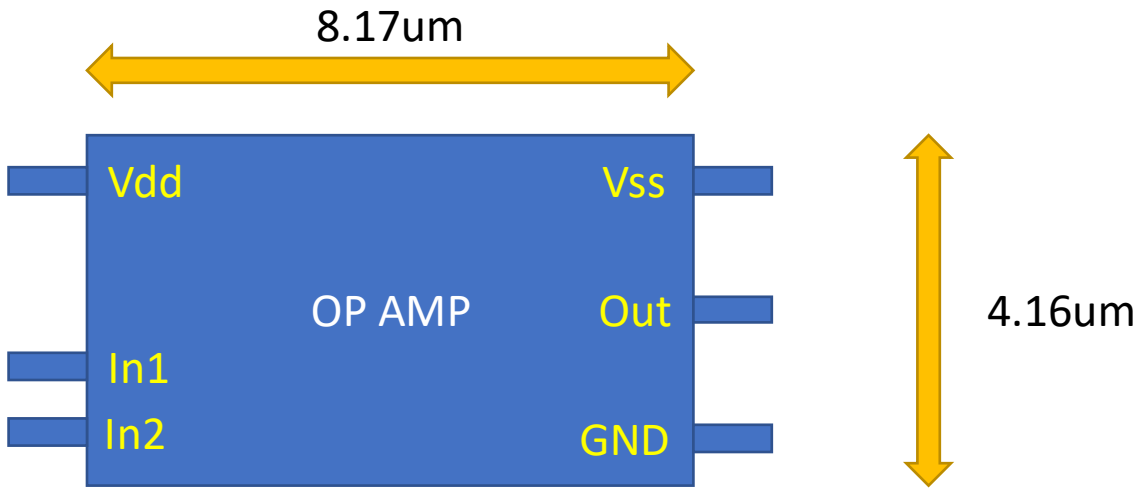
Application Note for Two Stage CMOS OP AMP

Voltage Controlled Oscillator



**Buffer, Schmitt Trigger and Inverter all use Operational Amplifier

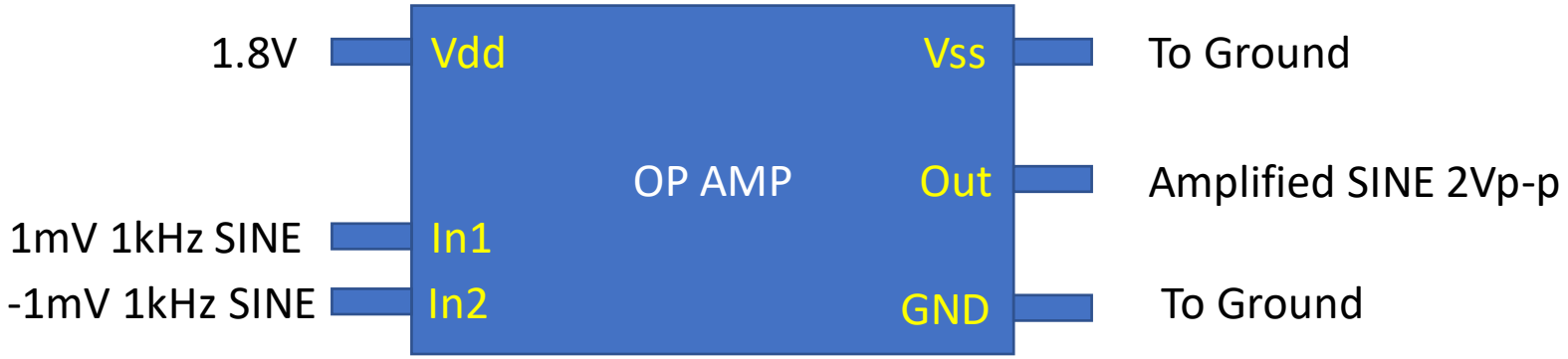
Dimensions



10*130*3 + 1*130*3 = 4.16um

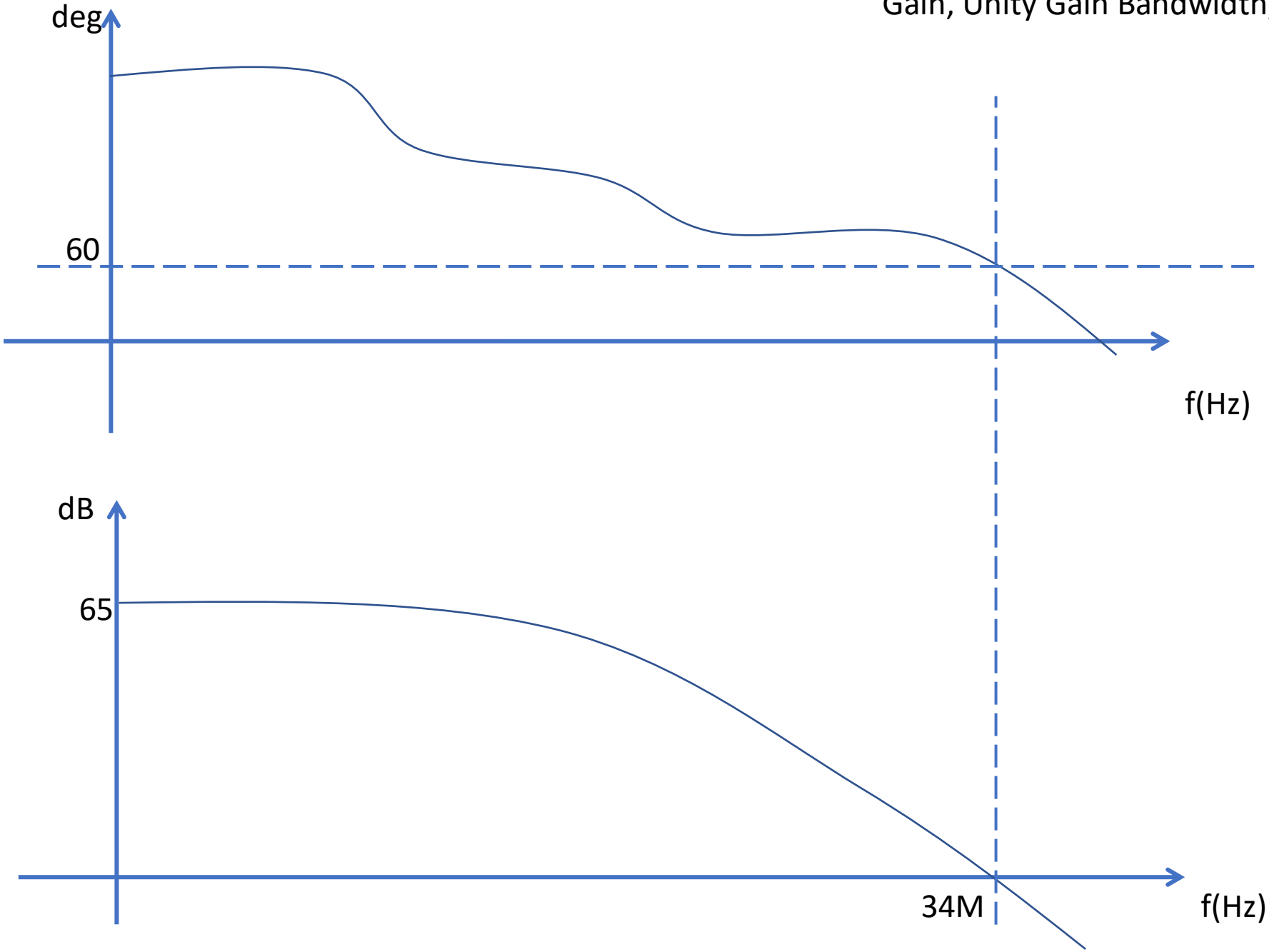
62.83*130 = 8.17um

Operating Mode

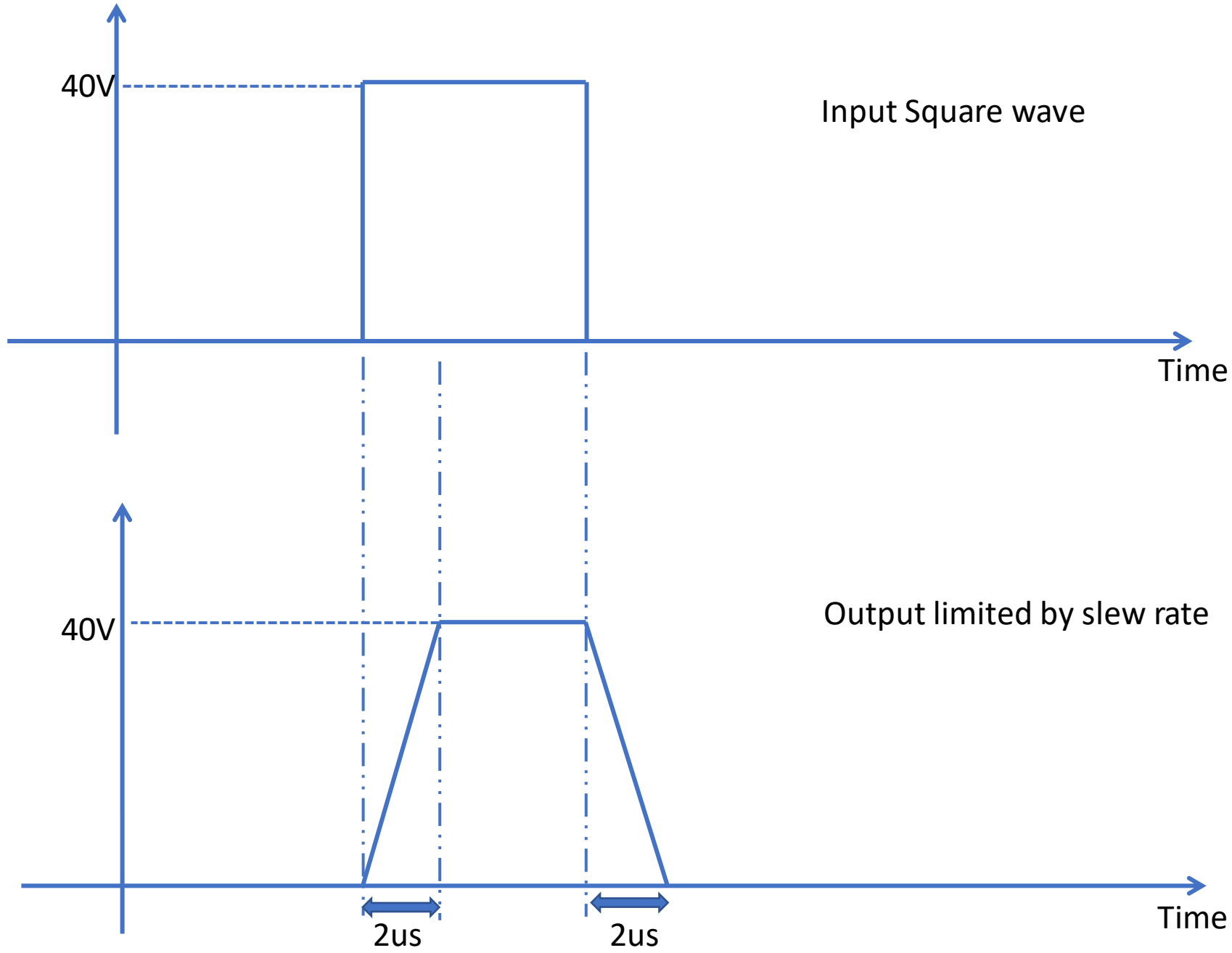


Target Waveforms

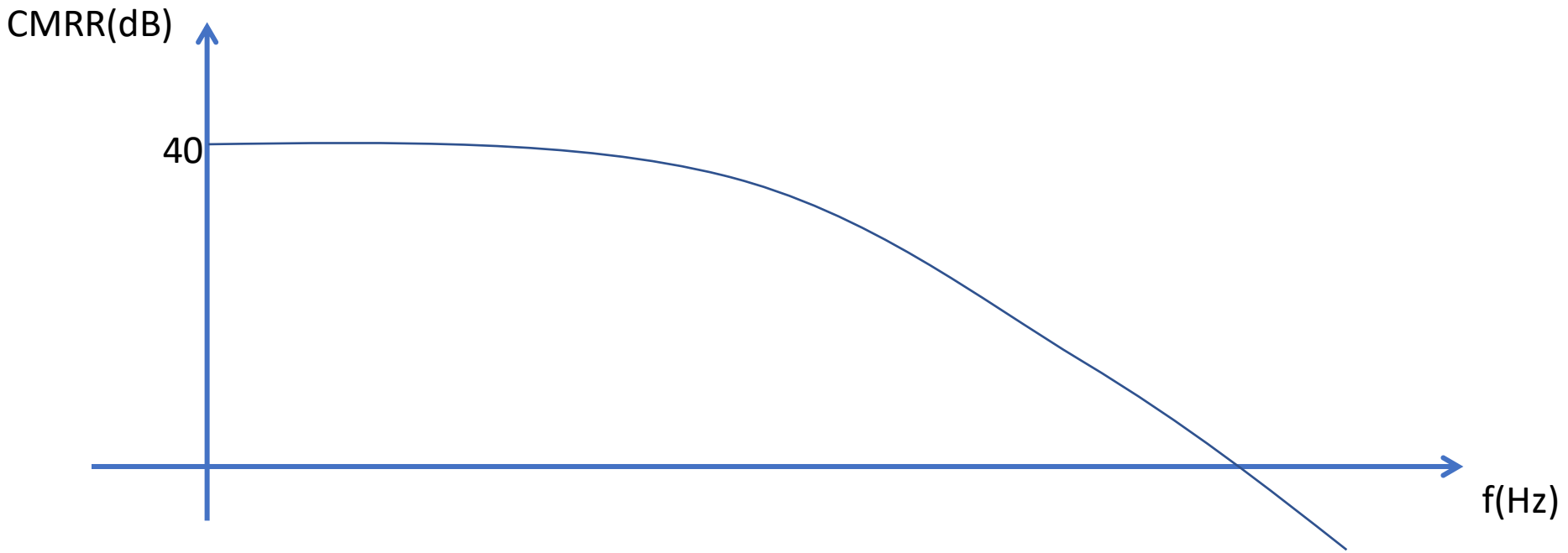
Gain, Unity Gain Bandwidth, Phase Margin



Slew rate = 20V/us



Common Mode Rejection Ratio = 40dB



Power Dissipation = 301uW