MIPS SIMULATOR-PHASE-2

CS18B029

CS18B022

- 5 stages

  IF   ID/RF   EX   MEM   WB

- Iteration-1

  →IF

  Iteration-2

  →ID/RF→IF

  Iteration-3

  →EX→ID/RF→IF

  Iteration-4

  →MEM→EX→ID/RF→IF

  Iteration-5

  →WB→MEM→EX→ID/RF→IF

  Iteration-6

  →WB→MEM→EX→ID/RF→IF

  ……..till end

- Bubble sort is rearranged in such a way so less number of stalls will be there.
- IPC=(no_of_cycles-no_of_stalls)/no_of_cycles.