## The second second

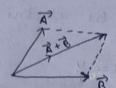
## CURRENT ELECTRICITY

Uses - i) Automabile

ii) Household

Scalar -> Do not have direction

Vector > Direction and magnitude



A---- current - Blow of charge

A > weter law of addition

current does not aboy vector law of addition current is not a vector. It is tensor.

charge 

Accelerated mation > Electromagnetic

Electric Current: It is defined as rate of flow of charge

it been it between the book of the book of

Jan infinitional i= da = caloumb charge dt sec

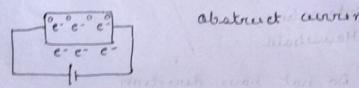
Ampère = Calocimb

lether one coloumb charge is flows through a circuit in 1 sec, the amount of current is said to be one ampere.

Though current how direction and magnitude. It is not considered as overlan because it doesn't abey vector law addition.



## Resistance (12)



abstruct awant

The property of resistance is to obstruct the flow ab electric current it is said to be electrical resist -ance.

CORRENT CLEOTRICITY

Unit - ahm Resistance, R= V

Jan any conductor of length i and area "A". The resistance is given by R= 9 &

Submigarious p - Resistivity

p = m = Resistivity. proved on alone? & have gabe at 13 i dreamed sented?

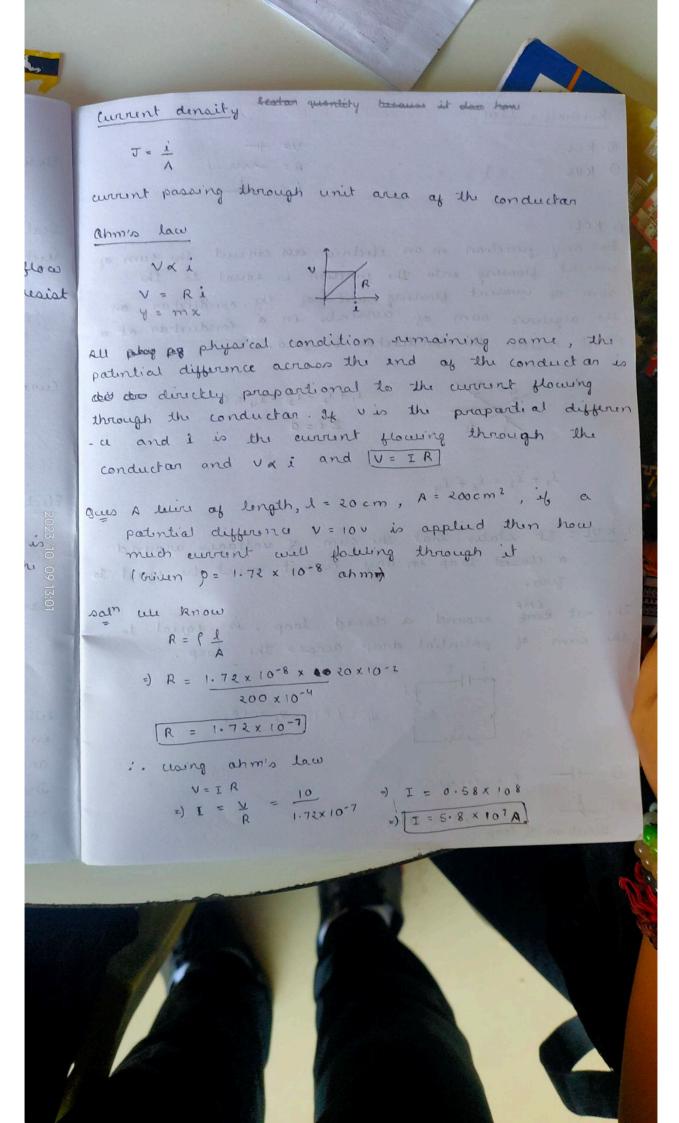
ques A wire of length (1) has resistance R this wire is structed to town the original length, find the new resistance.

1 421 -Rai Vi = Vb

=) Ax1 = A'x1'

=) A1 = A' x 21

= 4R





## Kirchaff's law

@ KCL

@ KVL

R= -MM\_ material all in acro line 1 = 10 - 1

0 KCL

Fan any junction in an electric con circuit the sum of current flowing into the junction is equal to the sum of current flowing out of the conductor or the algebraic sum of currents in a conductor at a paint inside the conductor is zero. a red bushes of is but out a one named to

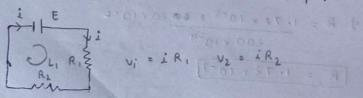


i, +12 -13 -14 = 0 Ei=0

 $i_1 + i_2 = i_3 + i_4$   $i_2 + i_3 + i_4$   $i_3 + i_4 + i_4 + i_5 + i_4$   $i_4 + i_5 + i_5 + i_4 + i_5 + i$ 

@ KVL: 9t states that the sum of valtages around a clased loop in any circuit must be equal to zero.

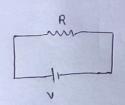
The net forms around a closed loop, is equal to the sum of patential drap across the loop.



€ is (+) we

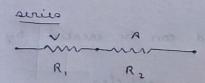
- LATALIR is 1+ lue Direction of loop





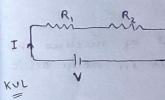
- 1 Resistance
- 1 Valtage
- 3 Current

Parallel R2



series

connection

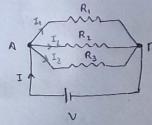




V= IR, + IR2

$$R = R_1 + R_2$$

Parallel connection





Parallel combination

Equivalent circuit

KCL I = I, + I<sub>2</sub> + I<sub>3</sub>

=) 
$$\frac{1}{R} = \frac{1}{R} + \frac{1}{R} + \frac{1}{R}$$

$$=) \begin{array}{|c|c|c|c|}\hline \bot & = & \bot & + & \bot & + & \bot \\ R & R_1 & R_2 & R_3 & R_3$$