Rohit S

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EDUCATION

Amrita University

Coimbatore

Electronics and Instrumentation engineer, B-Tech

2016-2020

Suguna PIP School

Coimbatore

CBSE 12th mark-415/500

2012-2016

EXPERIENCE

Yokogawa India Ltd

May 17 2021 - July 27 2022

Bangalore

Automation Engineer

- Was a project head for mangalam Organics plant expansion project where we co-ordinated with hardware, software and graphic team to build an automated control system
- Was involved in FAT's where we tested control systems for harsh factory uses for companies like IOCL and Atul
- Used programing languages like C to create sequencing and interlocks and intergrated it with Centum VP(DCS software

Star Communication

June 2020-April 2021, Aug 2022-present

Network Engineer

Karur

- Was involved in establishing partnership with Airtel and BSNL Which increased the revenue by 30 percent
- Assess and troubleshoot internet speed problems brought by Customers
- I have worked in management and technical fields involving server maintenance, OLT and Router configuration, Splicing and also Marketing

PROJECTS

Airline Reservation System | Java Swing, SQL, DSA

May 2023 - June 2023

- * Developed a full-stack windows application using java swing with SQL for the backend, Integrated both using JDBC driver
- * Implemented Automatic Id generation for Customer, flight and tickets
- * Created Login Authentication system and verified using admin credential in database
- * Used multiple relational tables in backend to query, add customers, book flights and generate tickets displayed in the respective GUI windows

Sudoku Solver | Java Swing, DSA

May 2023 – June 2023

- * Created a Sudoku Solver application with reset, show/hide solution and check correct moves functional buttons
- * Created and designed action listeners for 9*9 matrix buttons in JFrame

Compressor Decompressor | Java Swing, DSA

May 2023 – June 2023

- * Created a lossless compression and decompression application which can compress an decompress all types of data to reduce storage space
- * Have utilized the GZIP input and output stream class in java swing to compress and decompress the file

Optimized Viterbi decoder | C++, Xilinx Vivado HLS, verilog

Aug 2019 – May 2020

- * Coded the algorithm in C++ and converted to verilog based code for optimization
- * Generated RTL code and Optimized using Vivado HLS tool and created test bench which can be mapped in FPGA efficiently

TECHNICAL SKILLS

Programming and DataBase: Java, C/C++, JavaScript, HTML/CSS, SQL

Developer Tools:VS Code, IntelliJ, GitHub, NetBeans

Coursework: Design and Analysis of Algorithms, Object Oriented Programming, Operating Systems, Data Structures, Data Rose Management Systems

Structures, DataBase Management System