

# More Scalable LTL Model Checking via Discovering Design-Space Dependencies (D<sup>3</sup>)

Rohit Dureja and Kristin Yvonne Rozier

IOWA STATE  
UNIVERSITY

+



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Thanks to NSF CAREER Award CNS-1552934 for supporting this work.

# What is a Design Space?

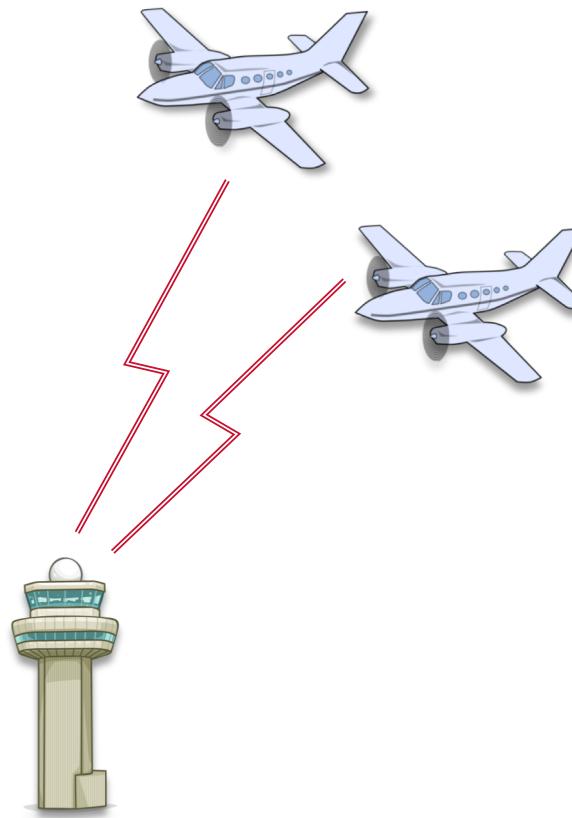
# Airspace Allocation

**Problem Statement:** Design of allocation policies to ensure all airborne agents are safely separated.



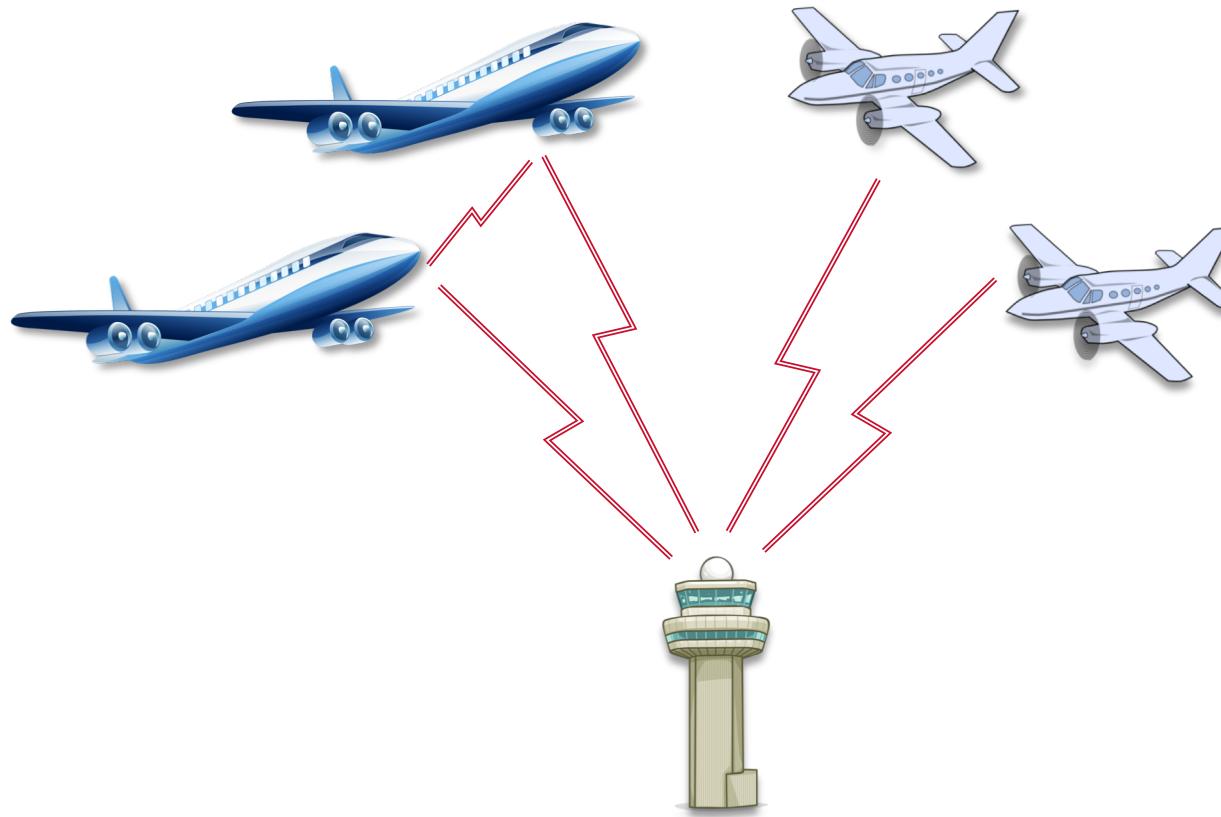
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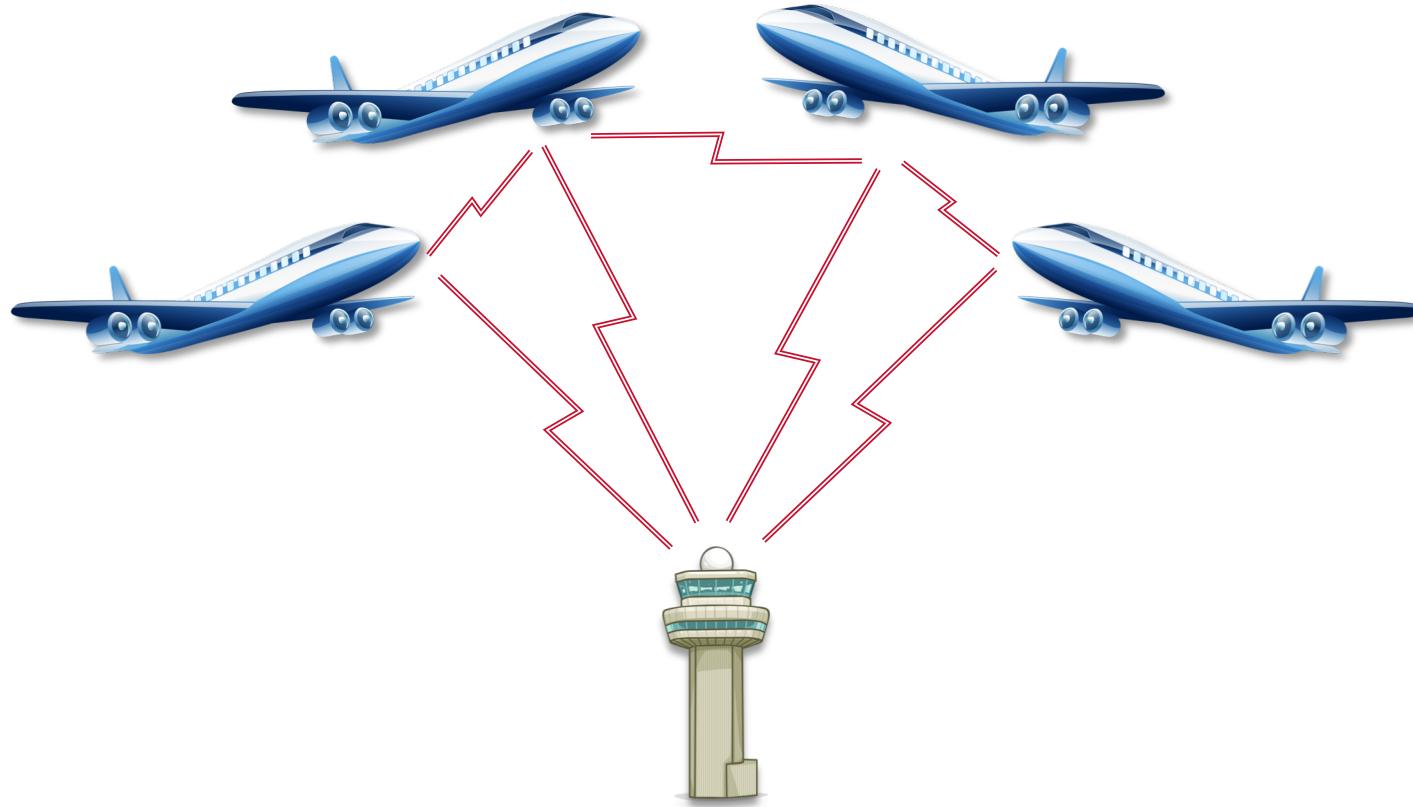
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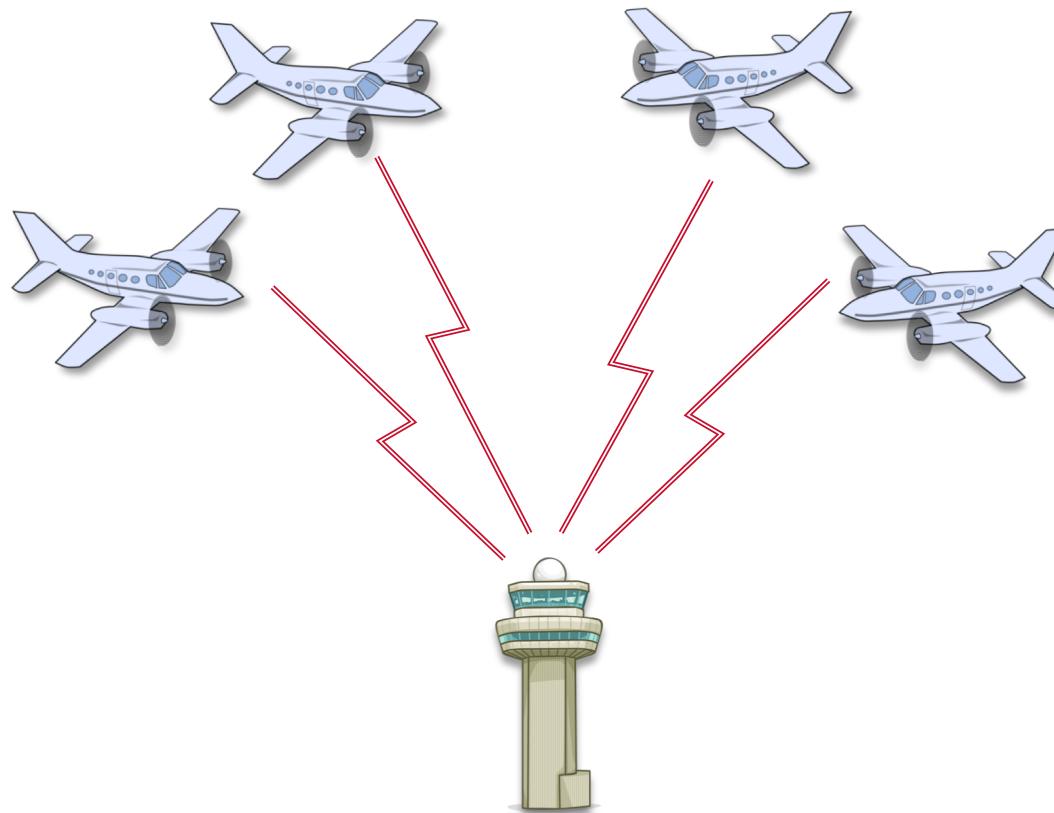
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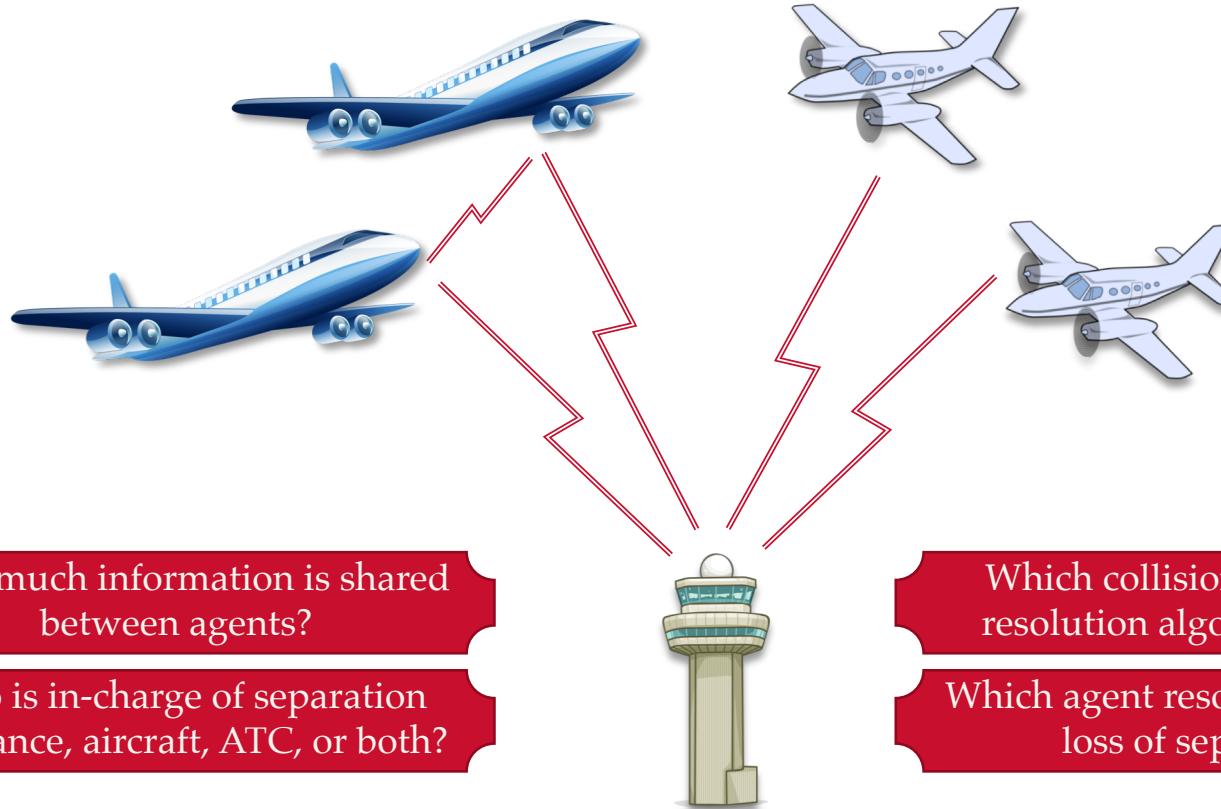
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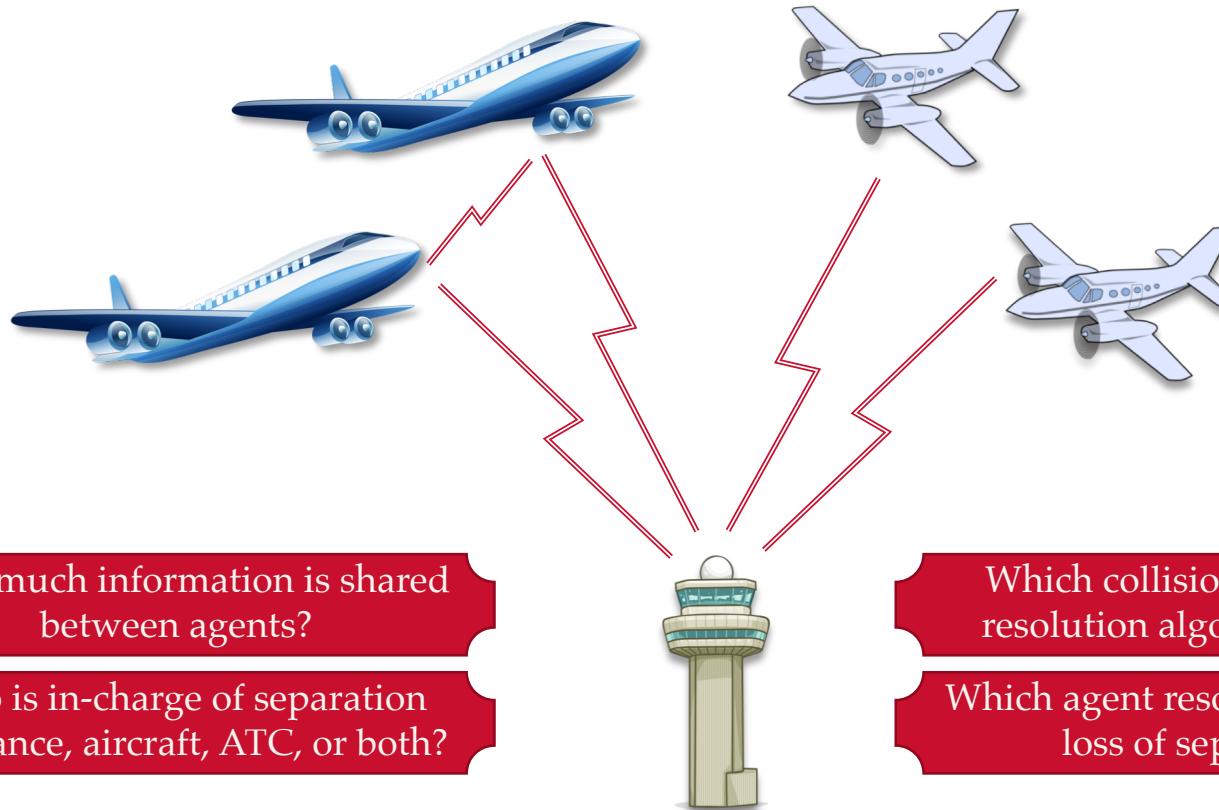
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Lots of Design Choices!

# What is a Design Space?

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Set of Design Choices for a System.

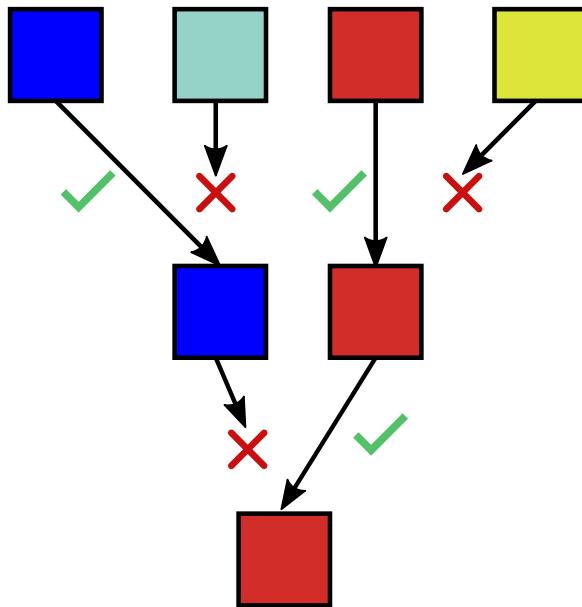
# Design Space Analysis

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Complex systems are often modeled as design spaces.

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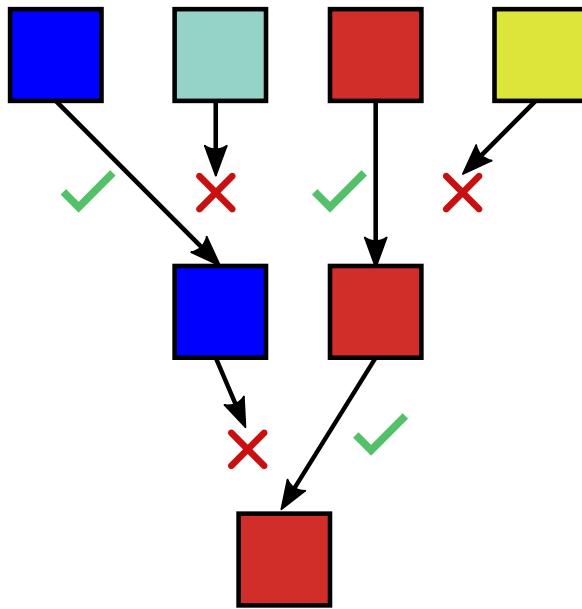
Complex systems are often modeled as design spaces.



Alternative comparison via design space exploration

# Design Space Analysis

Complex systems are often modeled as design spaces.



Alternative comparison via design space exploration

## Model Checking!

# Modeling Design Spaces

# Modeling Design Spaces

- Classical Method
  - *every design choice leads to a separate model*

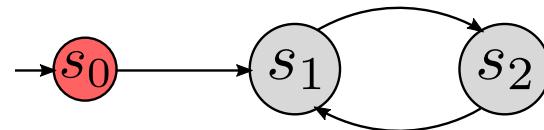
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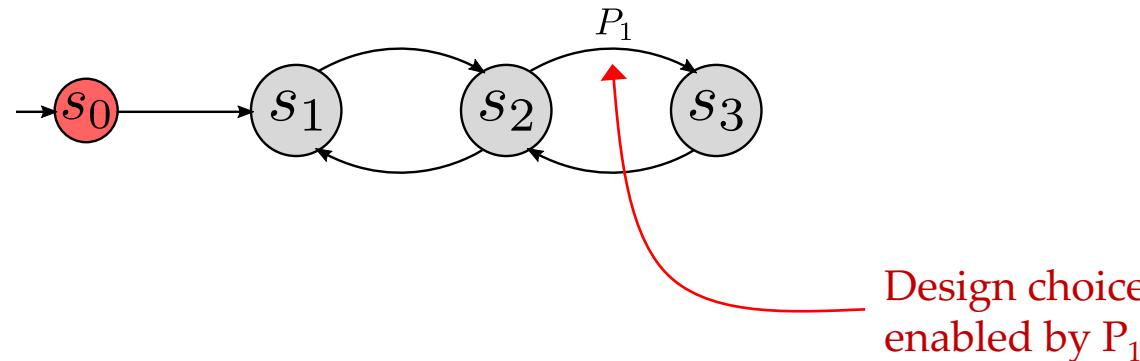
## Simple Example



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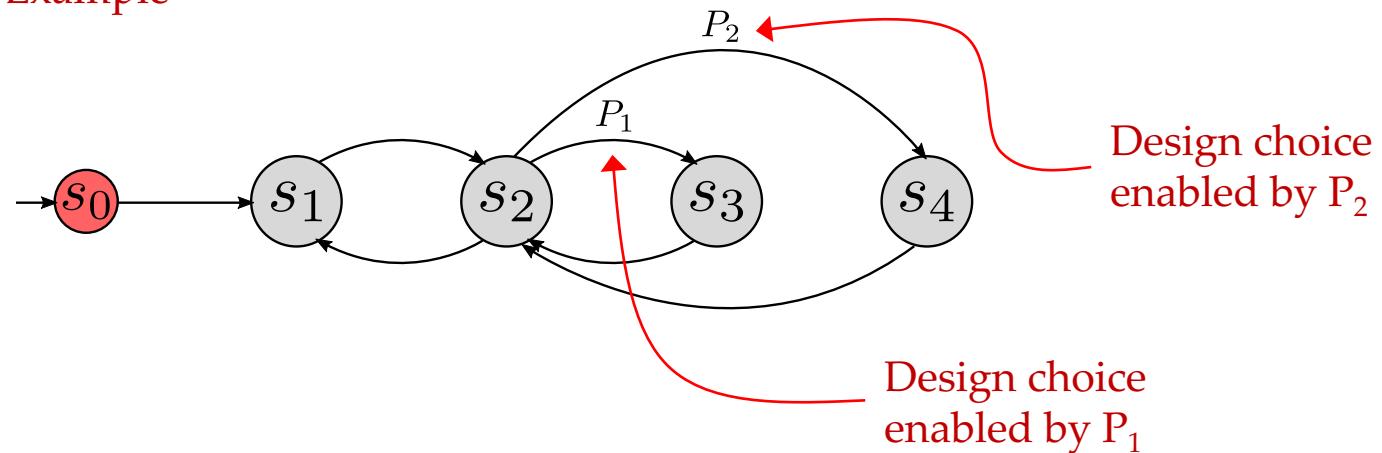
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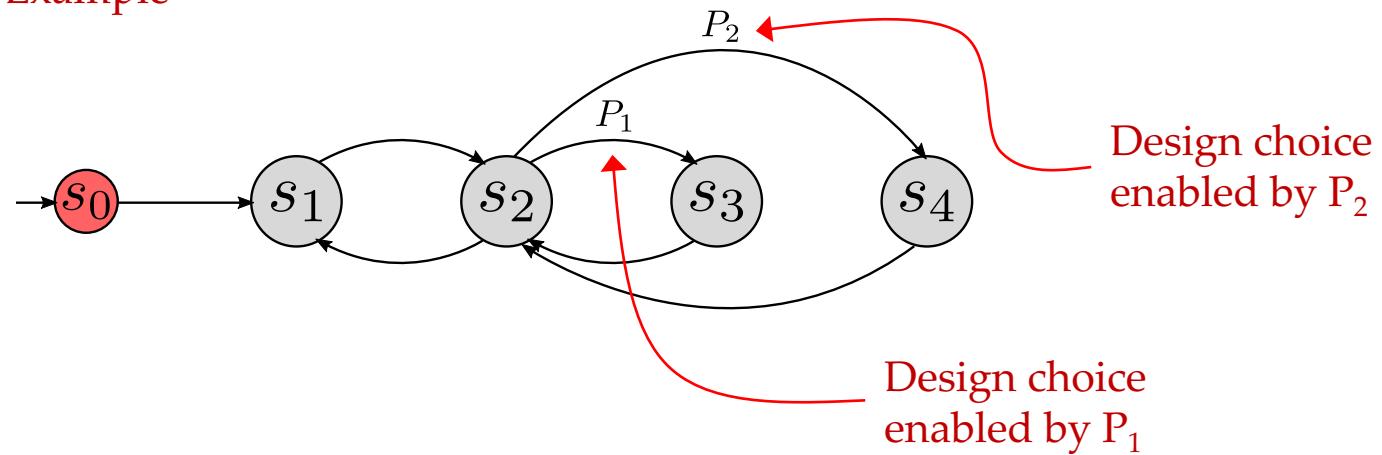
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# Modeling Design Spaces

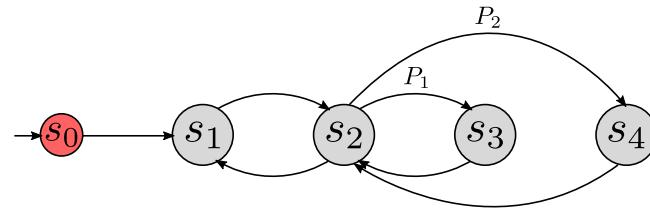
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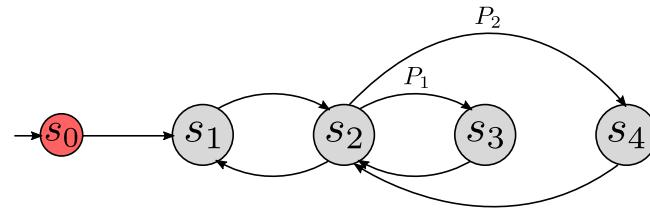


Combinatorial Transition System  
(CTS)

# Model Checking Combinatorial Systems

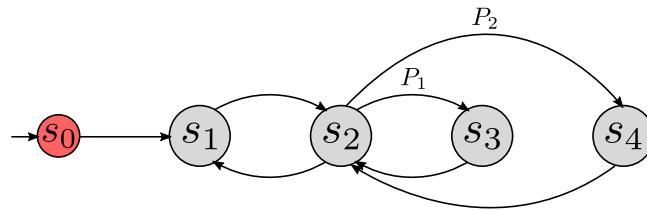


# Model Checking Combinatorial Systems



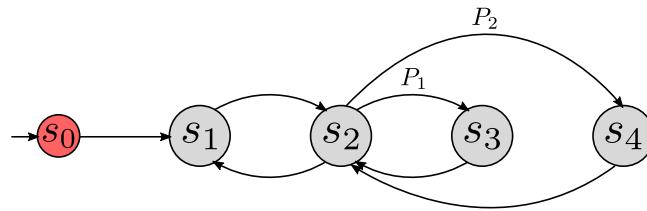
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  - *state-space explosion, counterexample belongs to single design choice*

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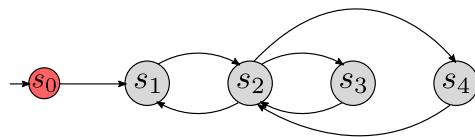


- Parameters as variables
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- Parameter configurations
  - *every parameter configuration leads to an individual model*

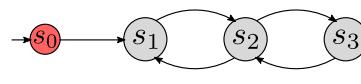
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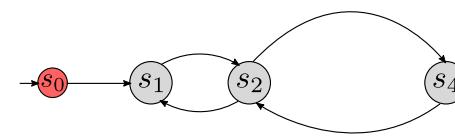
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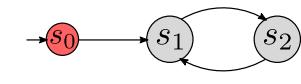
$$(P_1 = \top, P_2 = \top)$$



$$(P_1 = \top, P_2 = \perp)$$



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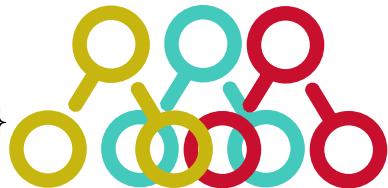


$$(P_1 = \perp, P_2 = \perp)$$

# Model Checking Design Spaces

Model Set

$$\mathcal{M} = \{M_1, \dots, M_n\}$$

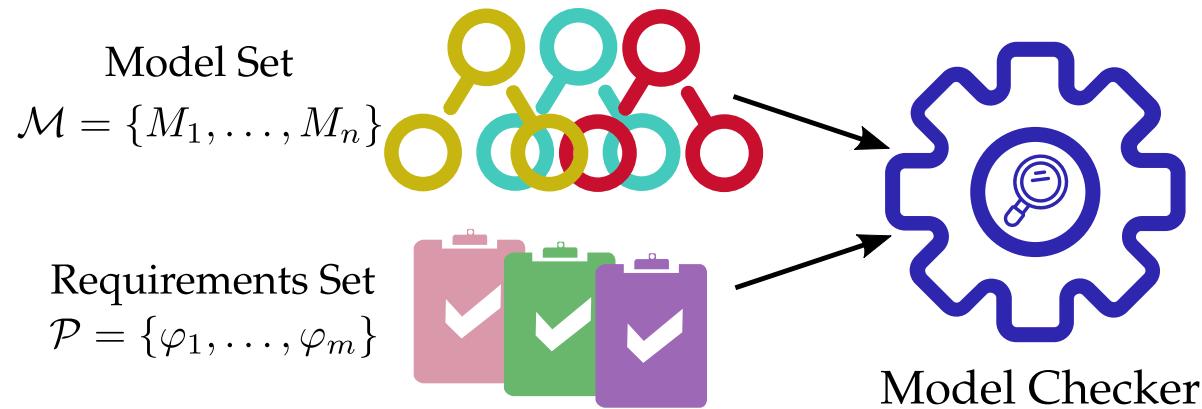


Requirements Set

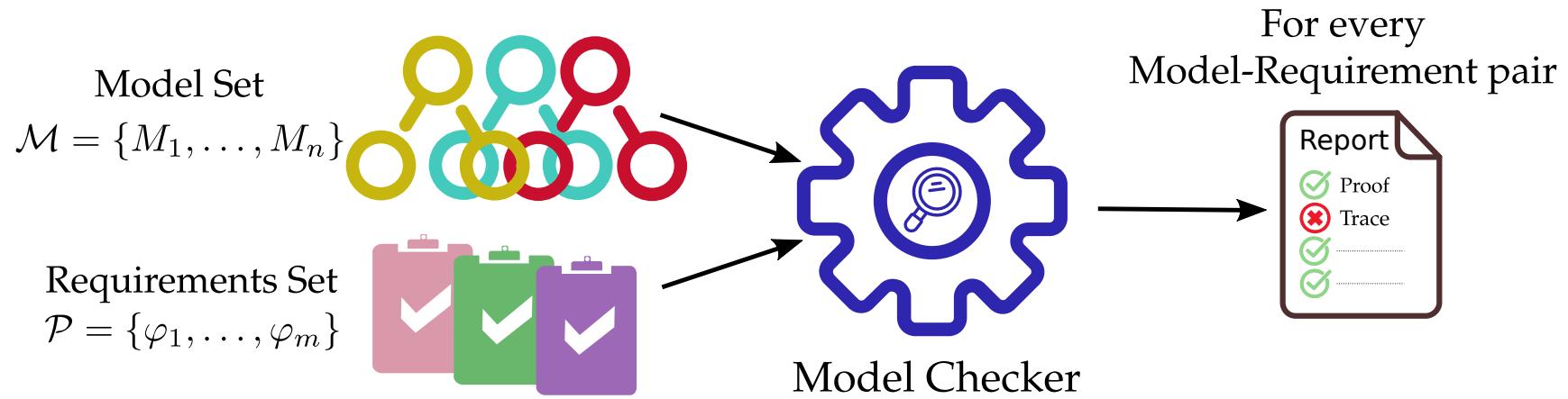
$$\mathcal{P} = \{\varphi_1, \dots, \varphi_m\}$$



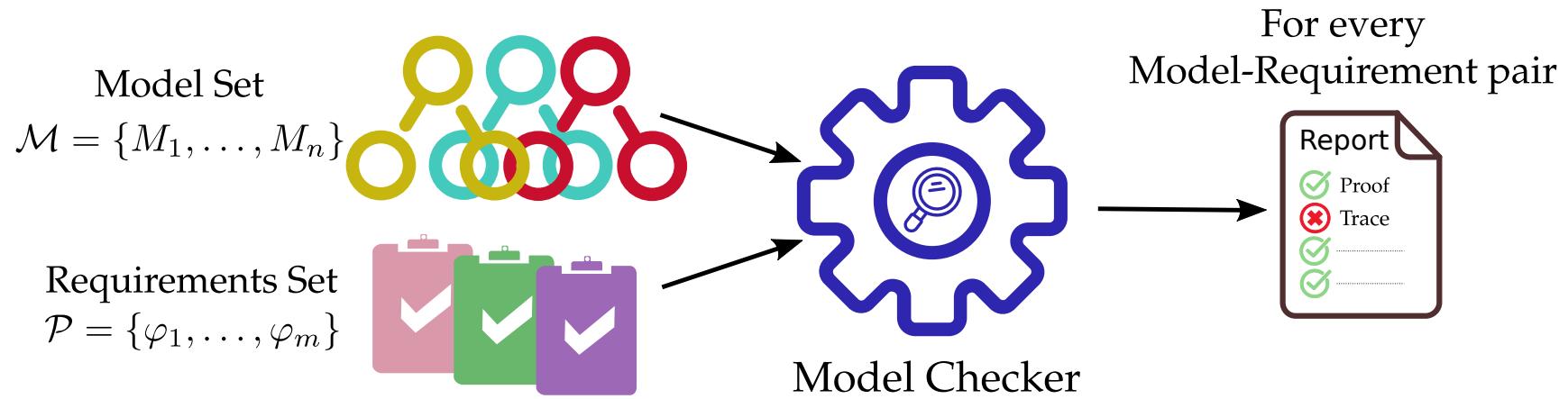
# Model Checking Design Spaces



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**Our Goal**  
Make model-checking for design-spaces more scalable

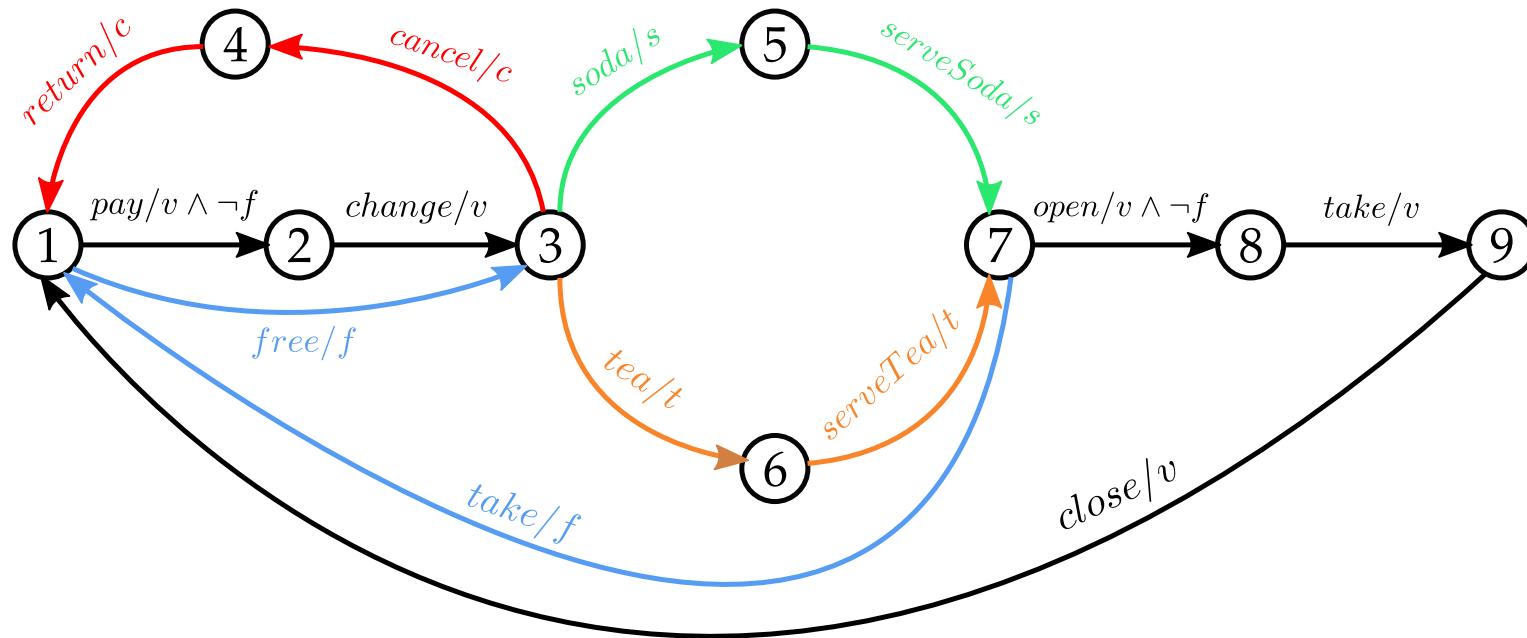
# Related Work

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## 1. Software Product Line verification

(Resenmüller et al., 2010; Schirmeier et al., 2009; Classen et al., 2010, 2011, 2013; Dimovski et al., 2015)

Many Models  
Many Requirements

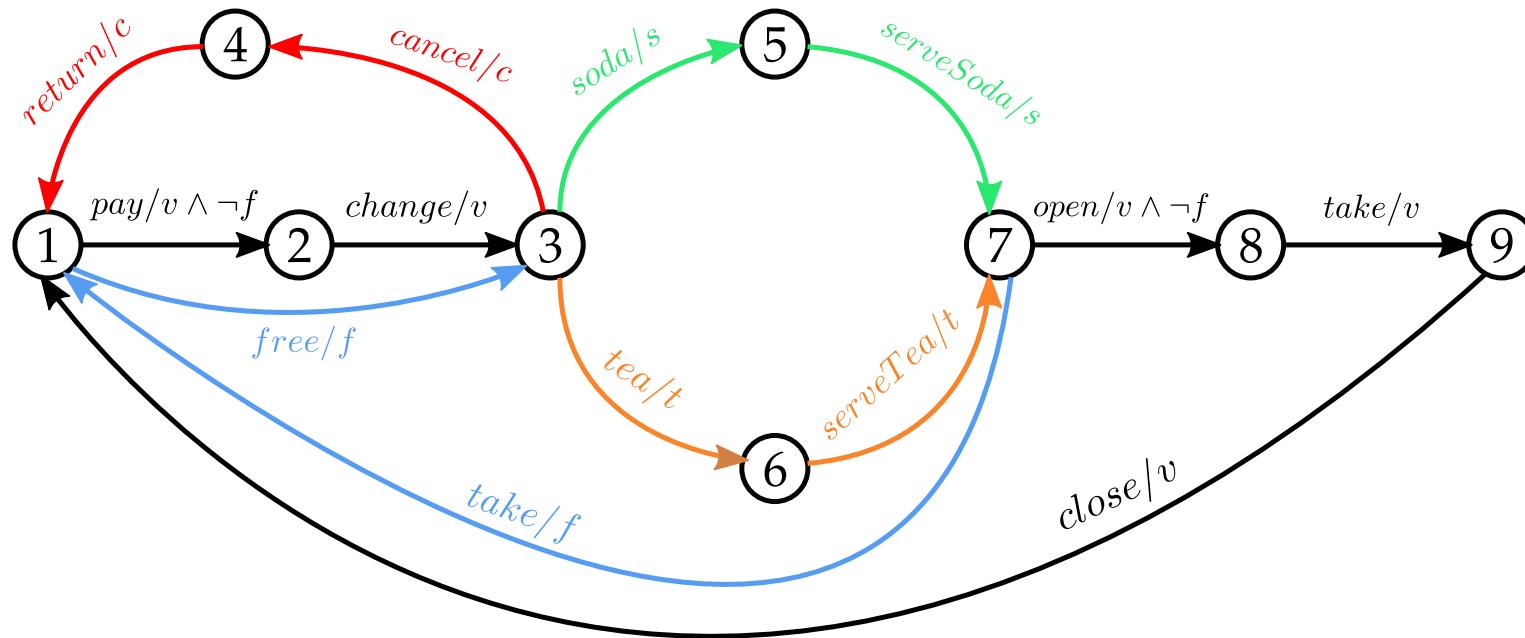


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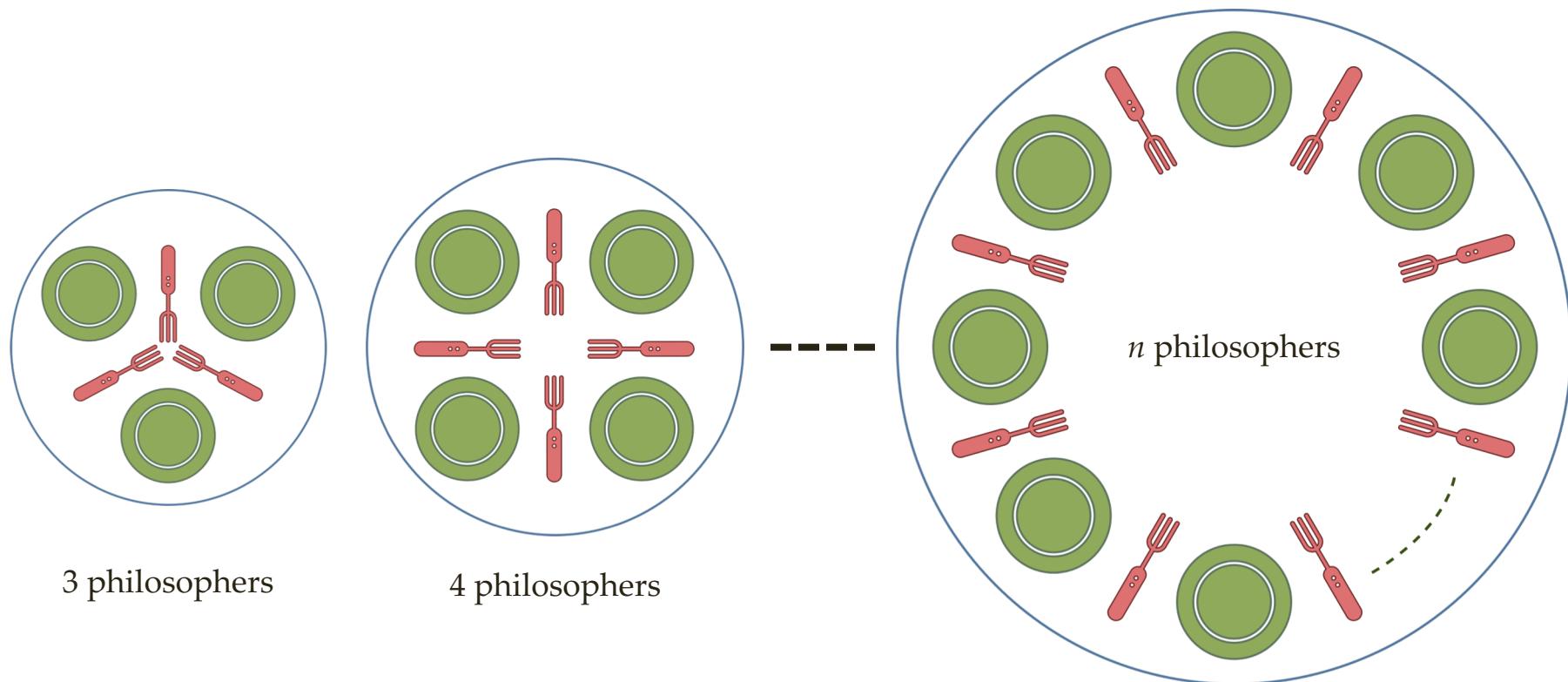


We focus on a more general approach that works with off-the-shelf model checkers

# Related Work

## 2. Parameterized model checking (Emerson and Kahlon, 2000)

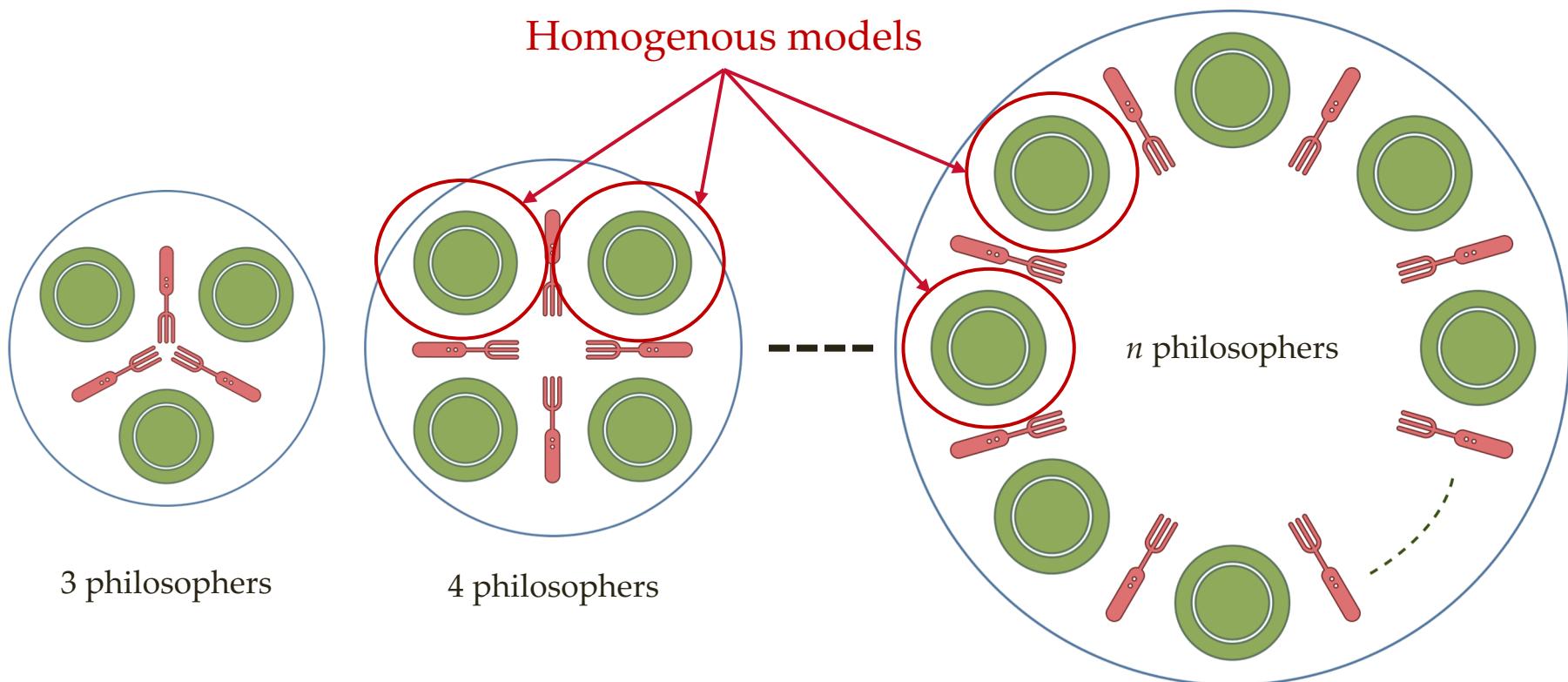
Many Models  
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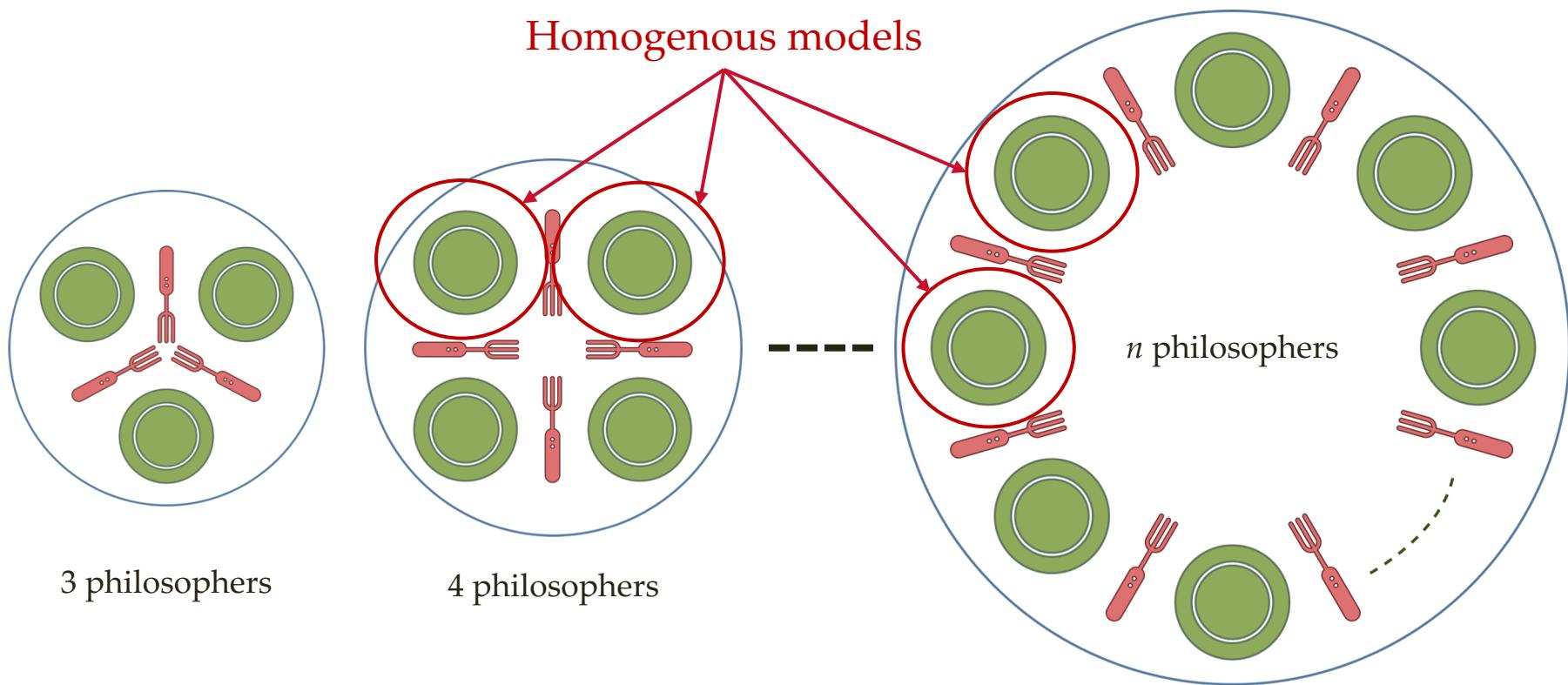
Many Models  
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# Related Work

## 2. Parameterized model checking (Emerson and Kahlon, 2000)

Many Models  
One Requirement



We focus on heterogeneous and finite number of models

# Related Work

## 3. Multi-property model checking

(Cabodi et al., 2010, 2011, 2017; Goldberg et al., 2018; Khasidashvili et al., 2006; Khasidashvili and Nadel, 2012; Camurati et al., 2014; Dureja and Rozier, 2017)

One Model  
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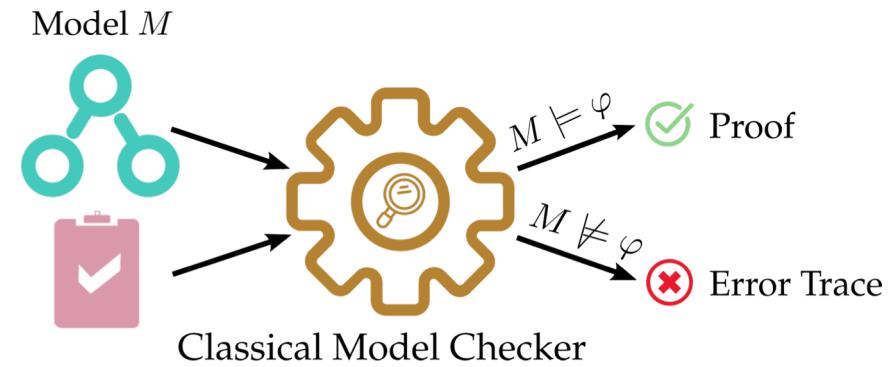
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One Model  
Many Requirements

Requirements

$\varphi_1$   
 $\varphi_2$   
 $\varphi_3$   
 $\varphi_4$   
 $\varphi_5$   
 $\varphi_6$   
 $\varphi_7$   
 $\varphi_8$

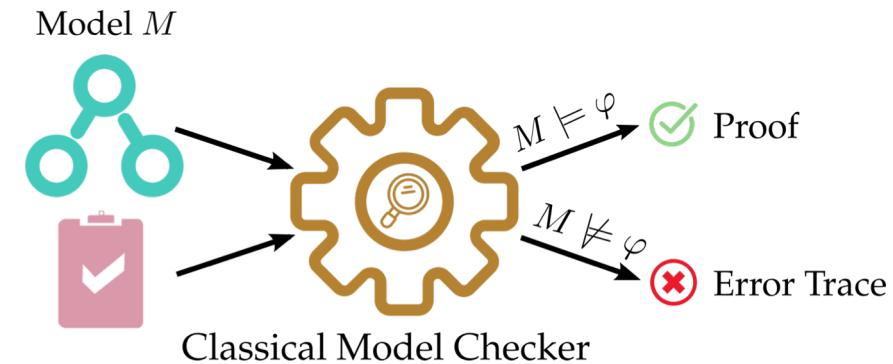
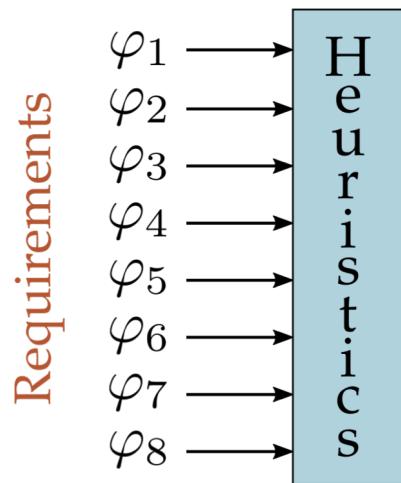


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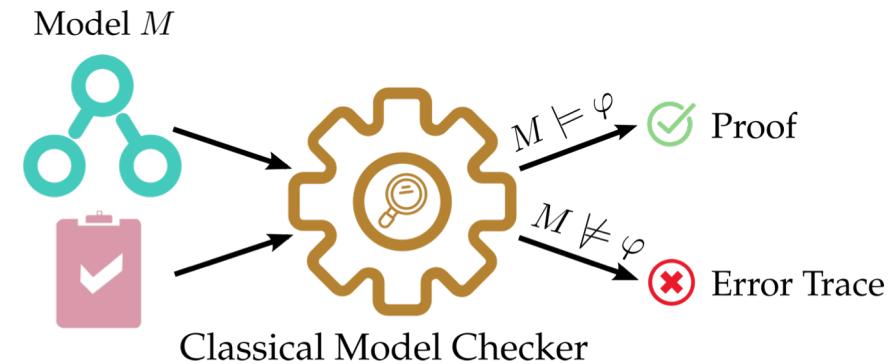
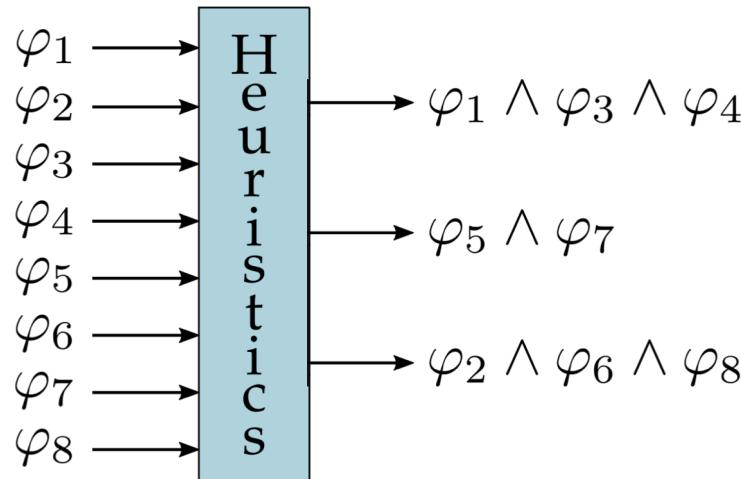
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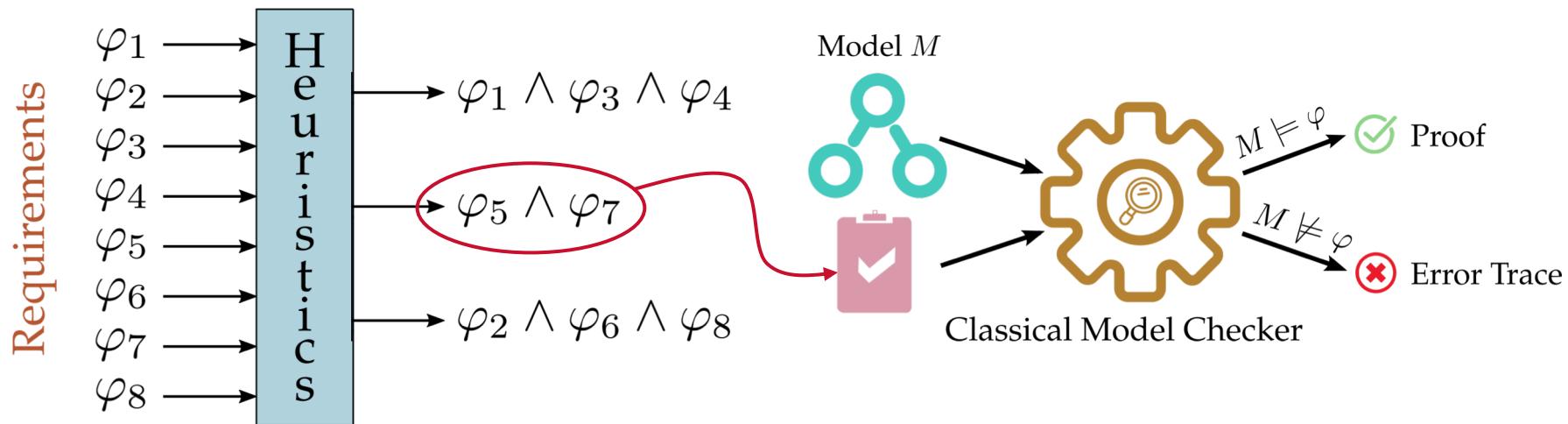


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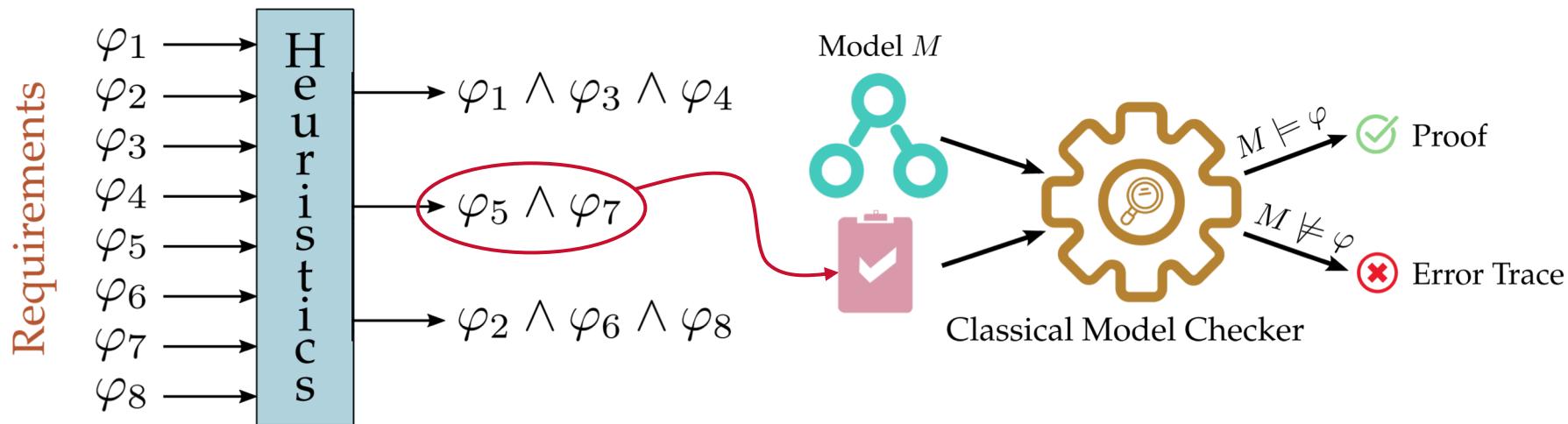


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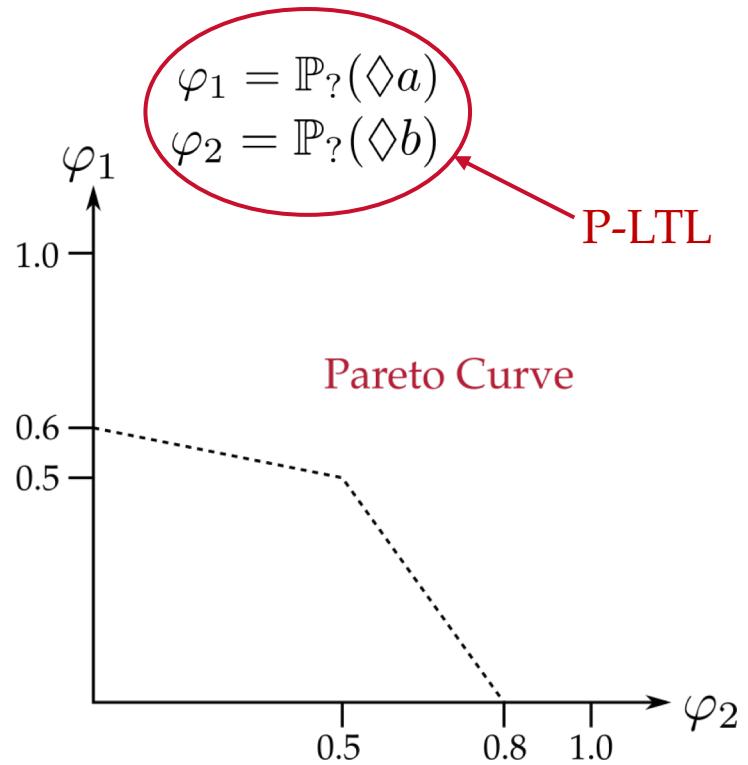
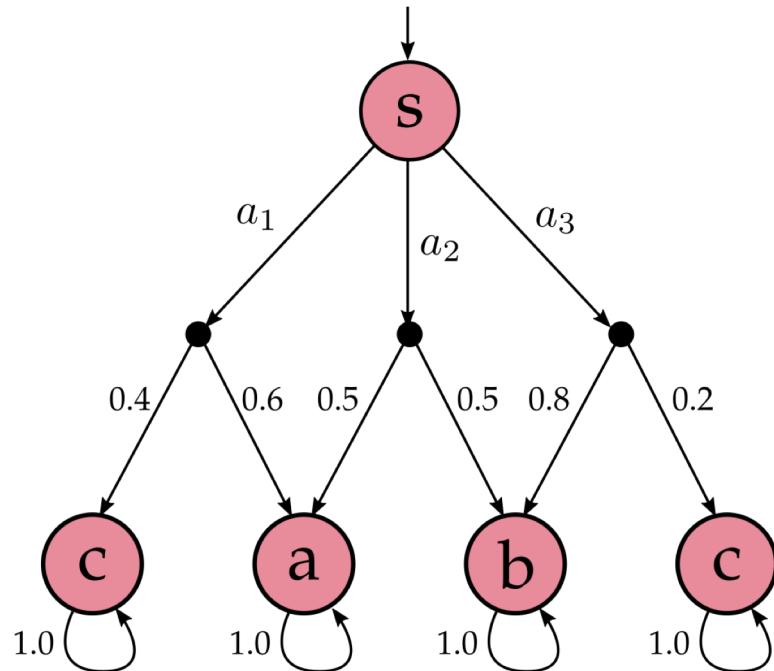
We focus on reducing the number of total model checking runs

# Related Work

## 4. Multi-objective probabilistic model checking

(Denhert et al., 2015, 2016, Baier et al., 2014, Etessami et al., 2007, Forejt et al., 2011, Kwiatkowska et al., 2013, Quatmann et al., 2016)

One Model  
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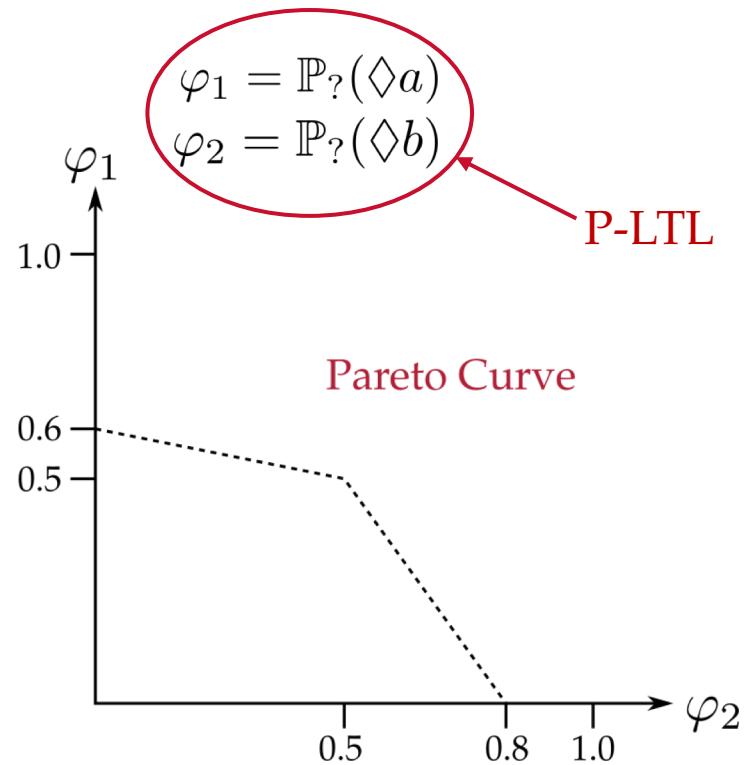
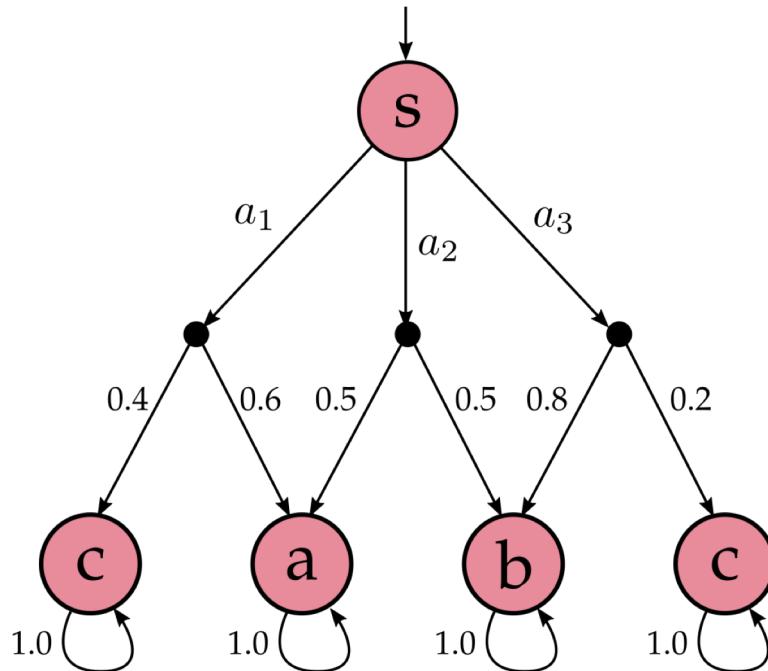


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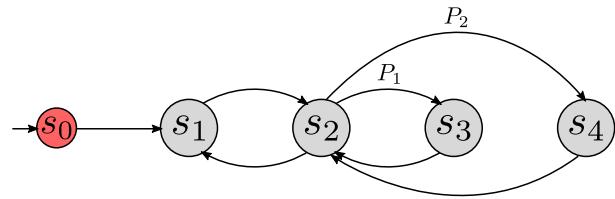
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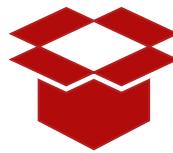
We focus on LTL/Invariant model checking with Boolean result.

# Discover Design-Space Dependencies ( $D^3$ )

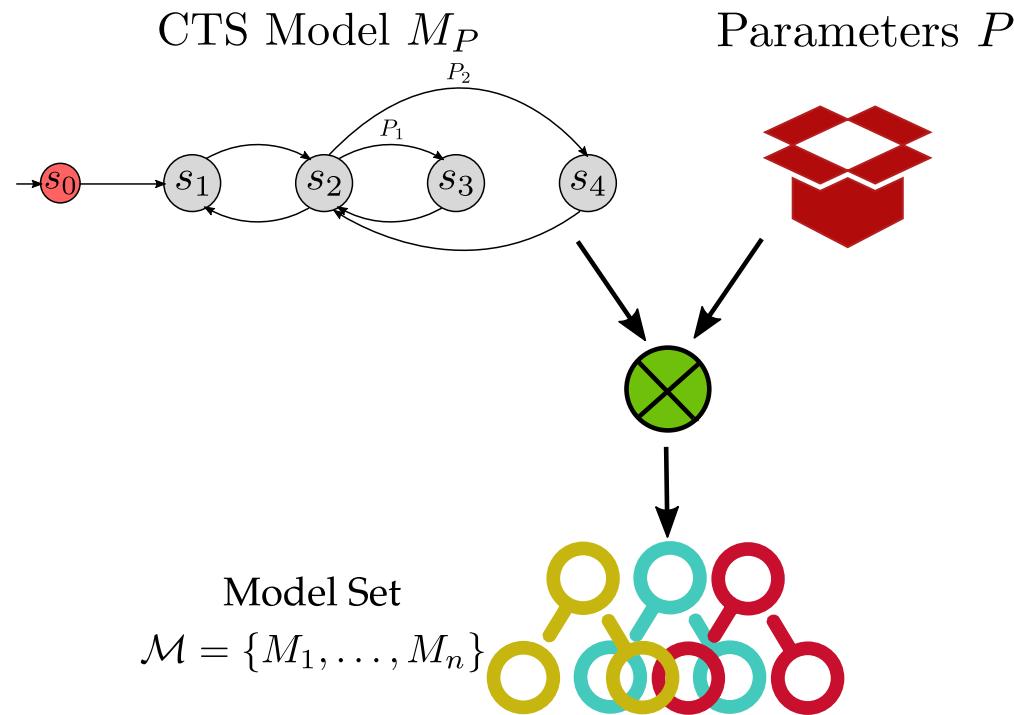
CTS Model  $M_P$



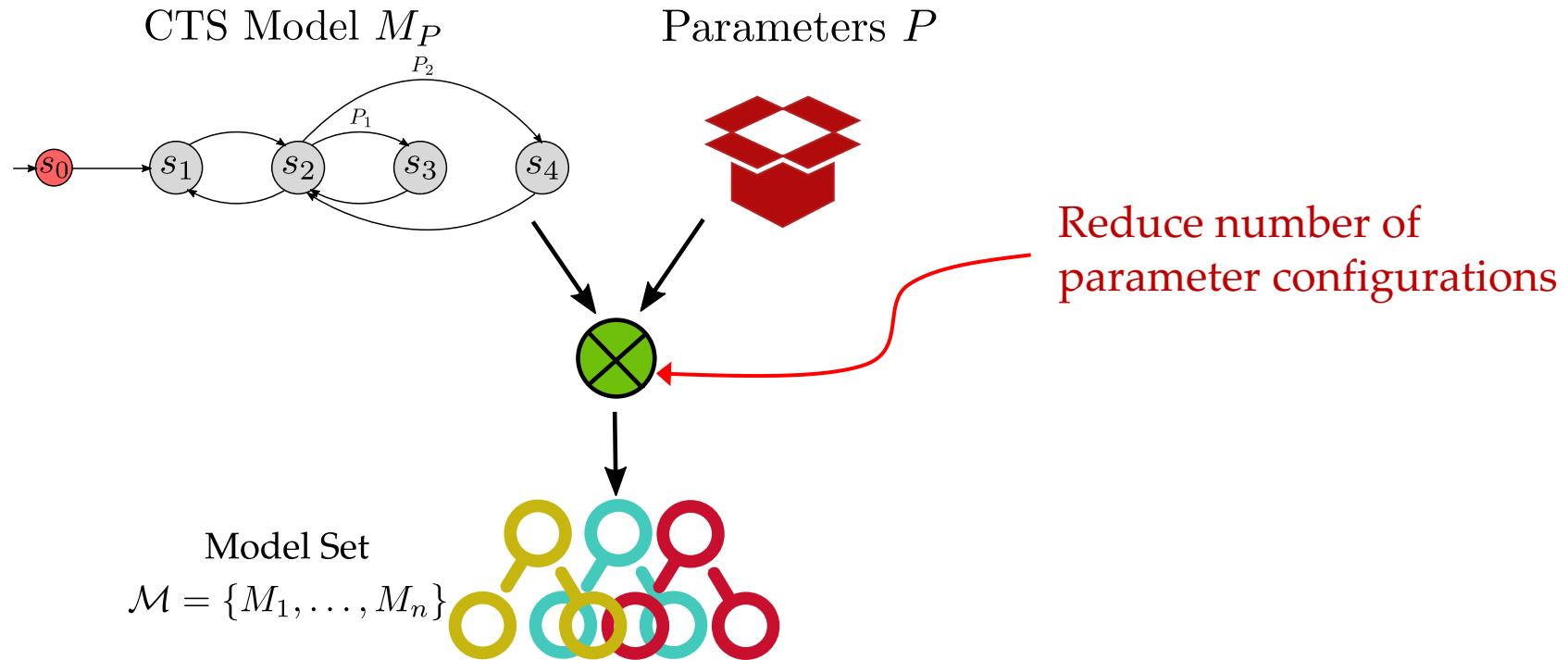
Parameters  $P$



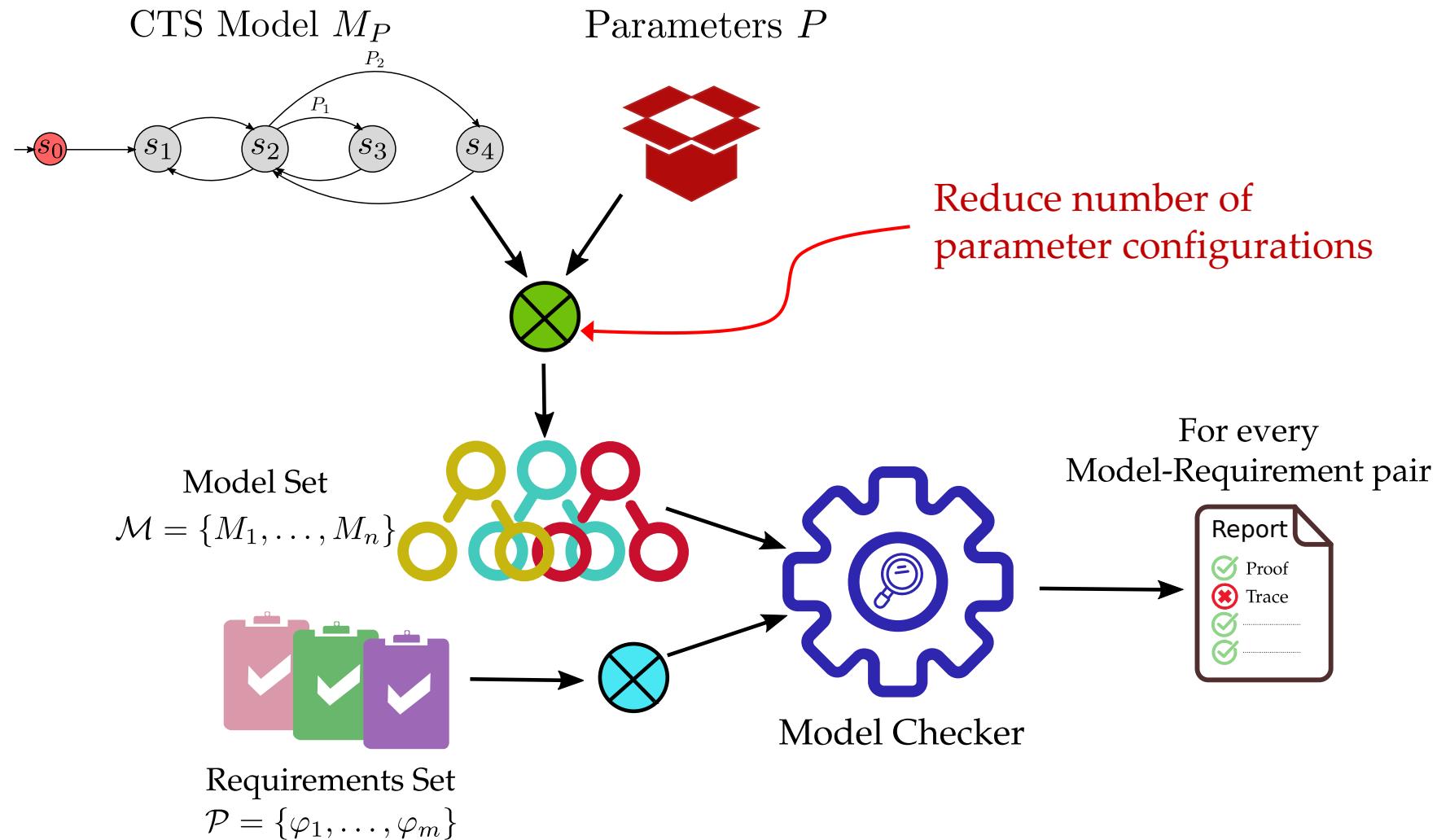
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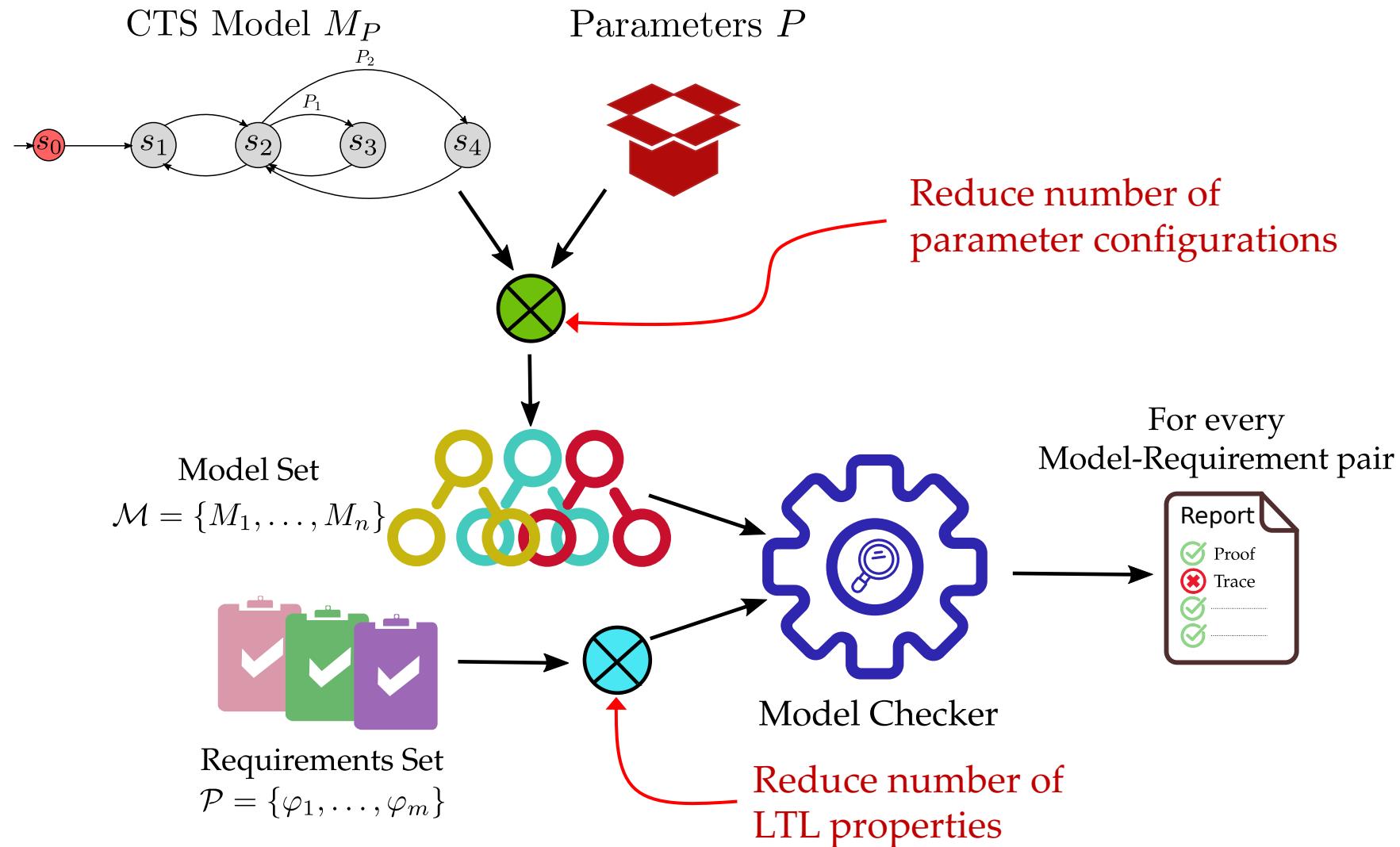
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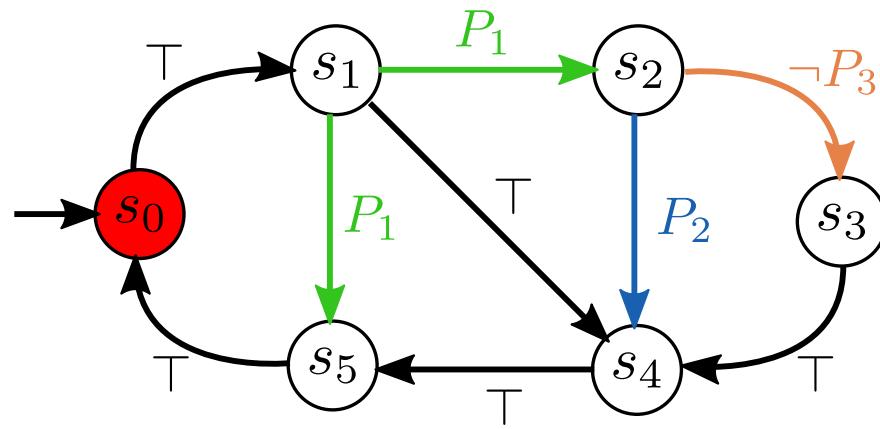
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# Parameter Configurations

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## Motivating Example

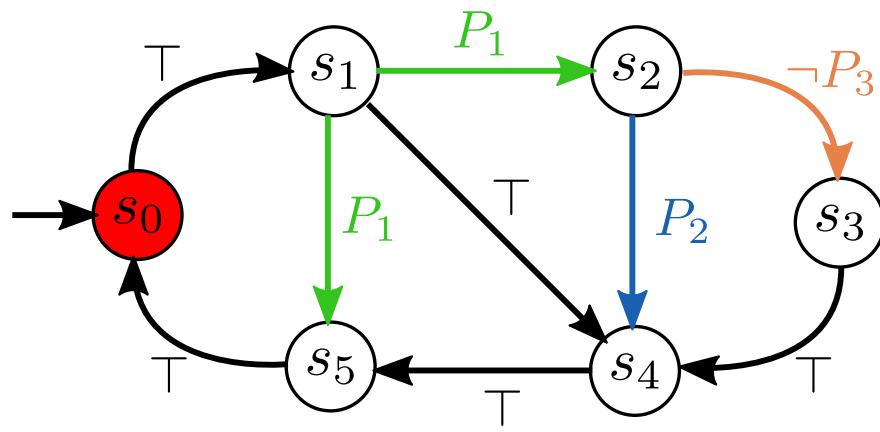
 $M_P$ 

Boolean parameters  $P_1$ ,  $P_2$ ,  $P_3$

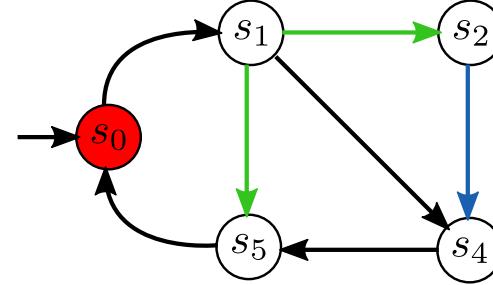
# Parameter Configurations

## Motivating Example

$M_P$



Boolean parameters  $P_1, P_2, P_3$

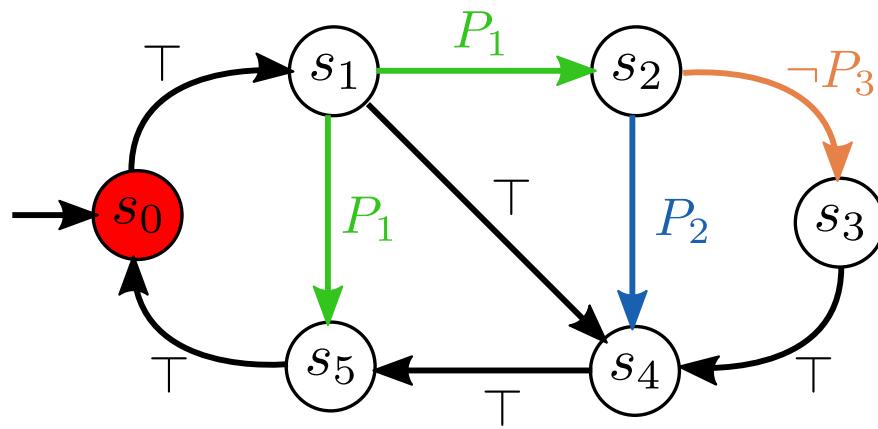


$$(P_1 = \top, P_2 = \top, P_3 = \top)$$

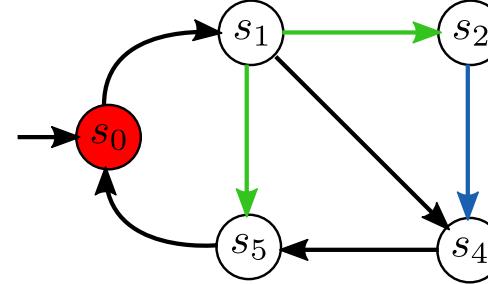
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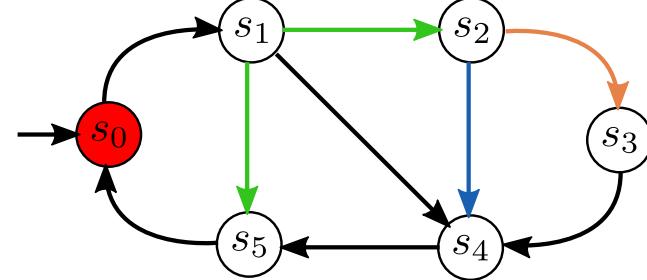
$M_P$



Boolean parameters  $P_1, P_2, P_3$



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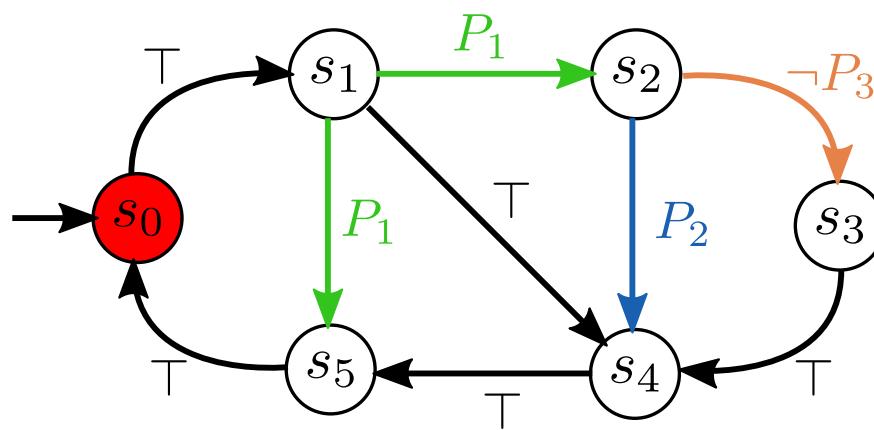


$$(P_1 = \top, P_2 = \top, P_3 = \perp)$$

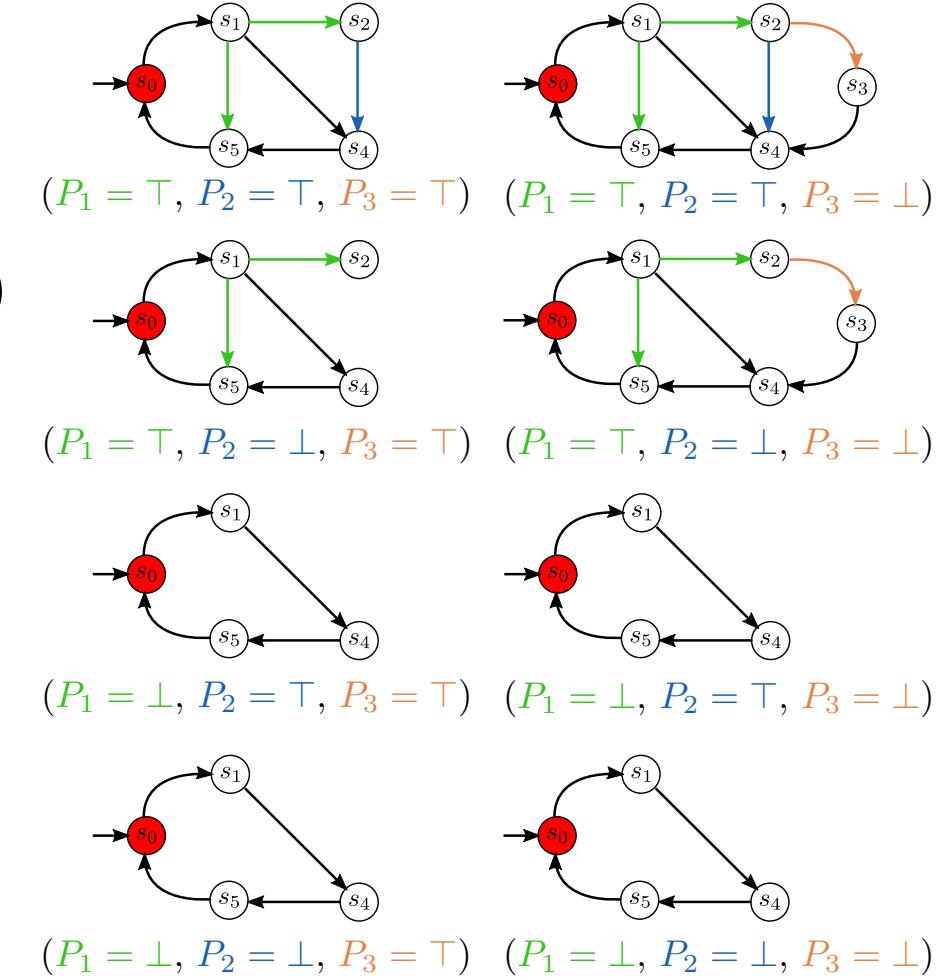
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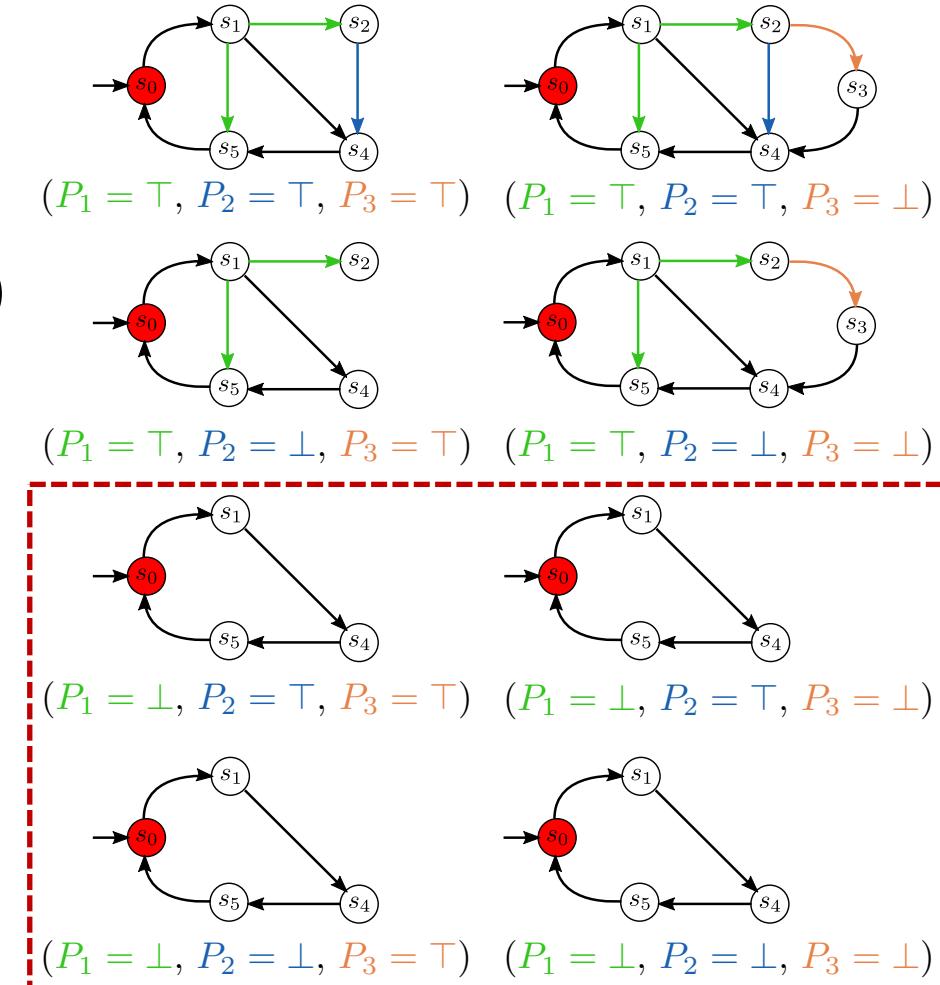
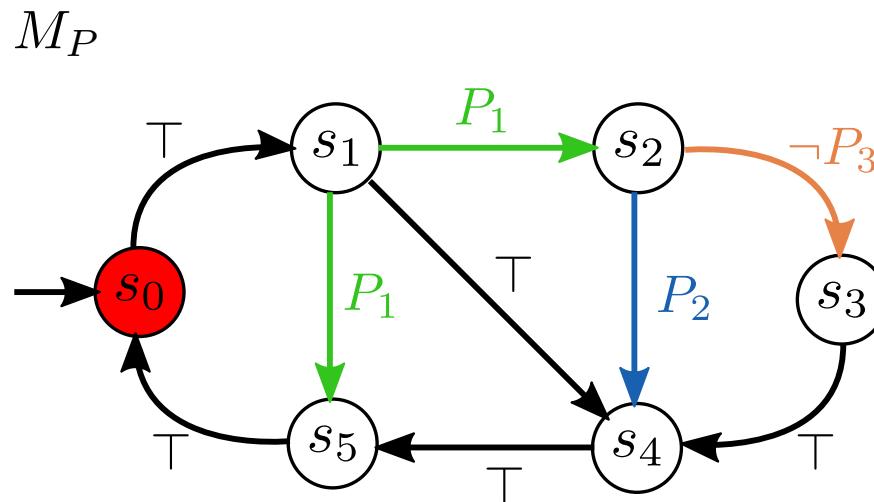


Boolean parameters  $P_1, P_2, P_3$



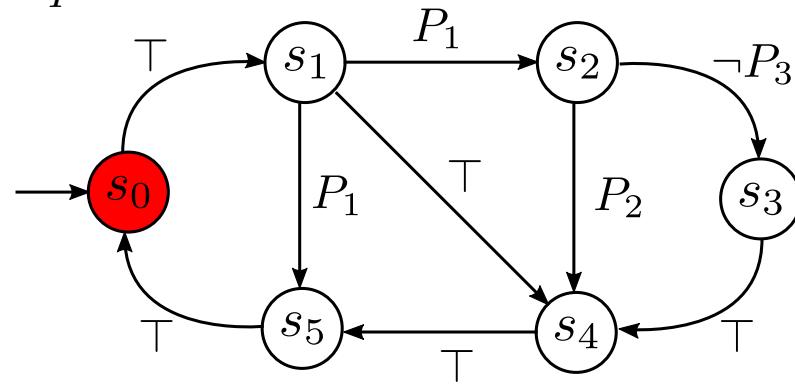
# Parameter Configurations

## Motivating Example



# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

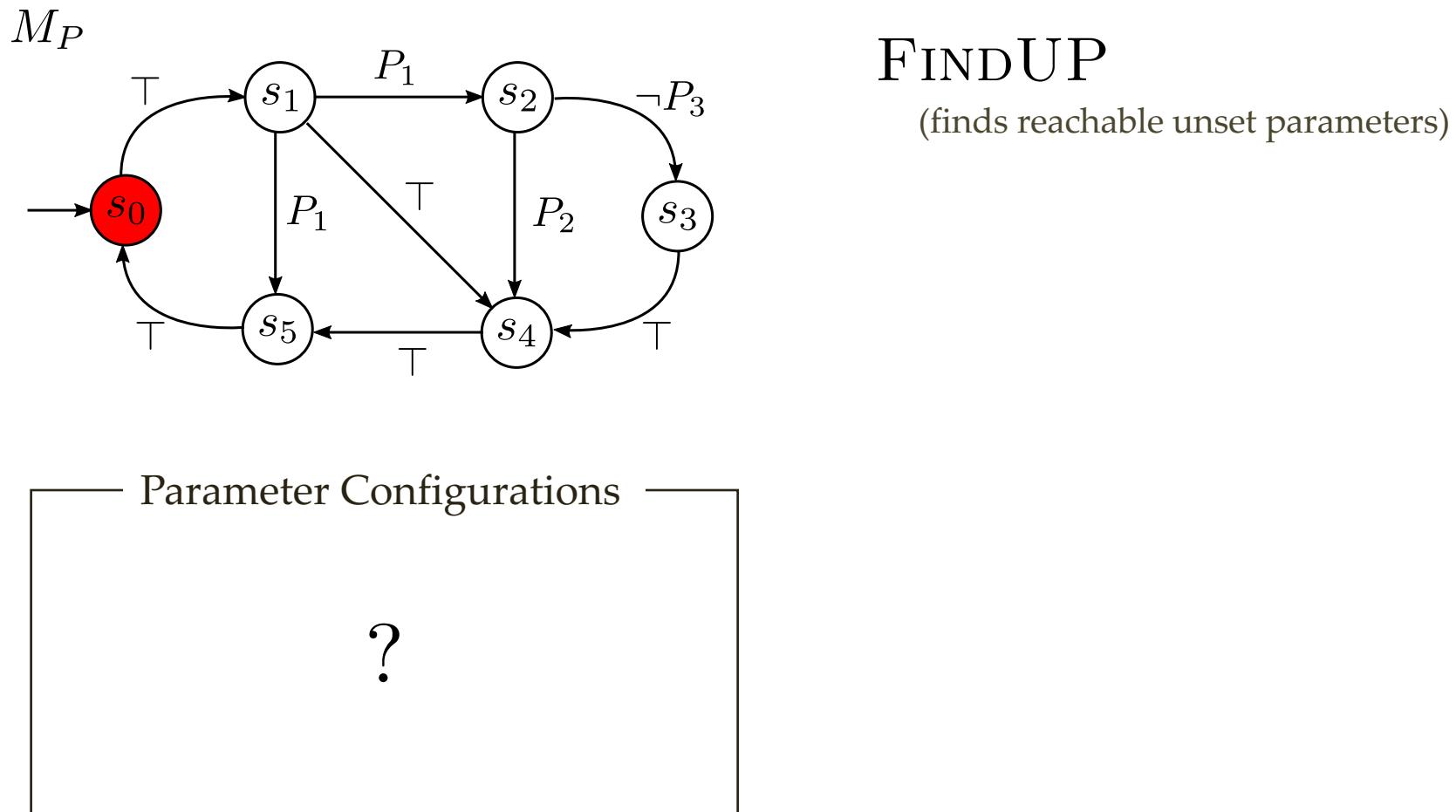
 $M_P$ 

Parameter Configurations

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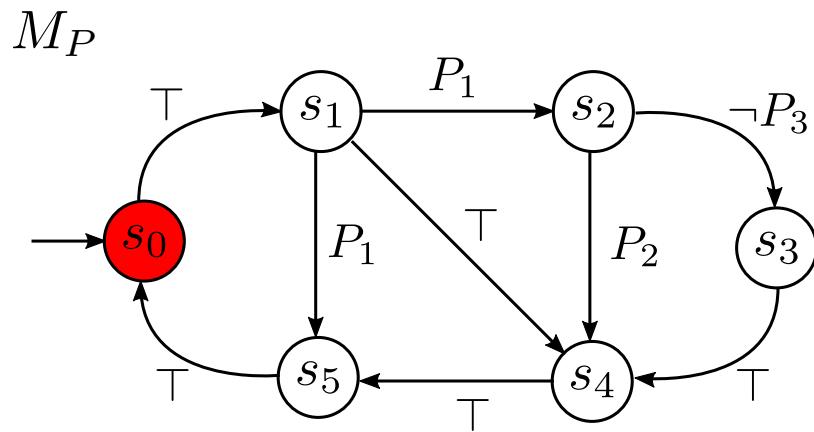


FINDUP

(finds reachable unset parameters)

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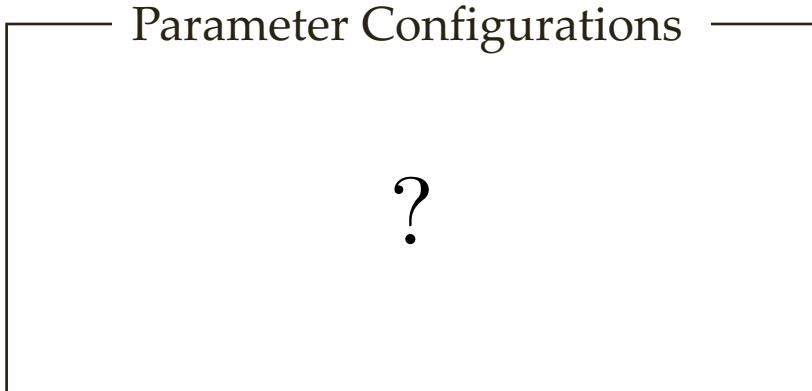
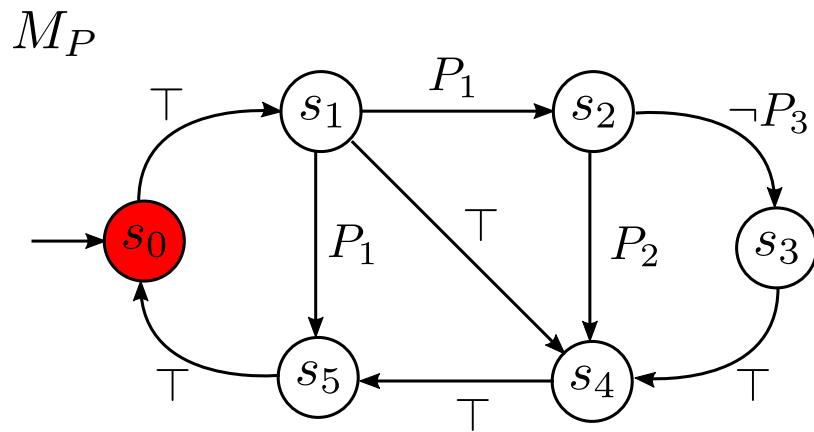
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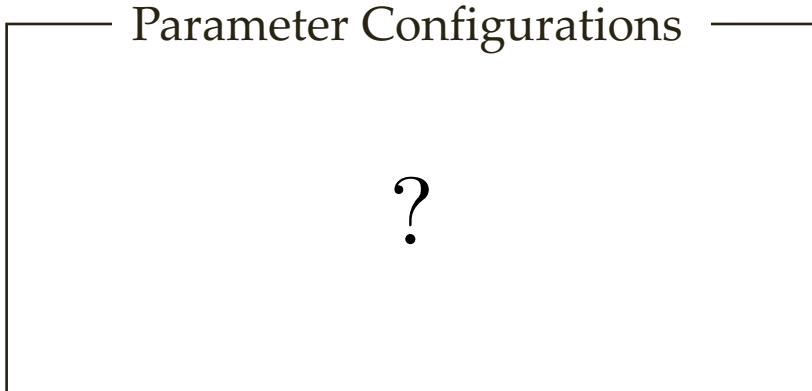
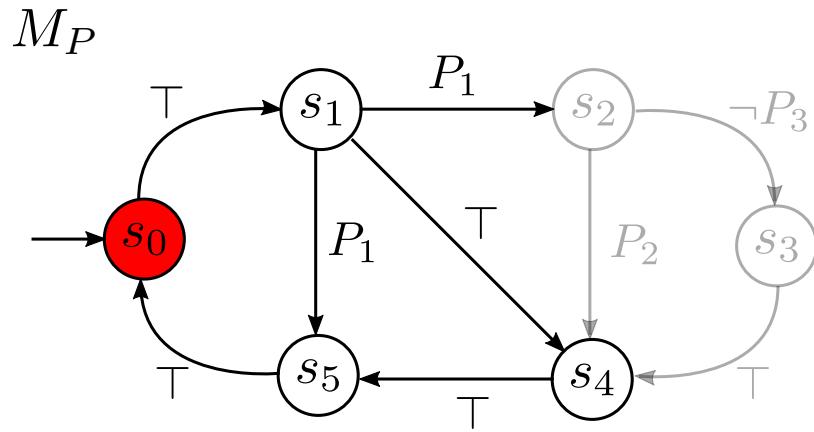
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Unset Parameters

$P_u = \text{FINDUP}(M_P, \hat{c})$

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Configuration

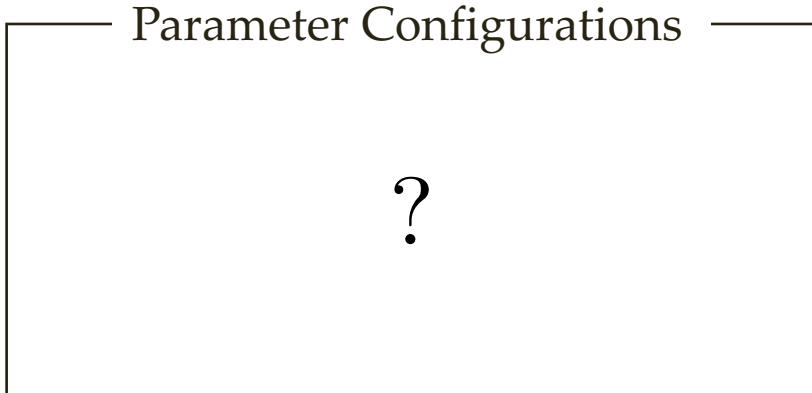
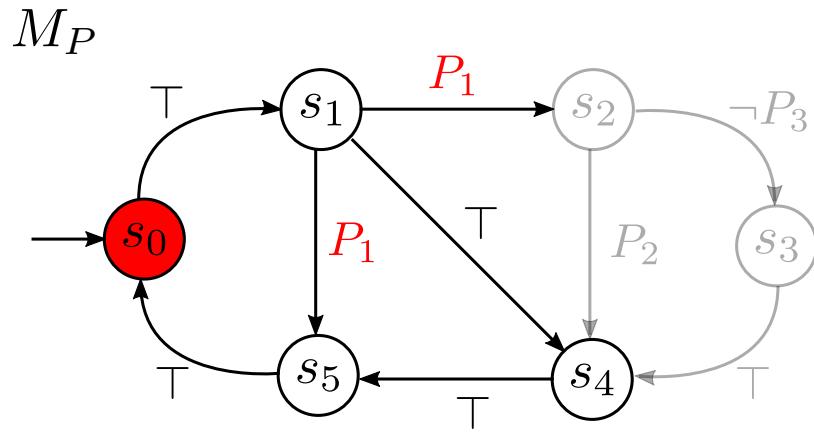
$\hat{c}$  = initially empty

Unset Parameters

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



FINDUP

(finds reachable unset parameters)

Configuration

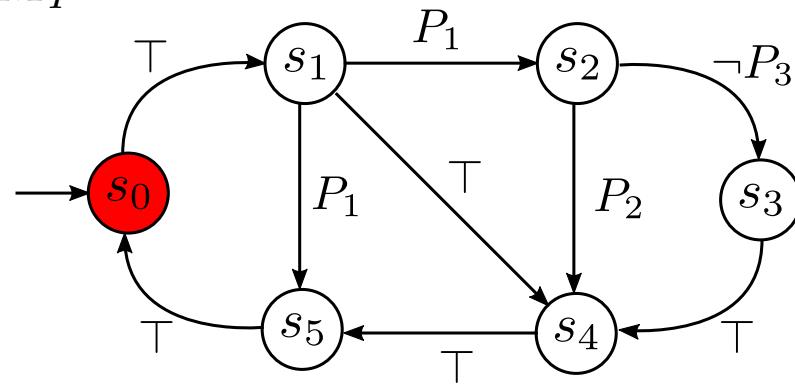
$\hat{c}$  = initially empty

Unset Parameters

$$\begin{aligned} P_u &= \text{FINDUP}(M_P, \hat{c}) \\ &= (P_1) \end{aligned}$$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = (P_1)$$

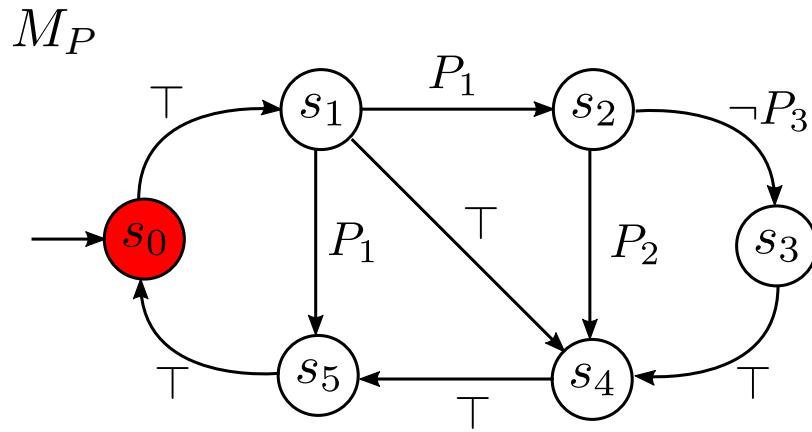
$\hat{c}$  = initially empty

Parameter Configurations

?

# Parameter Configurations

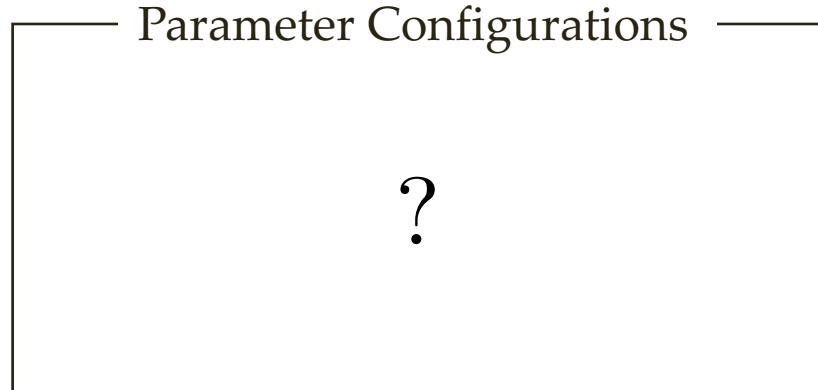
**Goal:** Reduce the number of parameter configurations to check



$$P_u = (P_1)$$

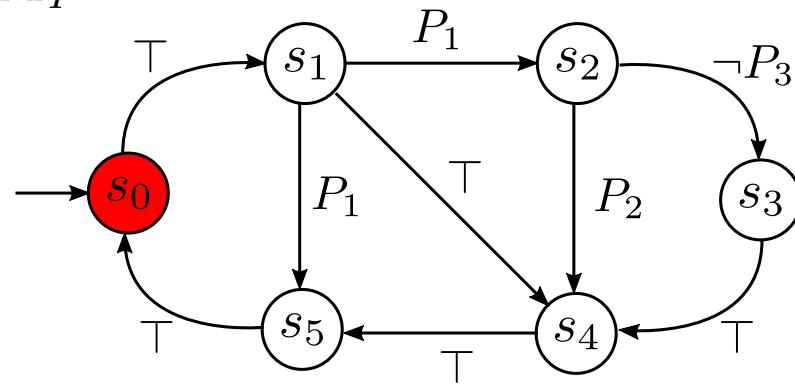
$\hat{c}$  = initially empty

**GENPC**  
(recursively generate  
parameter configurations)



# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = (P_1)$$

$\hat{c}$  = initially empty

**GENPC**

(recursively generate  
parameter configurations)

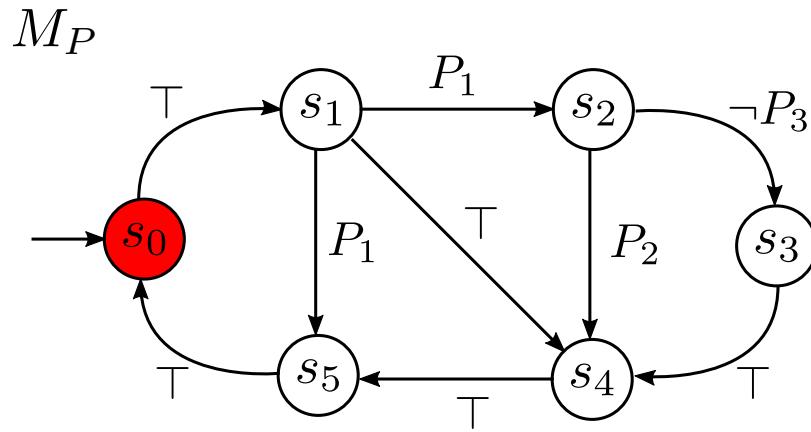
Parameter Configurations

?

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



$$P_u = (P_1)$$

$$\hat{c} = \text{initially empty}$$

**GENPC**  
(recursively generate  
parameter configurations)

Parameter Configurations

?

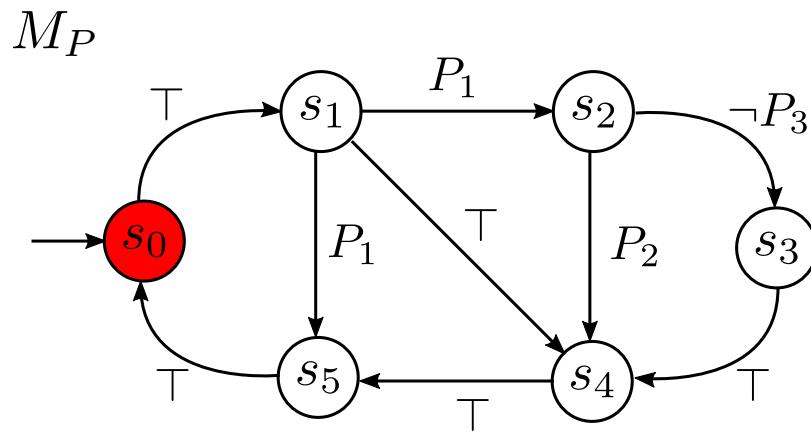
Call  $\text{GENPC}(M_P, P_u, \hat{c})$

$p = \text{dequeue from } P_u$   
 $= P_1$

$P_u = \text{empty}$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



$$P_u = (P_1)$$

$$\hat{c} = \text{initially empty}$$

**GENPC**  
(recursively generate  
parameter configurations)

Parameter Configurations

?

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

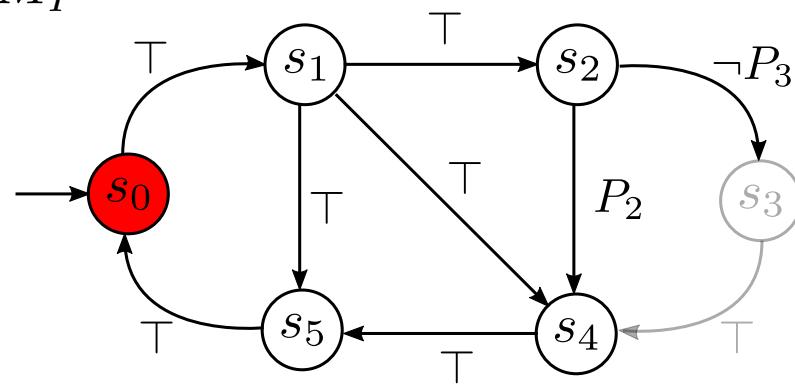
$p = \text{dequeue from } P_u$   
 $= P_1$

$P_u = \text{empty}$

$\hat{c} = (P_1 = \top) \text{ and } \hat{c} = (P_1 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \top)$$

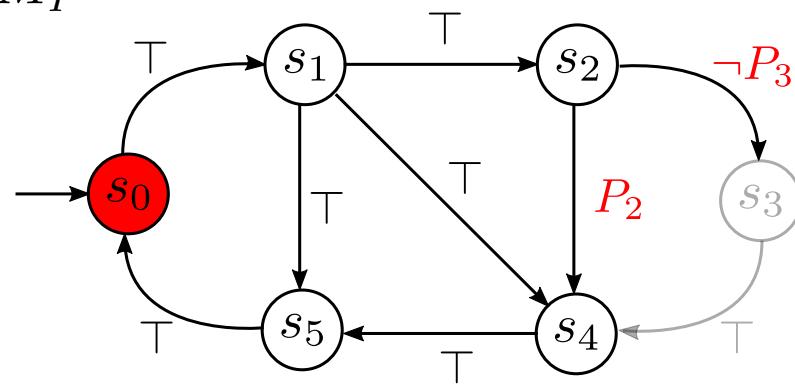
$$P_u = \text{FINDUP}(M_P, \hat{c})$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 


$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \top)$$

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

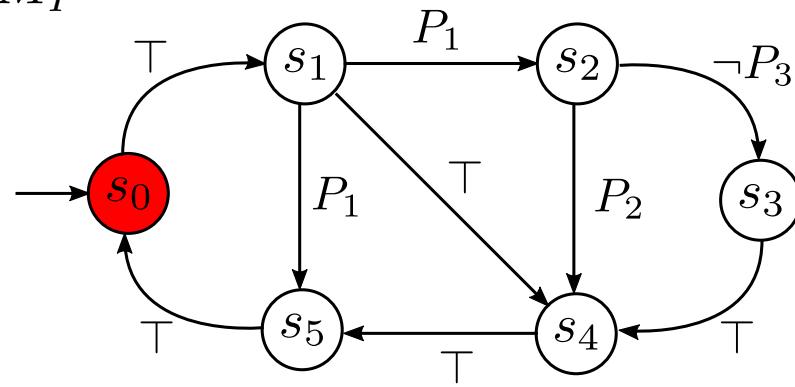
$$= (P_2, P_3)$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

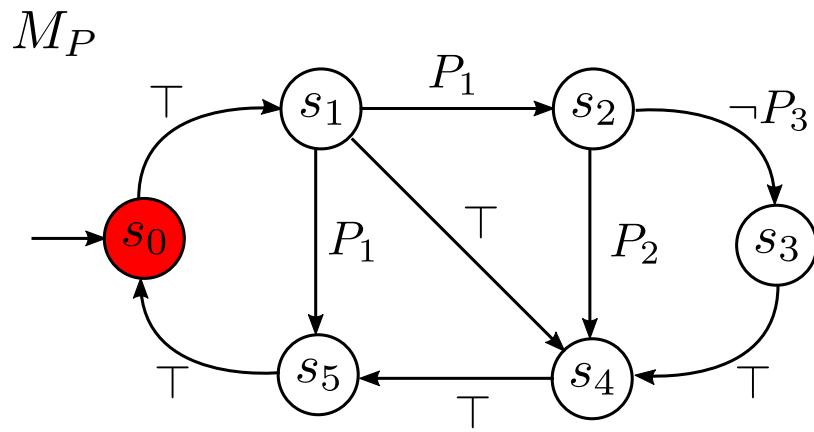
$$P_u = (P_2, P_3)$$
$$\hat{c} = (P_1 = \top)$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



$$\begin{aligned} P_u &= (P_2, P_3) \\ \hat{c} &= (P_1 = \top) \end{aligned}$$

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

$p = \text{dequeue from } P_u$   
 $= P_2$

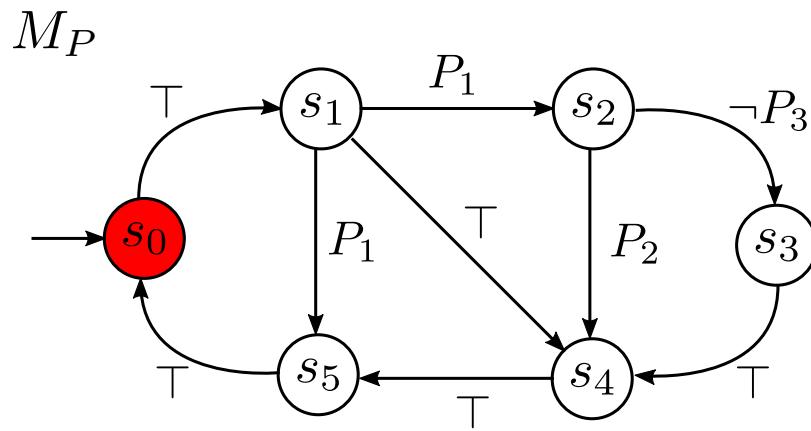
$$P_u = (P_3)$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

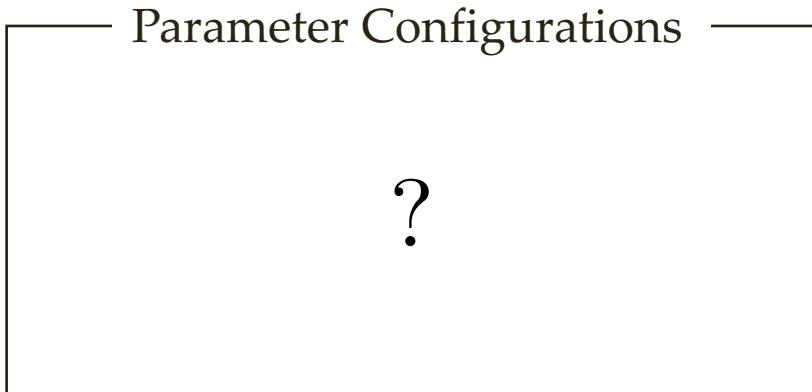


$$\begin{aligned} P_u &= (P_2, P_3) \\ \hat{c} &= (P_1 = \top) \end{aligned}$$

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

$p = \text{dequeue from } P_u$   
 $= P_2$

$$P_u = (P_3)$$

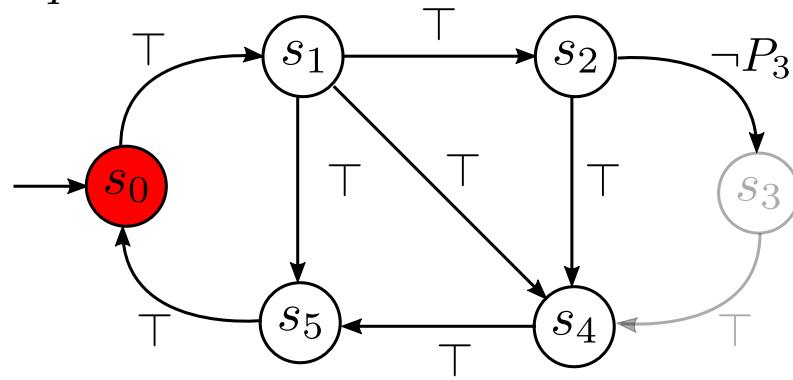


$\hat{c} = (P_1 = \top, P_2 = \top)$   
 and

$\hat{c} = (P_1 = \top, P_2 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = (P_3)$$

$$\hat{c} = (P_1 = \top, P_2 = \top)$$

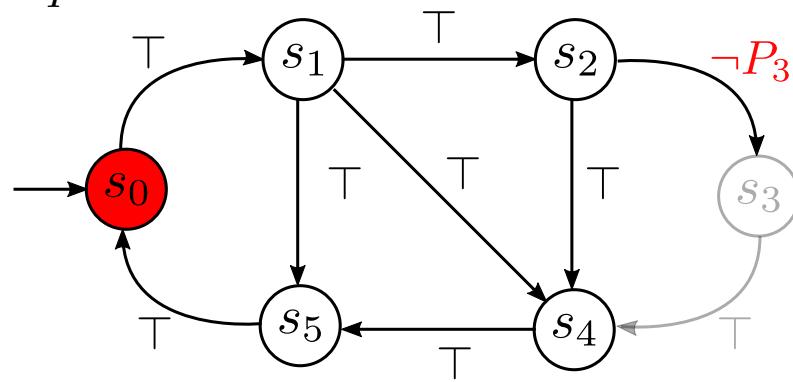
$$P_u = \text{FINDUP}(M_P, \hat{c})$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 


$P_u = (P_3)$

$\hat{c} = (P_1 = \top, P_2 = \top)$

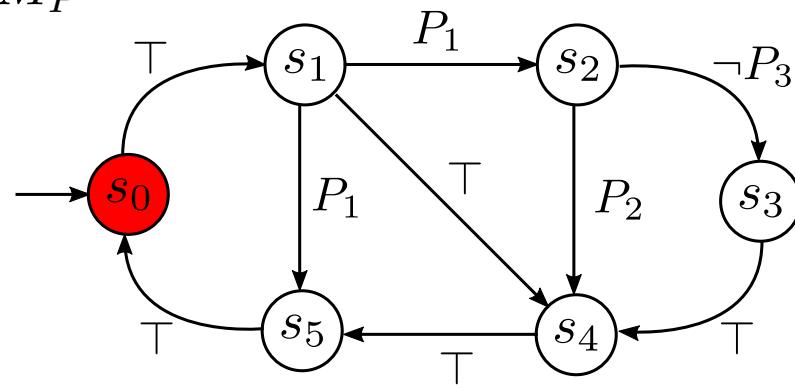
$$\begin{aligned} P_u &= \text{FINDUP}(M_P, \hat{c}) \\ &= (P_3) \end{aligned}$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = (P_3)$$

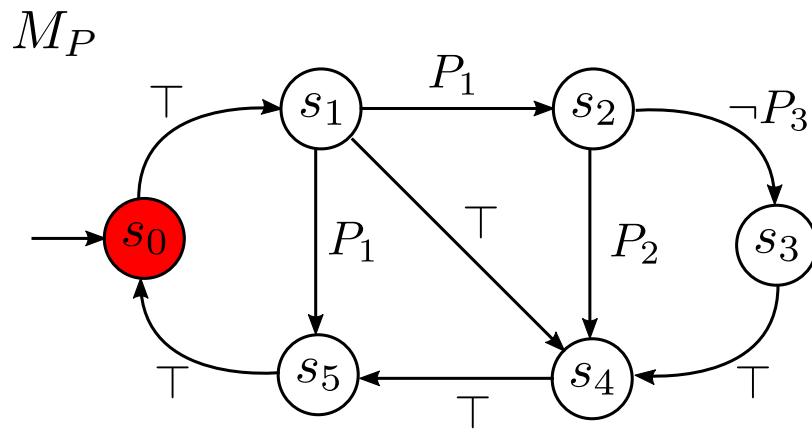
$$\hat{c} = (P_1 = \top, P_2 = \top)$$

Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



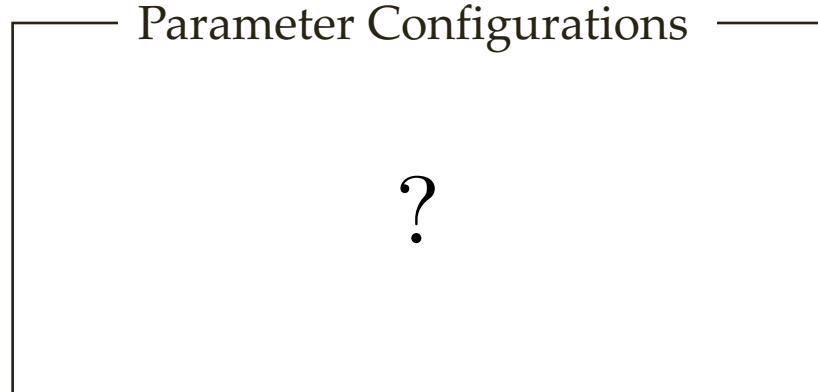
$$P_u = (P_3)$$

$$\hat{c} = (P_1 = \top, P_2 = \top)$$

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

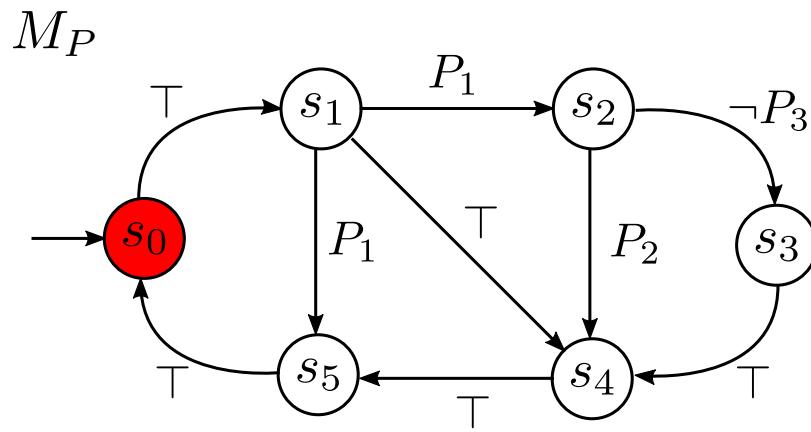
$p = \text{dequeue from } P_u$   
 $= P_3$

$P_u = \text{empty}$



# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check



$$P_u = (P_3)$$

$$\hat{c} = (P_1 = \top, P_2 = \top)$$

Call  $\text{GENPC}(M_P, P_u, \hat{c})$

$p = \text{dequeue from } P_u$   
 $= P_3$

$P_u = \text{empty}$

Parameter Configurations

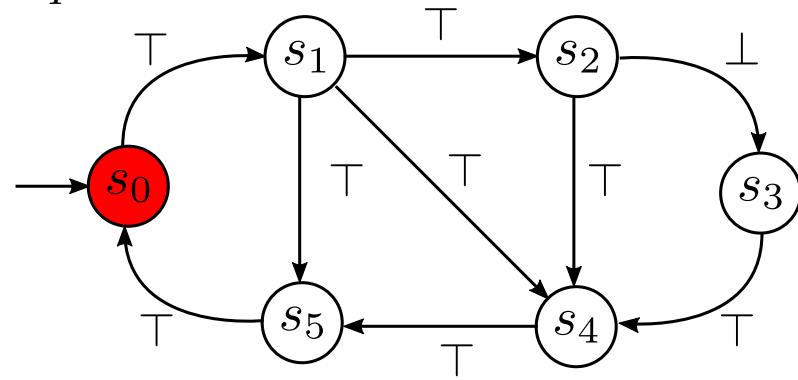
?

$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \top)$   
 and

$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \top)$$

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

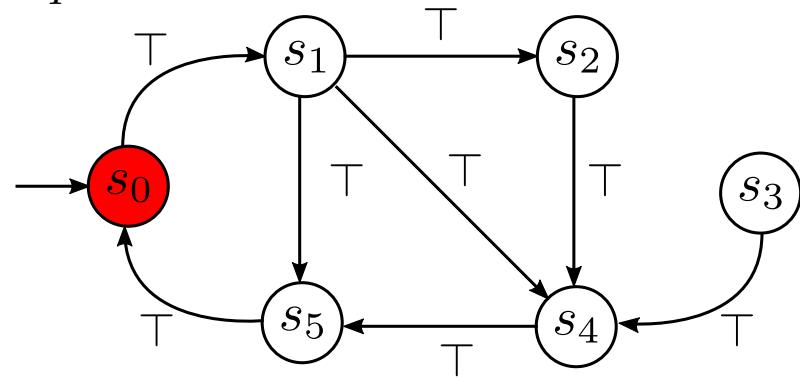
Parameter Configurations

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

$M_P$



$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \top)$$

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

= empty

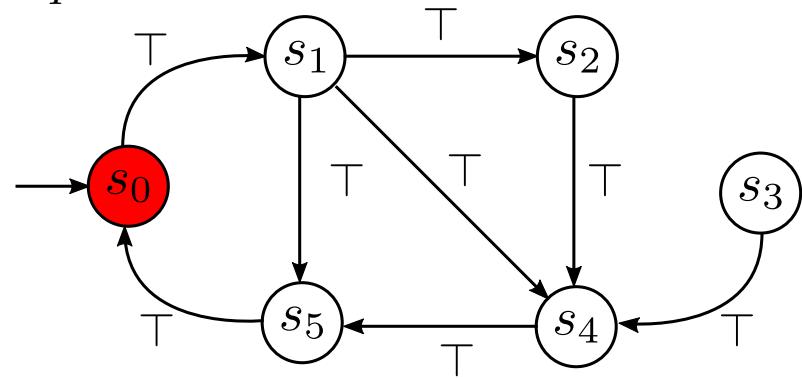
Parameter Configurations

$$(P_1 = \top, P_2 = \top, P_3 = \top)$$

?

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 


$P_u = \text{empty}$

$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \top)$

$P_u = \text{FINDUP}(M_P, \hat{c})$

= empty

Parameter Configurations

$(P_1 = \top, P_2 = \top, P_3 = \top)$

?

Configuration Found!

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

$$M_P$$

The diagram shows a state transition graph  $M_P$  with six states:  $s_0$ ,  $s_1$ ,  $s_2$ ,  $s_3$ ,  $s_4$ , and  $s_5$ . Transitions are labeled with parameters  $P_1$ ,  $P_2$ , and  $P_3$ :  
-  $s_0 \xrightarrow{\top} s_1$  (labeled  $P_1$ )  
-  $s_0 \xrightarrow{\top} s_5$  (labeled  $P_1$ )  
-  $s_1 \xrightarrow{\top} s_0$  (labeled  $\top$ )  
-  $s_1 \xrightarrow{P_1} s_2$   
-  $s_1 \xrightarrow{\top} s_5$  (labeled  $\top$ )  
-  $s_2 \xrightarrow{P_2} s_4$   
-  $s_2 \xrightarrow{\neg P_3} s_3$   
-  $s_3 \xrightarrow{\top} s_4$  (labeled  $\top$ )  
-  $s_4 \xrightarrow{\top} s_5$  (labeled  $\top$ )  
-  $s_4 \xrightarrow{\top} s_0$  (labeled  $\top$ )  
-  $s_5 \xrightarrow{\top} s_1$  (labeled  $\top$ )

$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \perp)$$

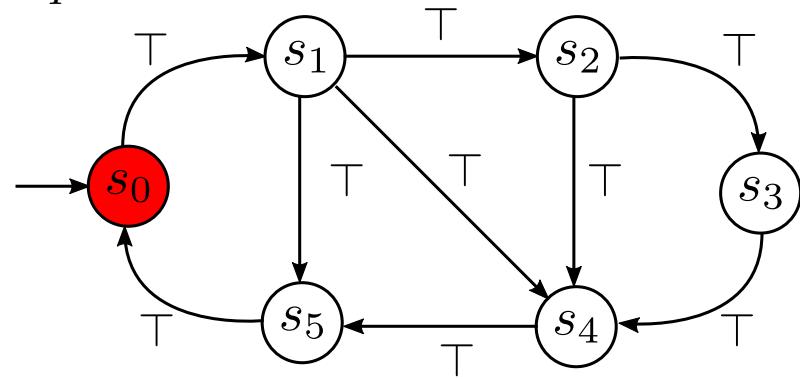
$$P_u = \text{FINDUP}(M_P, \hat{c})$$

## Parameter Configurations

$$(P_1 = \top, P_2 = \top, P_3 = \top)$$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

 $P_u = \text{empty}$ 
 $\hat{c} = (P_1 = \top, P_2 = \top, P_3 = \perp)$ 

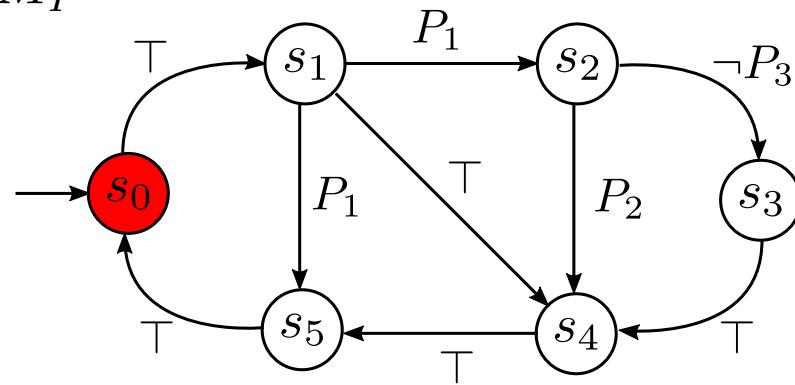
## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$

**Configuration Found!**

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

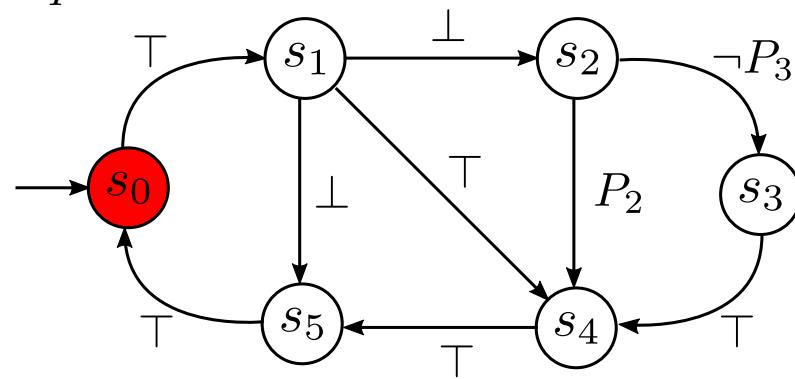
 $M_P$ 

## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$
- $(P_1 = \top, P_2 = \perp, P_3 = \top)$
- $(P_1 = \top, P_2 = \perp, P_3 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 


$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \perp)$$

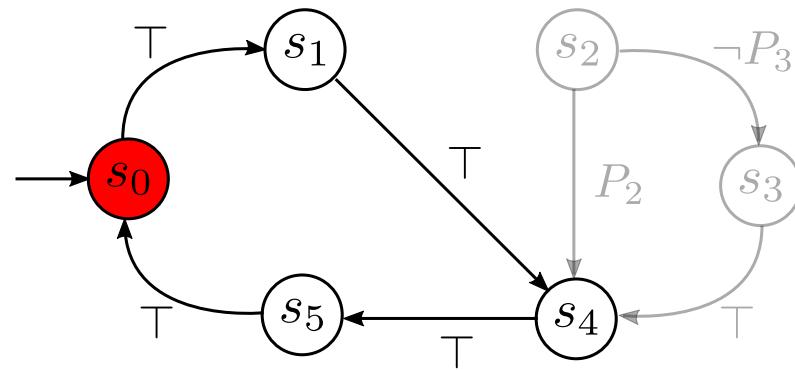
$$P_u = \text{FINDUP}(M_P, \hat{c})$$

## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$
- $(P_1 = \top, P_2 = \perp, P_3 = \top)$
- $(P_1 = \top, P_2 = \perp, P_3 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 


$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \perp)$$

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

$$= \text{empty}$$

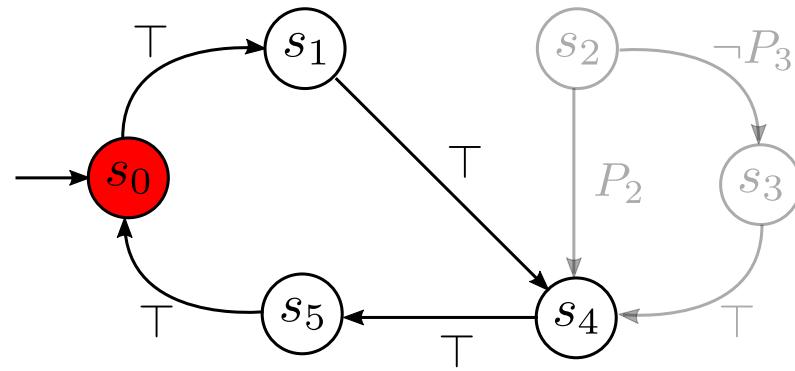
## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$
- $(P_1 = \top, P_2 = \perp, P_3 = \top)$
- $(P_1 = \top, P_2 = \perp, P_3 = \perp)$

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

$M_P$



$$P_u = \text{empty}$$

$$\hat{c} = (P_1 = \perp)$$

$$P_u = \text{FINDUP}(M_P, \hat{c})$$

$$= \text{empty}$$

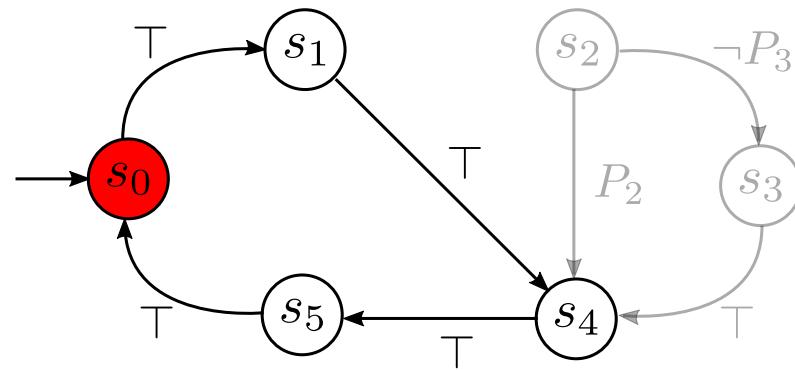
## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$
- $(P_1 = \top, P_2 = \perp, P_3 = \top)$
- $(P_1 = \top, P_2 = \perp, P_3 = \perp)$
- $(P_1 = \perp)$

**Configuration Found!**

# Parameter Configurations

**Goal:** Reduce the number of parameter configurations to check

 $M_P$ 

 $P_u = \text{empty}$ 
 $\hat{c} = (P_1 = \perp)$ 
 $P_u = \text{FINDUP}(M_P, \hat{c})$ 
 $= \text{empty}$ 

## Parameter Configurations

- $(P_1 = \top, P_2 = \top, P_3 = \top)$
- $(P_1 = \top, P_2 = \top, P_3 = \perp)$
- $(P_1 = \top, P_2 = \perp, P_3 = \top)$
- $(P_1 = \top, P_2 = \perp, P_3 = \perp)$
- $(P_1 = \perp)$

Configuration Found!

Minimal  
Guarantee

# LTL Properties

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \square q$$

$$\varphi_2 = \square(p \wedge q)$$

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \square q$$

$$\varphi_2 = \square(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \square q$$

$$\varphi_2 = \square(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \Box q$$

$$\varphi_2 = \Box(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \Box q$$

$$\varphi_2 = \Box(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

$$\varphi_3 = \Box(p \vee q)$$

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \Box q$$

$$\varphi_2 = \Box(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

$$\varphi_3 = \Box(p \vee q)$$

$M \models \varphi_2$  then  $M \models \varphi_3$

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \Box q$$

$$\varphi_2 = \Box(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

$$\varphi_3 = \Box(p \vee q)$$

$M \models \varphi_2$  then  $M \models \varphi_3$   
 $\varphi_2$  and  $\varphi_3$  are dependent

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \Box q$$

$$\varphi_2 = \Box(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

$$\varphi_3 = \Box(p \vee q)$$

$M \models \varphi_2$  then  $M \models \varphi_3$   
 $\varphi_2$  and  $\varphi_3$  are dependent

Knowing dependencies *at design-time* is hard!

# LTL Properties

## Motivating Example

**Given:** Model M over variables  $p$  and  $q$

$$\varphi_1 = \square q$$

$$\varphi_2 = \square(p \wedge q)$$

$M \models \varphi_2$  then  $M \models \varphi_1$  ( $M \not\models \varphi_1$  then  $M \not\models \varphi_2$ )  
 $\varphi_1$  and  $\varphi_2$  are dependent

$$\varphi_3 = \square(p \vee q)$$

$M \models \varphi_2$  then  $M \models \varphi_3$   
 $\varphi_2$  and  $\varphi_3$  are dependent

**Knowing dependencies *at design-time* is hard!**

We use LTL Satisfiability Checking

# LTL Properties

**Goal:** Reduce the number of LTL properties to check

$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

	keys	dependencies
$\varphi_1$	$T$	
	$F$	
$\varphi_2$	$T$	
	$F$	
$\varphi_3$	$T$	
	$F$	
.....		

Property Table

	result
$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
.....	

Result Array

# LTL Properties

**Goal:** Reduce the number of LTL properties to check

$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

keys      dependencies

$\varphi_1$	$T$						
	$F$						
$\varphi_2$	$T$						
	$F$						
$\varphi_3$	$T$						
	$F$						
		•	•	•	•	•	•

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
•	
•	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

# LTL Properties

**Goal:** Reduce the number of LTL properties to check

$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

keys      dependencies

$\varphi_1$	$T$						
	$F$						
$\varphi_2$	$T$						
	$F$						
$\varphi_3$	$T$						
	$F$						
		•	•	•	•	•	•

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
•	
•	

Result Array

CHECKRP

(checks reduced  
number of properties)

Is  $(\varphi_1 \rightarrow \varphi_2)$  satisfiable?

# LTL Properties

**Goal:** Reduce the number of LTL properties to check

$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

	keys	dependencies	
$\varphi_1$	T	$\varphi_2$	T
	F		
$\varphi_2$	T		
	F		
$\varphi_3$	T		
	F		
		.....	

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
...	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

Is  $(\varphi_1 \rightarrow \varphi_2)$  satisfiable?

Yes!

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keys    dependencies

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$\varphi_1$	T	$\varphi_2$	T
	F		
$\varphi_2$	T		
	F	$\varphi_1$	F
$\varphi_3$	T		
	F		
		.....	.....

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
...	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

Is  $(\varphi_1 \rightarrow \varphi_2)$  satisfiable?

Yes!

$(\neg\varphi_2 \rightarrow \neg\varphi_1)$  also satisfiable.

# LTL Properties

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$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

keys    dependencies

$\varphi_1$	$T$	$\varphi_2$	$T$				
	$F$						
$\varphi_2$	$T$						
	$F$	$\varphi_1$	$F$				
$\varphi_3$	$T$						
	$F$						
		$\dots$	$\dots$	$\dots$	$\dots$	$\dots$	$\dots$

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
$\dots$	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

$$\varphi_1 \rightarrow \varphi_2$$

$$\neg \varphi_1 \rightarrow \varphi_2$$

$$\neg \varphi_1 \rightarrow \neg \varphi_2$$

$$\varphi_1 \rightarrow \neg \varphi_2$$

+ contra-  
positives

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keys    dependencies

		keys	dependencies
$\varphi_1$	$T$	$\varphi_2$	$T$
	$F$		
$\varphi_2$	$T$		
	$F$	$\varphi_1$	$F$
$\varphi_3$	$T$		
	$F$		
.....			

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
...	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

$$\varphi_1 \rightarrow \varphi_2$$

$$\neg\varphi_1 \rightarrow \varphi_2$$

$$\neg\varphi_1 \rightarrow \neg\varphi_2$$

$$\varphi_1 \rightarrow \neg\varphi_2$$

Interesting

# LTL Properties

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keys      dependencies

		T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T							
	F		$\varphi_3$	F				
$\varphi_2$	T		$\varphi_3$	T	$\varphi_4$	T		
	F		$\varphi_1$	F				
$\varphi_3$	T		$\varphi_1$	T	$\varphi_4$	T		
	F		$\varphi_2$	F	$\varphi_6$	T		
		•	•	•	•	•	•	•

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
•	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

# LTL Properties

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keys      dependencies

		T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T							
	F		$\varphi_3$	F				
$\varphi_2$	T		$\varphi_3$	T	$\varphi_4$	T		
	F		$\varphi_1$	F				
$\varphi_3$	T		$\varphi_1$	T	$\varphi_4$	T		
	F		$\varphi_2$	F	$\varphi_6$	T		
.....								

Property Table

result

$\varphi_1$	
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
...	

Result  
Array

CHECKRP

(checks reduced  
number of properties)

Check if  $\forall \varphi \in \mathcal{P}$   
 $M \models ? \varphi$

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	T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T						
	F			$\varphi_3$	F		
$\varphi_2$	T			$\varphi_3$	T	$\varphi_4$	T
	F			$\varphi_1$	F		
$\varphi_3$	T			$\varphi_1$	T	$\varphi_4$	T
	F			$\varphi_2$	F	$\varphi_6$	T
	•	•	•	•	•	•	•

Property Table

result

$\varphi_1$	F
$\varphi_2$	
$\varphi_3$	
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
•	•



CHECKRP

(checks reduced  
number of properties)

Check if  $\forall \varphi \in \mathcal{P}$   
 $M \models? \varphi$

$M \not\models \varphi_1$

Result  
Array

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$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

keys    dependencies

	T	$\varphi_2$ T	$\varphi_3$ T	$\varphi_5$ T
$\varphi_1$	T	$\varphi_2$ T	$\varphi_3$ T	$\varphi_5$ T
	F	$\varphi_3$ F		
$\varphi_2$	T	$\varphi_3$ T	$\varphi_4$ T	
	F	$\varphi_1$ F		
$\varphi_3$	T	$\varphi_1$ T	$\varphi_4$ T	
	F	$\varphi_2$ F	$\varphi_6$ T	
	• • • • •			

Property Table

result

$\varphi_1$	F
$\varphi_2$	
$\varphi_3$	F
$\varphi_4$	
$\varphi_5$	
$\varphi_6$	
• •	

Result Array

CHECKRP

(checks reduced  
number of properties)

Check if  $\forall \varphi \in \mathcal{P}$   
 $M \models? \varphi$

$M \not\models \varphi_1$

$M \not\models \varphi_3$

# LTL Properties

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$$\mathcal{P} = \{\varphi_1, \varphi_2, \varphi_3, \varphi_4, \varphi_5, \varphi_6\}$$

keys    dependencies

	T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T						
	F			$\varphi_3$	F		
$\varphi_2$	T		$\varphi_3$	T		$\varphi_4$	T
	F			$\varphi_1$	F		
$\varphi_3$	T		$\varphi_1$	T		$\varphi_4$	T
	F			$\varphi_2$	F		$\varphi_6$
	...	...	...	...	...	...	...

Property Table

result

$\varphi_1$	F	✓
$\varphi_2$	F	✓
$\varphi_3$	F	✓
$\varphi_4$		
$\varphi_5$		
$\varphi_6$	T	✓
...		

Result Array

CHECKRP

(checks reduced  
number of properties)

Check if  $\forall \varphi \in \mathcal{P}$   
 $M \models? \varphi$

$M \not\models \varphi_1$

$M \not\models \varphi_2$     $M \not\models \varphi_3$     $M \models \varphi_6$

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	T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T						
	F			$\varphi_3$	F		
$\varphi_2$	T		$\varphi_3$	T		$\varphi_4$	T
	F			$\varphi_1$	F		
$\varphi_3$	T		$\varphi_1$	T		$\varphi_4$	T
	F		$\varphi_2$	F		$\varphi_6$	T
.....							

Property Table

result

$\varphi_1$	F	✓
$\varphi_2$	F	✓
$\varphi_3$	F	✓
$\varphi_4$		
$\varphi_5$		
$\varphi_6$	T	✓
...		

Result Array

CHECKRP

(checks reduced  
number of properties)

Check if  $\forall \varphi \in \mathcal{P}$   
 $M \models? \varphi$

$M \not\models \varphi_1$

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**Self Validating!**

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		T	$\varphi_2$	T	$\varphi_3$	T	$\varphi_5$	T
$\varphi_1$	T							
	F		$\varphi_3$	F				
$\varphi_2$	T		$\varphi_3$	T		$\varphi_4$	T	
	F		$\varphi_1$	F				
$\varphi_3$	T		$\varphi_1$	T		$\varphi_4$	T	
	F		$\varphi_2$	F		$\varphi_6$	T	
		•	•	•	•	•	•	•

Property Table

result

$\varphi_1$	F	✓
$\varphi_2$	F	✓
$\varphi_3$	F	✓
$\varphi_4$		
$\varphi_5$		
$\varphi_6$	T	✓
	•	•

Result Array

CHECKRP

(checks reduced  
number of properties)

Heuristics

- H1: Maximum Dependence
- H2: Property Grouping

⋮

# Experiment Setup

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- D<sup>3</sup> is implemented as a preprocessing script in ~2,000 lines of Python.



Source code available at  
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# Experiment Setup

- D<sup>3</sup> is implemented as a preprocessing script in ~2,000 lines of Python.
- We use NUXMV as the interfaced state-of-the-art model checker
- Benchmarks Evaluated
  - NASA NextGen Air Traffic Control (ATC) System (Gario et al., 2016)
  - BOEING AIR 6110 Wheel Braking (WBS) System (Bozzano et al., 2015)

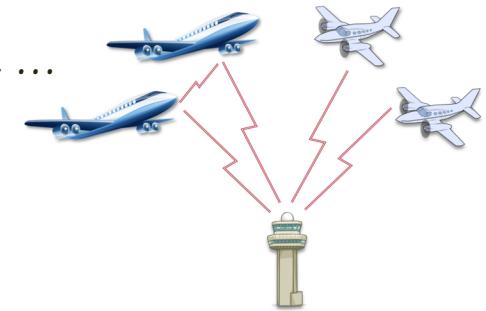


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# NASA ATC Benchmark

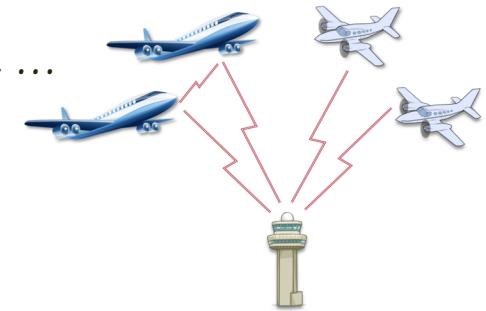
# NASA ATC Benchmark

- Large design-space with 1,620 design choices  
*different types of aircraft, varying levels of information sharing, ...*



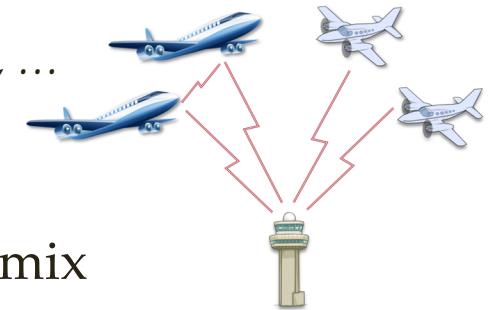
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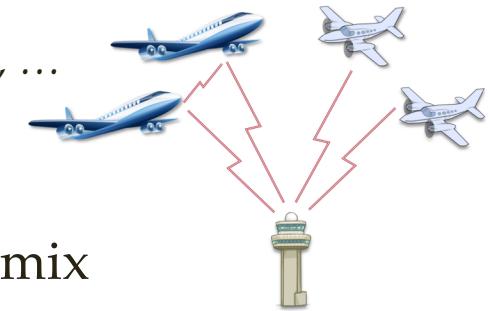
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*mix of SAT and UNSAT properties*



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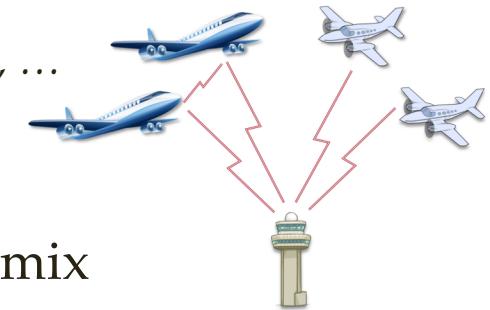
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Individual model checking: >200 hours

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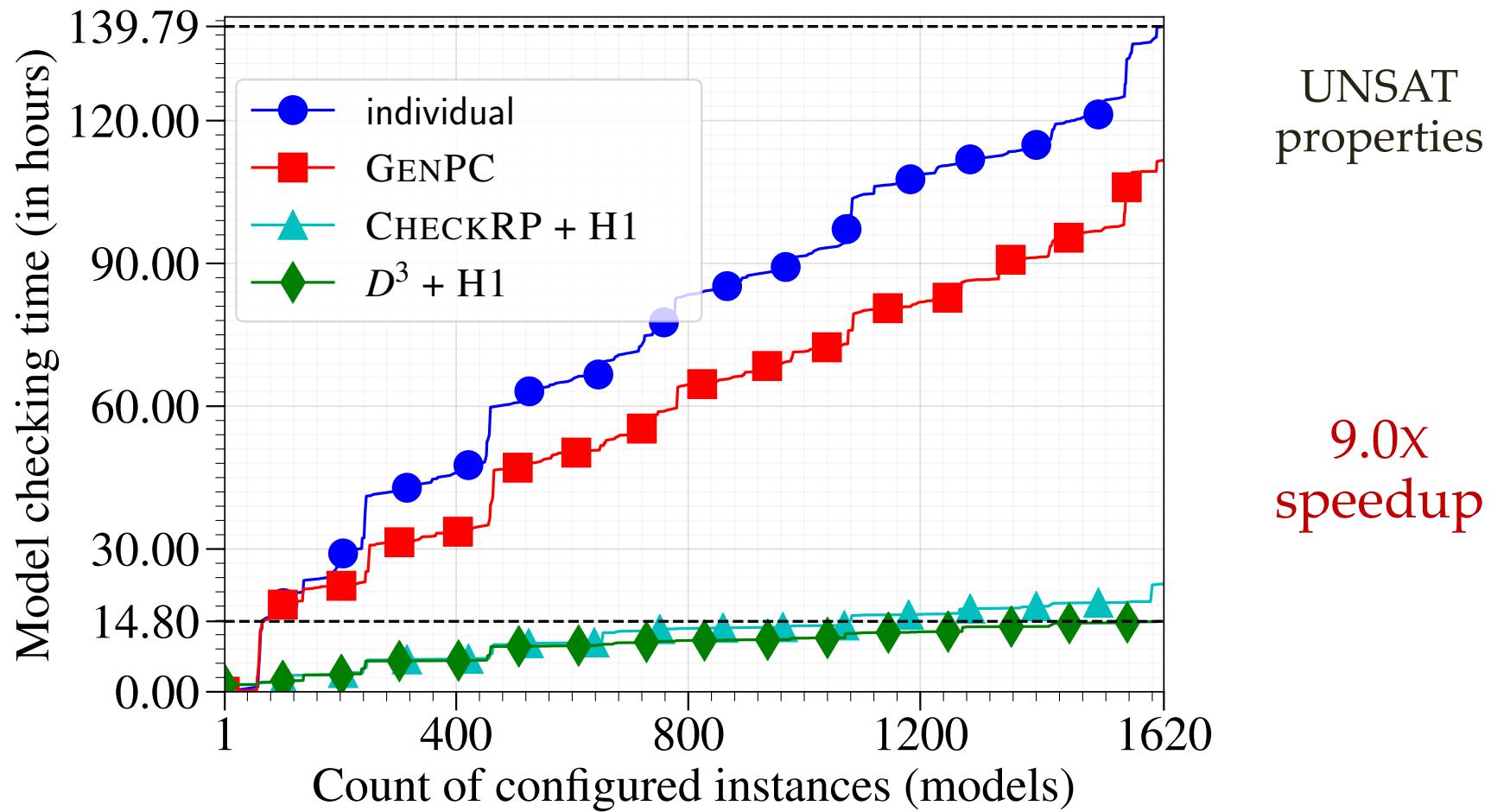
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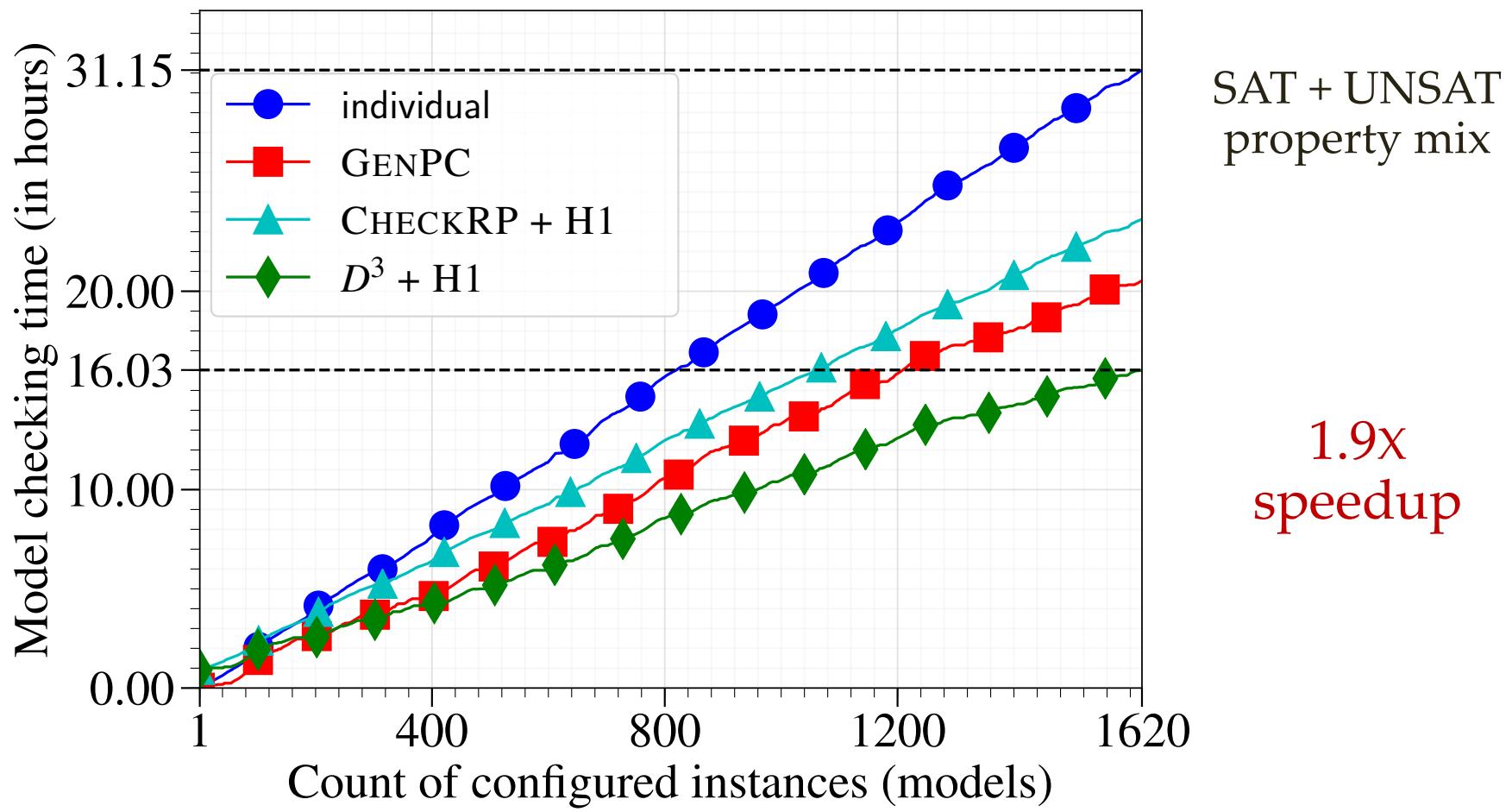
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**Model checking with D<sup>3</sup>: 50 hours**

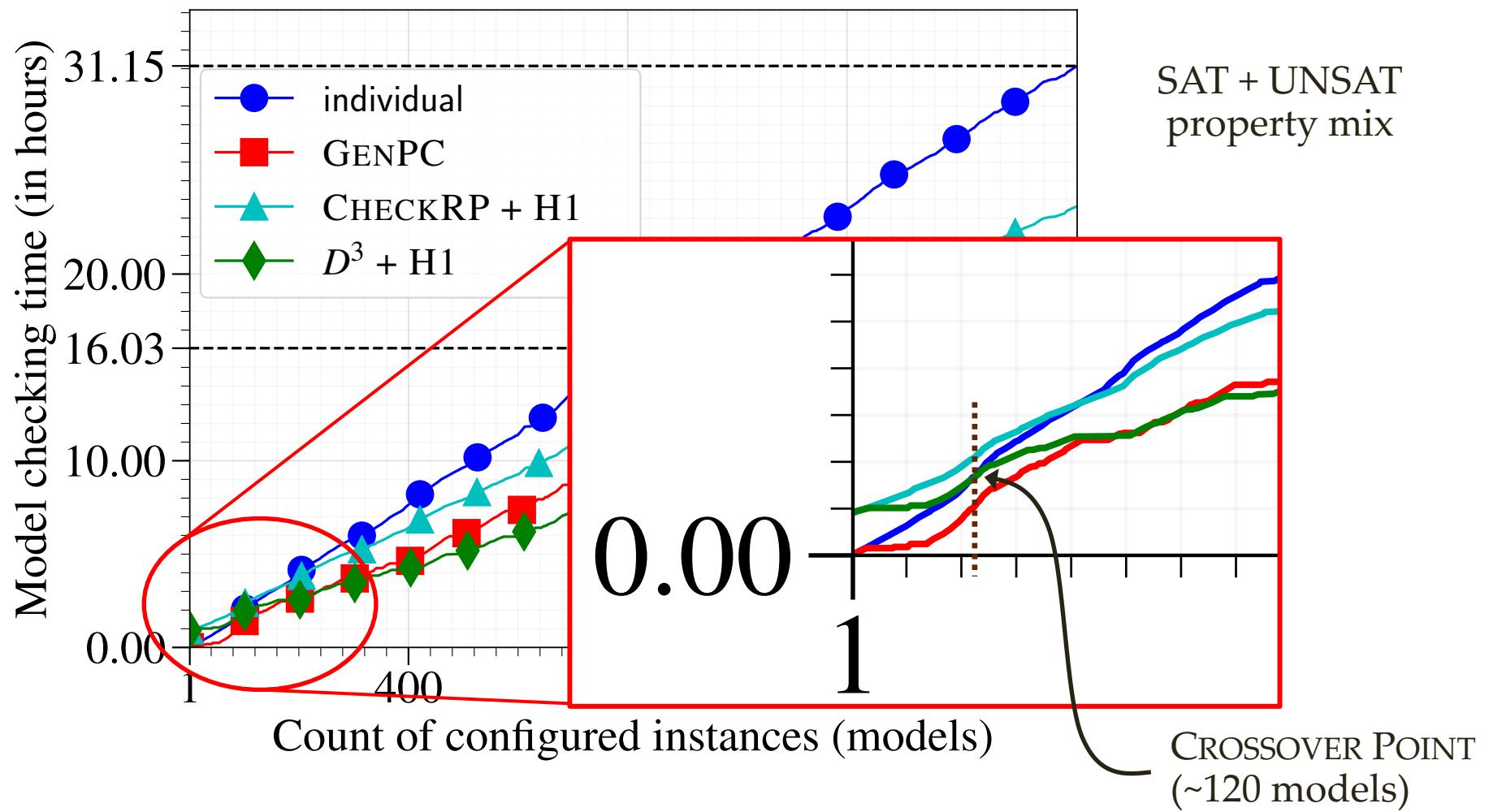
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# NASA ATC Benchmark



# BOEING WBS Benchmark

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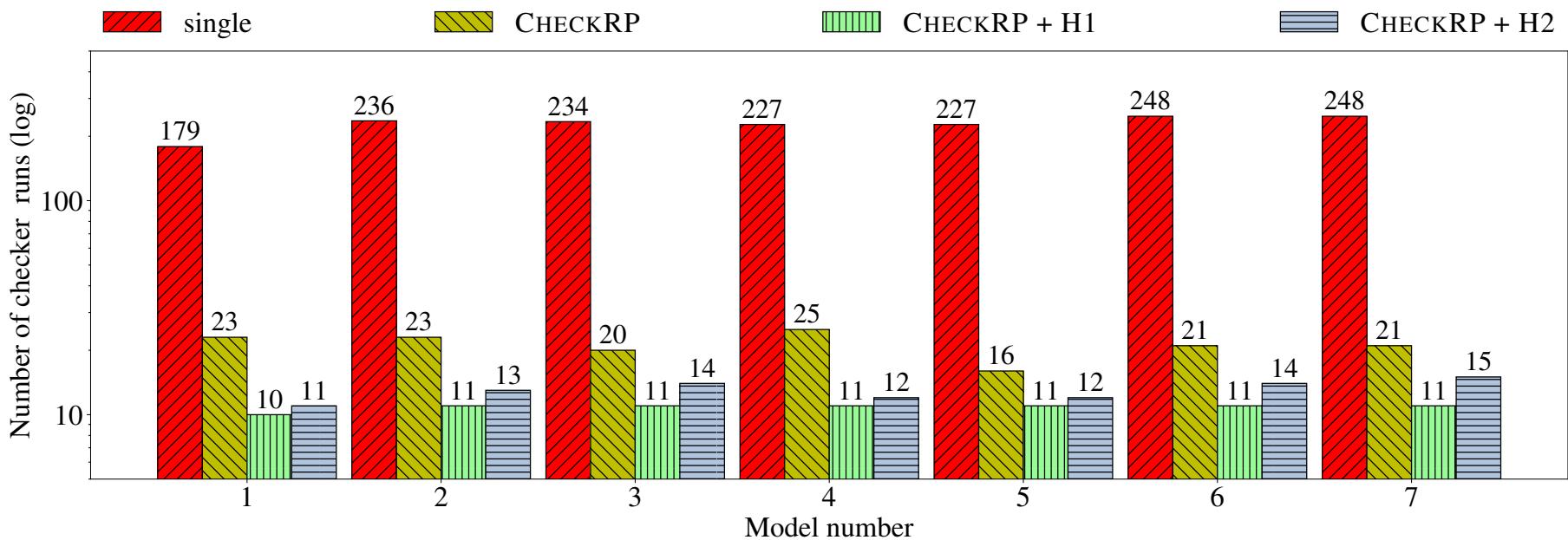
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  - Design-space parameters – reduction to a reachability problem
  - Finds **minimal** configurations, all property dependencies

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  - SMV models extended with parameters as preprocessor directives

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  - Can we re-use intermediate results across several models?
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Thank You!

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