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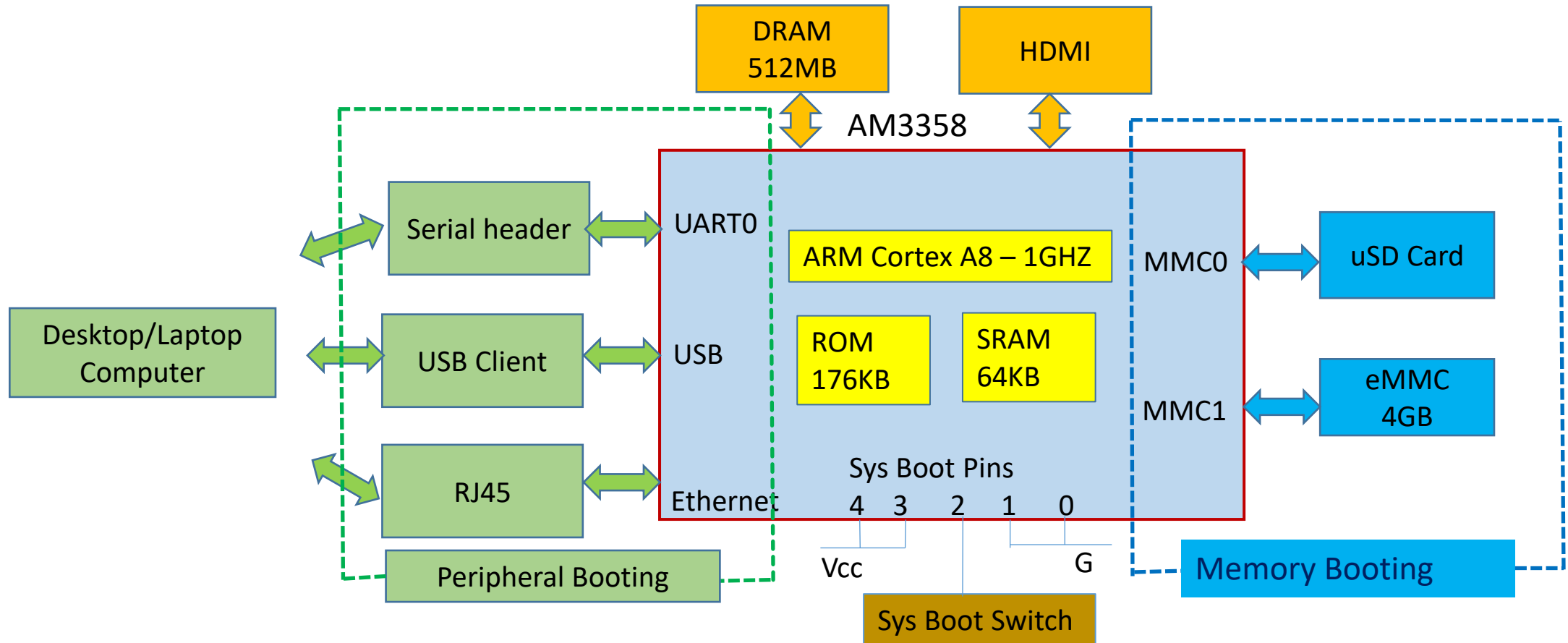
I2C Framework Diagrams

Source Code Flow

Embedded Linux Boot Sequence

Host Machine

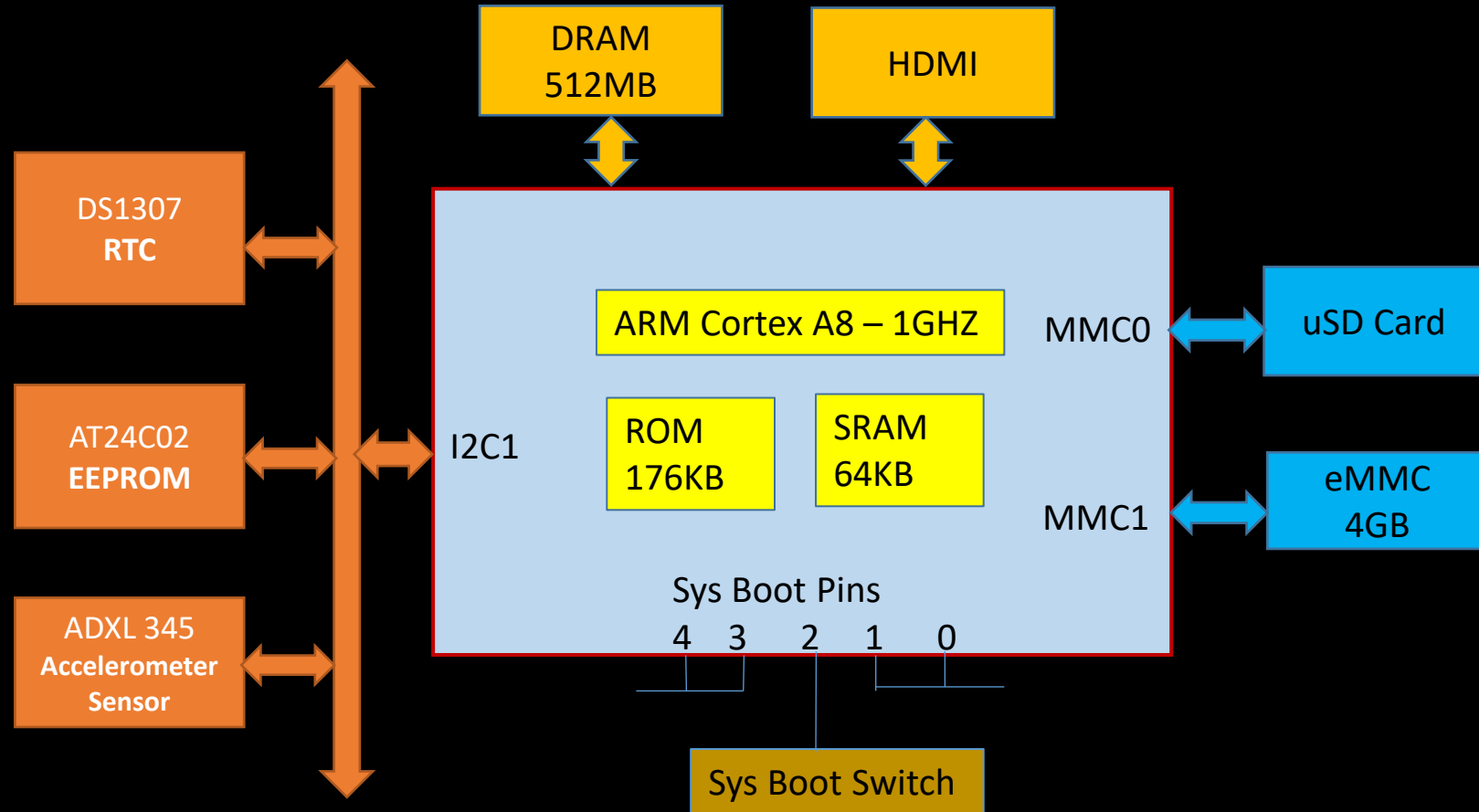
Target Machine – Beagle Bone Black



Sysboot SW	Sysboot pins	1 st Order	2 nd Order	3 rd Order	4 th Order
OFF	11100	MMC1	MMC0	UART0	USB0
ON	11000	SPIO	MMC0	USB0	UART0



I2C – AM335x Interface with Beagle Bone Black



Sysboot SW	Sysboot pins	1 st Order	2 nd Order	3 rd Order	4 th Order
OFF	11100	MMC1	MMC0	UART0	USB0
ON	11000	SPIO	MMC0	USB0	UART0

I2C

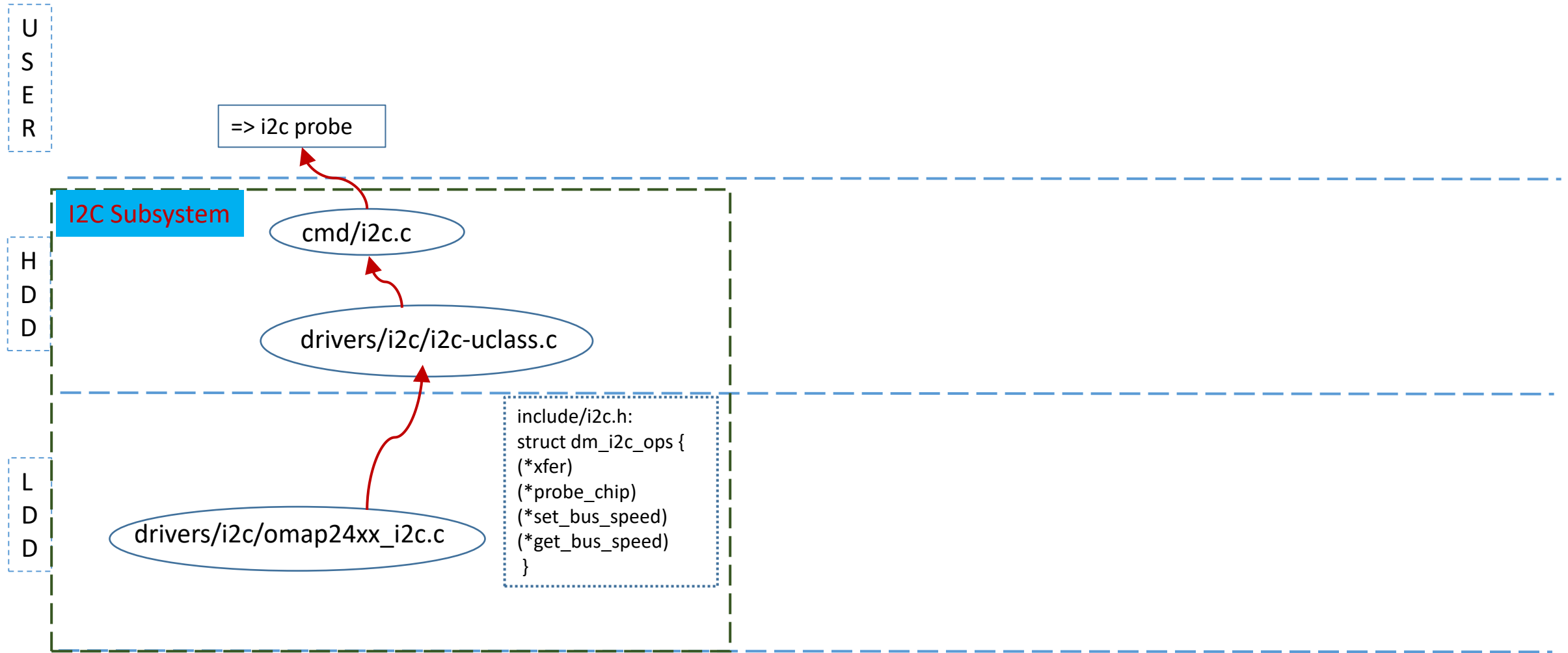
AM335X I2C Controller Basic Framework

Framework

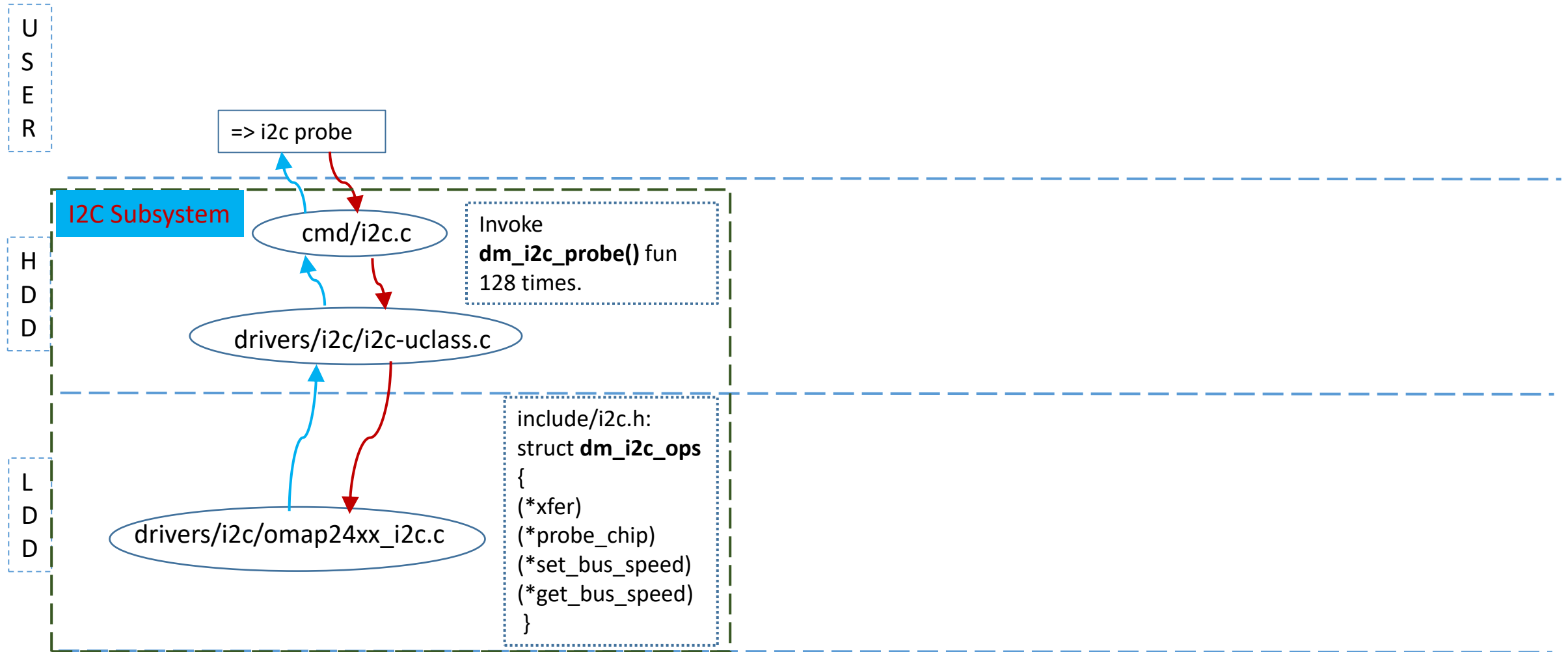
Two types of framework

1. I2C Driver Initialization
2. I2C Driver Operation

I2C Master Initialization Framework in U-boot



I2C Master Operation Framework in U-boot



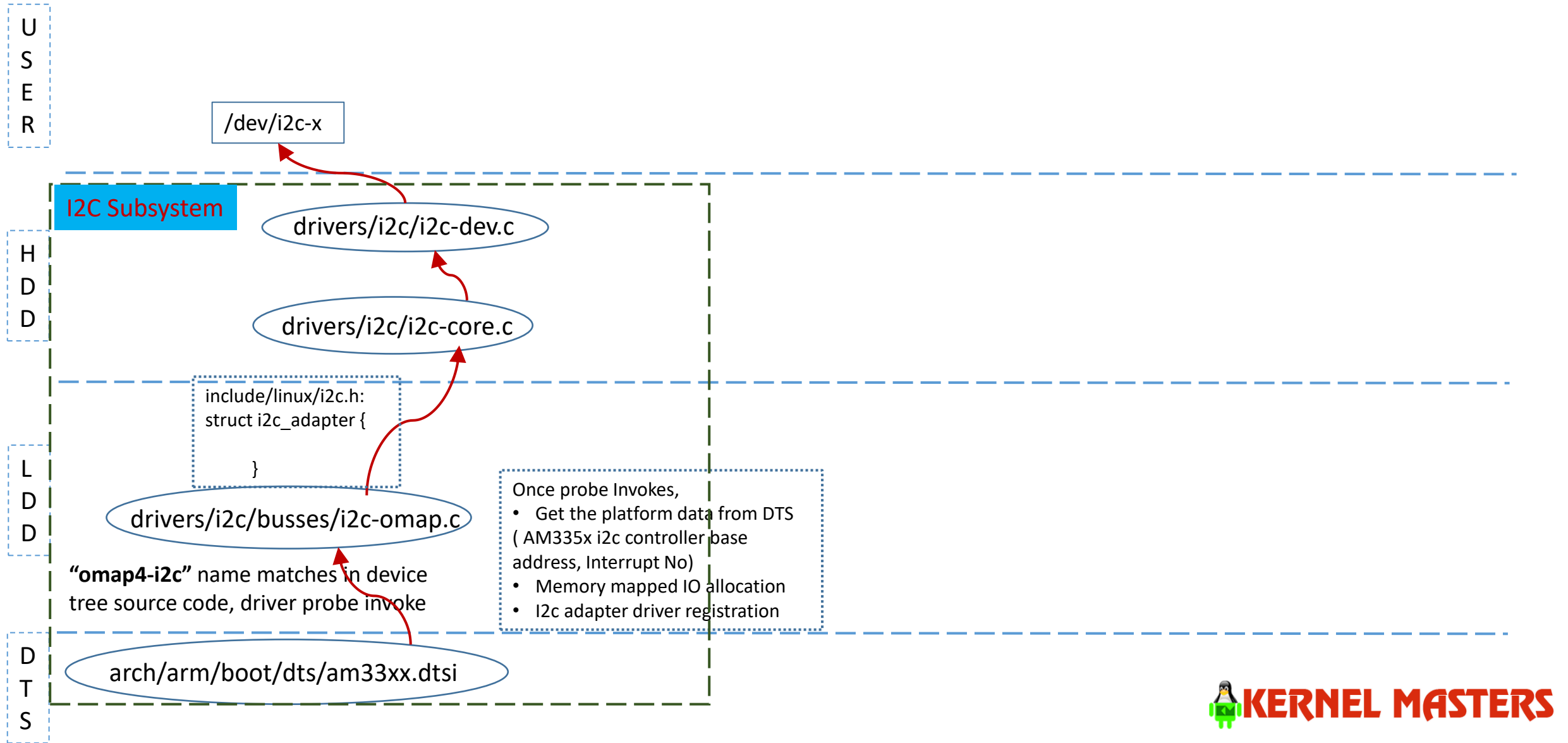
Call back functions

Driver Operations

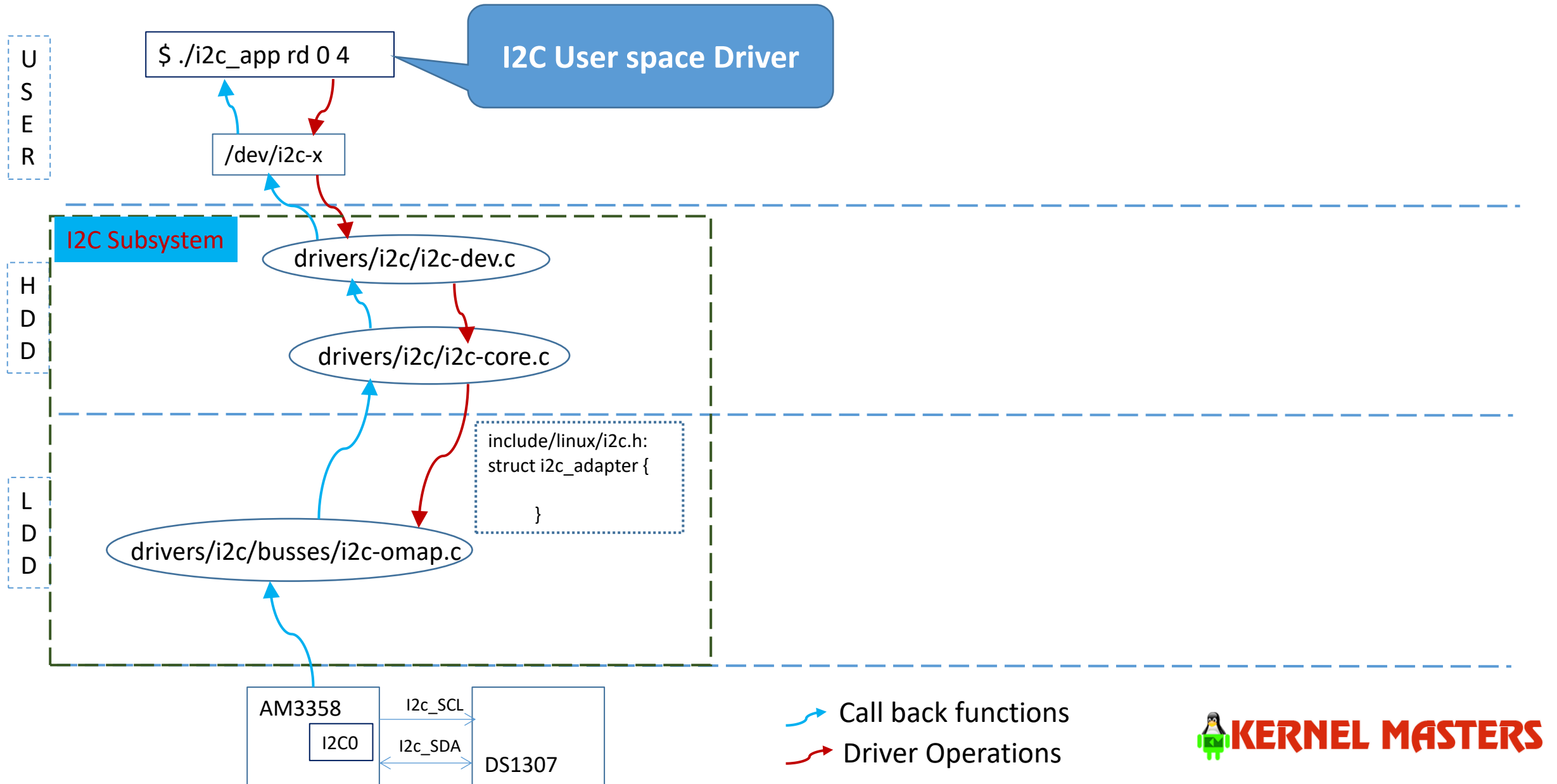
I2C Framework in Kernel

AM335X I2C Controller Basic Framework

I2C Master Initialization Framework in Kernel



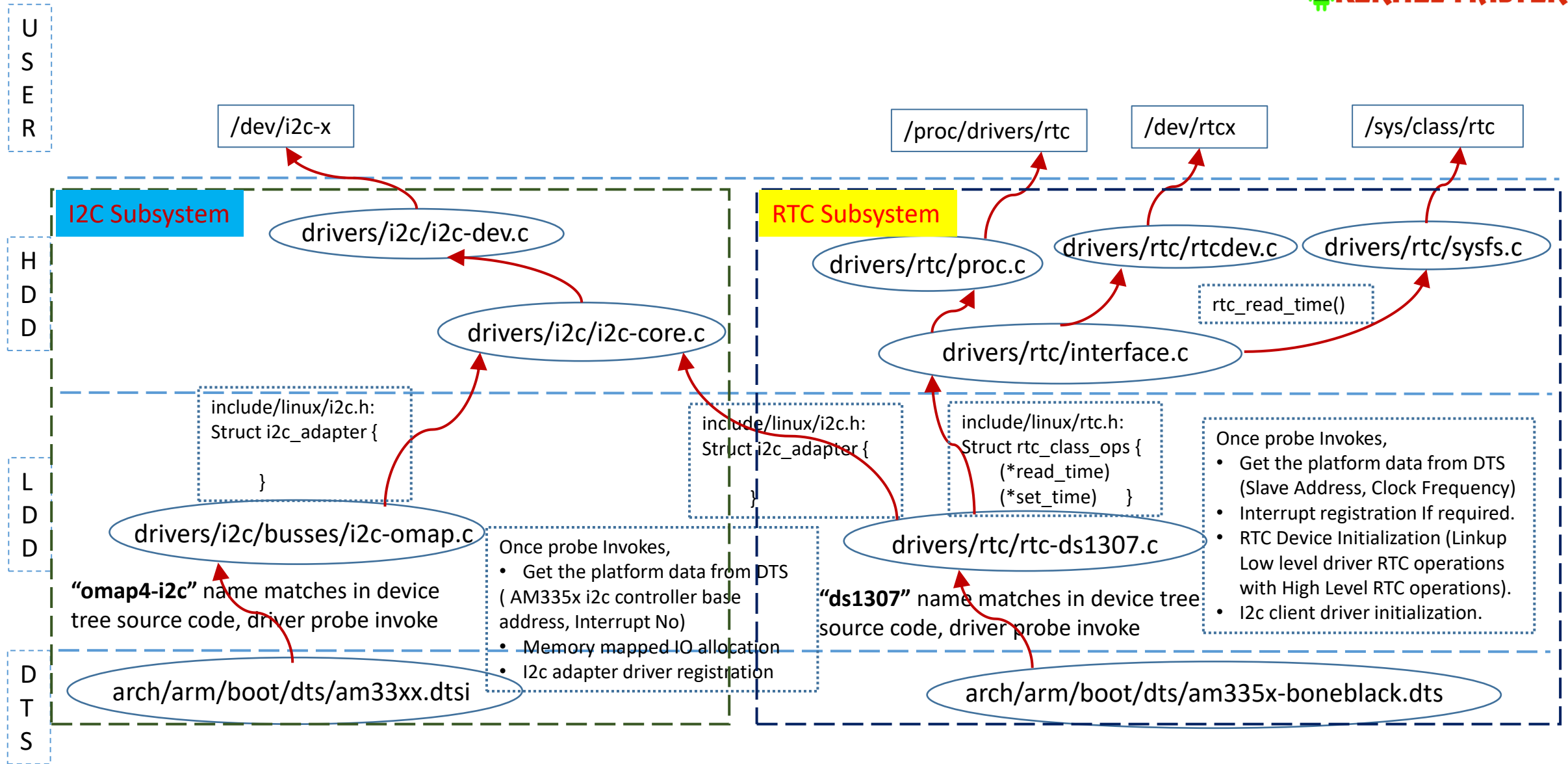
I2C Master Operation Framework in Kernel



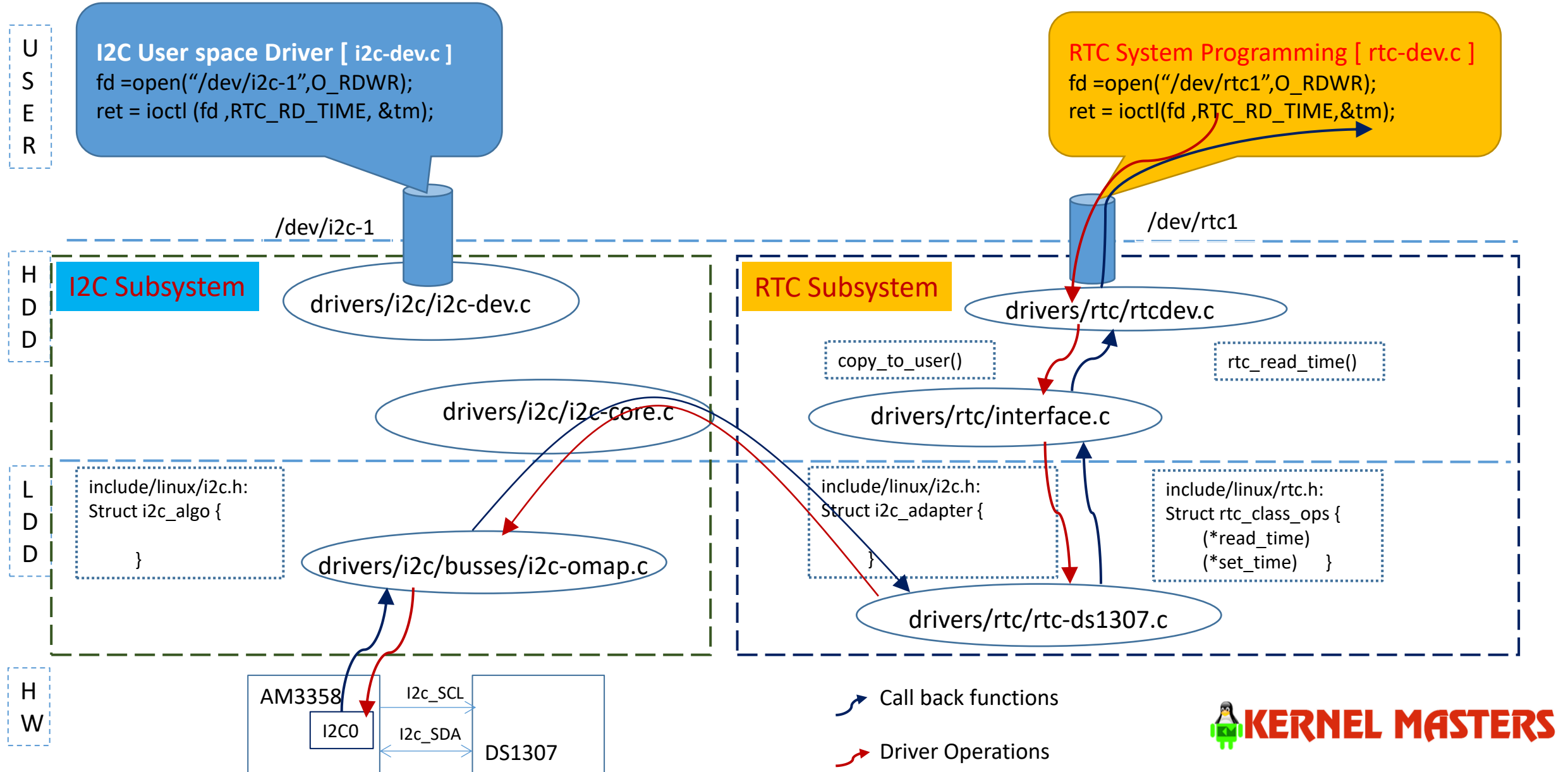
I2C Interface

DS1307 RTC Interface with AM335X I2C Controller

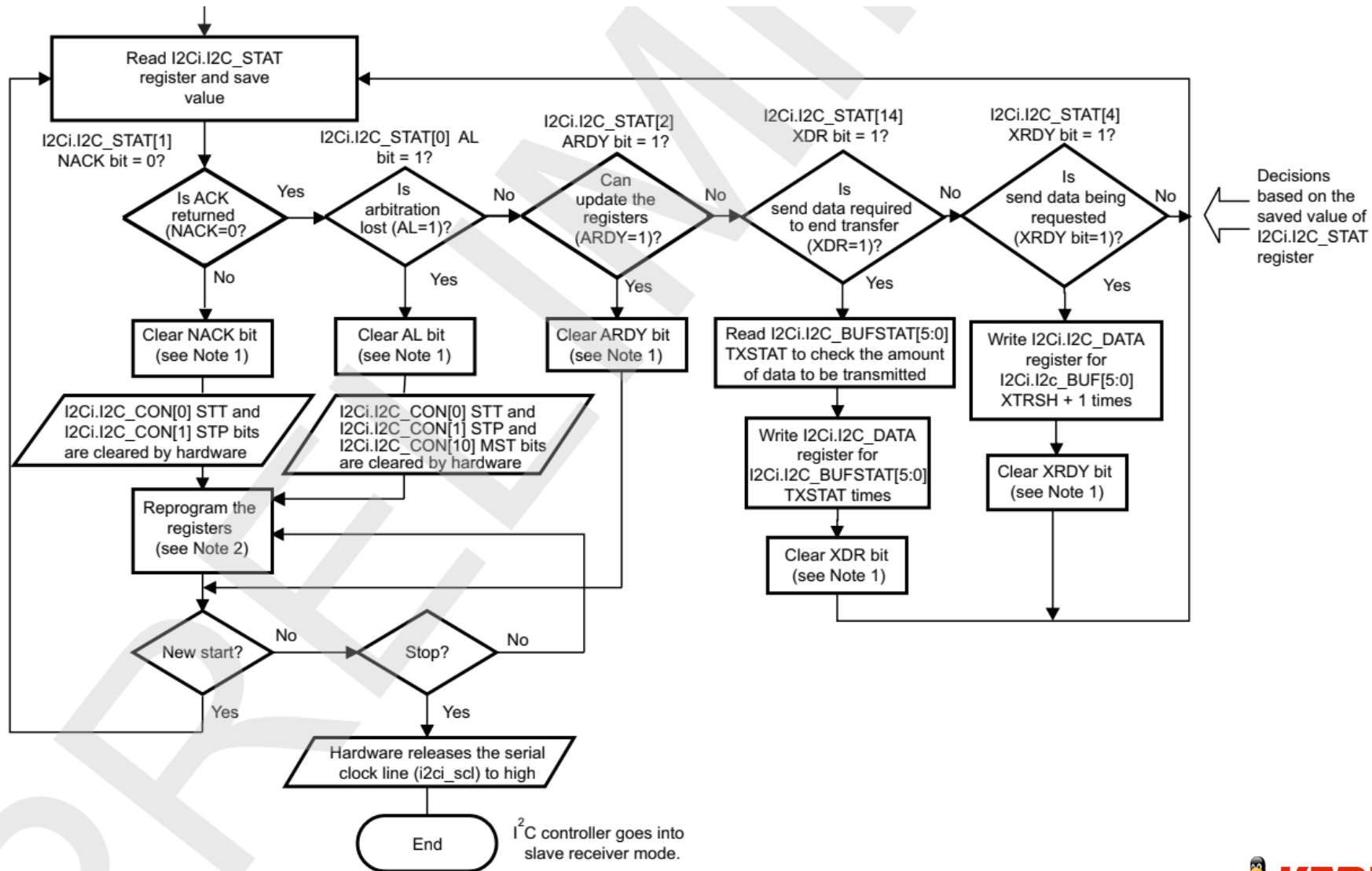
I2C & RTC Initialization Framework in Kernel



I2C & RTC Operation Framework in Kernel



I2c Master Transmit – Polling Method



I2c Master Transmit – Interrupt Method

