A. ECAD Design Information

This appendix contains information that supports the development of the PCB ECAD model for this device. It is intended to be used by PCB designers.

A.1 Part Number Indexing

Orderable Part Number	Number of Pins	Package Type	Package Code/POD Number			
R7F7015013AFD-C#AA2	176	LFQFP	PLQP0176KH-A			
R7F7015013AFD-C#BA2	176	LFQFP	PLQP0176KH-A			
R7F7015013AFD-C#KA2	176	LFQFP	PLQP0176KH-A			
R7F7015013AFE-C#AA2	176	LFQFP	PLQP0176KG-A			
R7F7015013AFE-C#BA2	176	LFQFP	PLQP0176KG-A			
R7F7015013AFE-C#KA2	176	LFQFP	PLQP0176KG-A			
R7F7015014AFD-C#AA2	176	LFQFP	PLQP0176KH-A			
R7F7015014AFD-C#BA2	176	LFQFP	PLQP0176KH-A			
R7F7015014AFD-C#KA2	176	LFQFP	PLQP0176KH-A			
R7F7015014AFE-C#AA2	176	LFQFP	PLQP0176KG-A			
R7F7015014AFE-C#BA2	176	LFQFP	PLQP0176KG-A			
R7F7015014AFE-C#KA2	176	LFQFP	PLQP0176KG-A			
R7F7015023AFD-C#AA2	176	LFQFP	PLQP0176KH-A			
R7F7015023AFD-C#BA2	176	LFQFP	PLQP0176KH-A			
R7F7015023AFD-C#KA2	176	LFQFP	PLQP0176KH-A			
R7F7015023AFE-C#AA2	176	LFQFP	PLQP0176KG-A			
R7F7015023AFE-C#BA2	176	LFQFP	PLQP0176KG-A			
R7F7015023AFE-C#KA2	176	LFQFP	PLQP0176KG-A			
R7F7015024AFD-C#AA2	176	LFQFP	PLQP0176KH-A			
R7F7015024AFD-C#BA2	176	LFQFP	PLQP0176KH-A			
R7F7015024AFD-C#KA2	176	LFQFP	PLQP0176KH-A			
R7F7015024AFE-C#AA2	176	LFQFP	PLQP0176KG-A			
R7F7015024AFE-C#BA2	176	LFQFP	PLQP0176KG-A			
R7F7015024AFE-C#KA2	176	LFQFP	PLQP0176KG-A			
R7F7015303AFD-C#AA2	100	LFQFP	PLQP0100KM-A			
R7F7015303AFD-C#BA2	100	LFQFP	PLQP0100KM-A			
R7F7015303AFD-C#KA2	100	LFQFP	PLQP0100KM-A			
R7F7015303AFE-C#AA2	100	LFQFP	PLQP0100KL-A			
R7F7015303AFE-C#BA2	100	LFQFP	PLQP0100KL-A			
R7F7015303AFE-C#KA2	100	LFQFP	PLQP0100KL-A			
R7F7015313AFD-C#AA2	100	LFQFP	PLQP0100KM-A			
R7F7015313AFD-C#BA2	100	LFQFP	PLQP0100KM-A			
R7F7015313AFD-C#KA2	100	LFQFP	PLQP0100KM-A			
R7F7015313AFE-C#AA2	100	LFQFP	PLQP0100KL-A			
R7F7015313AFE-C#BA2	100	LFQFP	PLQP0100KL-A			
R7F7015313AFE-C#KA2	100	LFQFP	PLQP0100KL-A			

A.2 **Symbol Pin Information**

A.2.1 176-LFQFP

Pin	Primar	Primary	Alternate Pin Name(s)						
Numb	y Pin	Electric							
er	Name	al Type							
1	P10_3	1/0	TAUD017/TAUD007/RIICOSCL/KR011/PWGA30/ADCA0TRG1/TAPA0VN/CSIH1SSI#/MEMC0CLK						
2	P10_4	1/0	TAUD019/TAUD009/RLIN21RX/KR012/ADCA0SEL0/ADCA0TRG2/TAPA0WP/CSIG0SSI#/ETNB0RXD2						
3	P10_5	1/0	TAUD0111/TAUD0011/RLIN21TX/KR013/ADCA0SEL1/TAPA0WN/CSIG0RYI/CSIG0RYO/ETNB0RXD3						
4	BVCC	Power	-						
5	BVSS	Power	-						
6	P10_15	1/0	CSIH3RYI/CSIH3RYO/PWGA24O/RLIN22RX/TAUB0I9/TAUB0O9/MEMCORD#						
7	P11_0	1/0	CSIH2RYI/CSIH2RYO/ADCA1TRG2/PWGA25O/RLIN22TX/TAUB0I11/TAUB0011/MEMC0WR#						

Pin	Primar	Primary	Alternate Pin Name(s)					
Numb	y Pin	Electric						
er	Name	al Type						
8	P11_8	1/0	CSIG1SSI#/RLIN35TX/PWGA48O/TAUB1I11/TAUB1O11/MEMCOCS0#					
9	P11_9	1/0	CSIG1SO/RLIN35RX/INTP15/PWGA49O/TAUB1I13/TAUB1O13/MEMC0CS1#					
10	P11_10	1/0	CSIG1SC/PWGA50O/TAUB1I15/TAUB1O15/MEMC0CS2#/ETNB0COL					
11	P11_11	1/0	CSIG1SI/RLIN25TX/PWGA51O/TAUB1IO/TAUB1O0/MEMC0CS3#/ETNB0RXDV					
12	P11_12	1/0	RLIN25RX/PWGA52O/TAUB1I2/TAUB1O2/MEMC0WAIT#					
13	P11_13	I/O	RLIN24RX/PWGA53O/TAUB1I4/TAUB1O4/MEMC0BEN0#/ETNB0CRS/ETNB0CRSDV/CSIG2RYI/CSIG2R YO					
14	P11_14	I/O	RLIN24TX/PWGA54O/TAUB1I6/TAUB1O6/MEMC0BEN1#/ETNB0RXERR/ETNB0RRXERR/CSIG2SSI#					
15	P12_3	I/O	RLIN27RX/ETNB0REFCLK/PWGA68O/CSIG2SI					
16	P12_4	I/O	RLIN27TX/PWGA69O/CSIG2SC/ETNB0MDIO					
17	P12_5	I/O	PWGA700/ETNB0MDC/CSIG2SO					
18	P0_0	1/0	TAUD012/TAUD002/RLIN20RX/CAN0TX/PWGA100/CSIH/DPO/RESETOUT#					
19	P0_1	1/0	TAUD014/TAUD004/CAN0RX/INTP0/RLIN20TX/PWGA110/CSIH0SI/APO					
20	P0_2	1/0	TAUD016/TAUD006/CAN1RX/INTP1/RLIN30TX/PWGA120/CSIH0SC/DPO					
21	P0_3	1/0	TAUD018/TAUD008/RLIN30RX/INTP10/CAN1TX/DPIN1/PWGA130/CSIH0SO					
22	EVCC	Power	- DUNI24DV/INTD14/C4N2TV/DWC4400/CCU44C/CSI 220/DDINI2					
23	P0_4 P0_5	I/O I/O	RLIN31RX/INTP11/CAN2TX/PWGA10O/CSIH1SI/SELDP0/DPIN8 CAN2RX/INTP2/RLIN31TX/DPIN9/SELDP1/CSIH1SO					
24	P0_5 P0_6		INTP2/DPIN10/SELDP2/CSIH1SC/PWGA35O					
25 26	P0_6 P0_11	I/O I/O	RIICOSDA/DPIN12/CSIH1CSS2/TAUB0I8/TAUB008/RLIN26RX/PWGA340					
27	P0_11 P0_12	1/0	RIICOSCL/DPIN13/PWGA450/TAUB0I10/TAUB0010/CSIGOSI/RLIN26TX					
28	P0_12 P0_13	1/0	RLIN32RX/INTP12/PWGA450/TAUB0I12/TAUB0012/CSIG0S0/CAN5RX/INTP5					
29	P0_13	1/0	RLIN32TX/PWGA470/TAUB0114/TAUB0014/CSIG0SC/CAN5TX					
30	P0_14 P1 0	1/0	RLIN321A/PWGA47O/TAOBOO14/TAOBOO14/CSIGUSC/CANSTA					
31	P1_0 P1_1	1/0	RLIN33TX					
32	P1_1 P1_2	1/0	CAN3RX/INTP3					
33	P1 3	1/0	CANSTX/DPIN23					
34	P1_12	1/0	CAN4RX/INTP4					
35	P1 13	1/0	CAN4TX					
36	P2_6	1/0	ADCA0SEL2					
37	EVSS	Power	-					
38	P8 2	I/O	TAUJ0I0/TAUJ000/DPIN2/CSIH0CSS0/INTP6/PWGA220/ADCA0I4S					
39	P8_10	1/0	CSIH3CSS3/DPIN14/PWGA420/ADCA0I17S					
40	P8_11	I/O	TAUJ112/TAUJ102/DPIN15/PWGA430/CSIH1CSS4/ADCA0I18S					
41	P8_12	1/0	TAUJ113/TAUJ103/DPIN16/PWGA440/CSIH1CSS5/ADCA0119S					
42	JP0_5	1/0	NMI/RTCA0OUT/TAUJ0I3/TAUJ0O3/DCURDY#/LPDCLKOUT					
43	JP0_4	I/O	DCUTRST#					
44	JP0_3	I/O	INTP3/CSCXFOUT/TAUJ012/TAUJ002/DCUTMS					
45	JP0_2	1/0	INTP2/TAUJ0I1/TAUJ0O1/DCUTCK/LPDCLK					
46	JP0_1	I/O	INTP1/TAUJ0I0/TAUJ000/DCUTDO/LPDO					
47	JP0_0	I/O	INTPO/DCUTDI/LPDI/LPDIO					
48	P2_1	I/O	RLIN27TX/CAN6TX					
49	P2_0	1/0	RLIN27RX/INTP6/CAN6RX					
50	P1_11	1/0	ADCA1TRG2/RLIN24TX/DPIN22					
51	P1_10	1/0	RLIN24RX/DPIN21					
52	P1_9	1/0	RLIN34TX/DPIN20					
53	P1_8	I/O	RLIN34RX/INTP14					
54	\RESET	Input	-					
55	EVCC	Power	-					
56	XT1	Input	-					
57	XT2	Input	IPO_0					
58	AWOVS S	Power	-					
59	AWOVC L	Power	-					
60	REGVC C	Power	-					
61	X2	Input	-					
62	X1	Input	-					
63	FLMD0	Input	-					

Pin	Primar	Primary	Alternate Pin Name(s)
Numb	y Pin	Electric	
er	Name	al Type	
64	P2_3	I/O	RLIN28TX
65	P2_2	I/O	RLIN28RX
66	JP0_6	I/O	EVTO#
67	P0_10	I/O	INTP3/CSIH1CSS1/DPIN11/RLIN22TX/TAUB0I6/TAUB006/CAN4TX
68	P0_9	I/O	INTP12/CSIH1CSSO/DPIN7/RLIN22RX/TAUB0I4/TAUB0O4/CAN4RX/INTP4
69	P0_8	I/O	RLIN21TX/DPIN6/CSIH0CSS6/CSIH1SSI#/TAUB0I2/TAUB0O2/CAN3TX
70	P0_7	1/0	RLIN21RX/DPIN5/CSCXFOUT/CSIH1RYI/CSIH1RYO/TAUB0I0/TAUB000/CAN3RX/INTP3
71	EVSS	Power	-
72	P1_7	1/0	ADCA1TRG1/RLIN25TX/DPIN19
73	P1_6	1/0	RLIN25RX/DPIN18
74 75	P1_5 P1_4	I/O I/O	ADCA1TRGO/RLIN35TX/DPIN17 RLIN35RX/INTP15
76	_	1/0	RLIN29RX/ADCA0SEL0
77	P2_4 P2_5	1/0	RLIN29TX/ADCA0SEL0 RLIN29TX/ADCA0SEL1
78	P1 14	1/0	RLIN23TX/ADCAGSELT
79	P1_14 P1_15	1/0	RLIN23TX
80	P8 0	1/0	TAUJOIO/TAUJOOO/DPIN2/PWGA14O/INTP4/CSIHOCSSO/ADCAOIOS
81	P8 1	1/0	TAPA0ESO/TAUJ001/DPIN0/PWGA150/INTP5/CSIH1CSS3/ADCA0I1S
82	P8_3	1/0	TAUJO1/TAUJO01/DPIN3/CSIHOCSS1/INTP7/PWGA230/ADCA015S
83	P8 4	1/0	TAUJOI2/TAUJOO2/DPIN4/CSIHOCSS2/INTP8/PWGA36O/ADCAOI6S
84	P8_5	1/0	TAUJ0I3/TAUJ0O3/CSIH0CSS3/INTP9/PWGA37O/ADCA0I7S
85	P8_6	1/0	NMI/CSIH0CSS4/PWGA380/RTCA0OUT/ADCA0I8S
86	P8_7	I/O	CSIH3CSSO/PWGA39O/ADCA0114S
87	P8_8	1/0	CSIH3CSS1/PWGA400/ADCA0I15S
88	P8_9	I/O	CSIH3CSS2/PWGA410/ADCA0I16S
89	A0VSS	Power	-
90	AOVREF	Power	-
91	AP0_15	I/O	ADCA0I15
92	AP0_14	1/0	ADCA0I14
93	AP0_13	I/O	ADCA0I13
94	AP0_12	I/O	ADCA0I12
95	AP0_11	I/O	ADCA0I11
96	AP0_10	I/O	ADCA0I10
97	AP0_9	1/0	ADCA019
98	AP0_8	1/0	ADCA018
99	AP0_7	1/0	ADCA0I7
100	AP0_6	1/0	ADCAOLS
101	APO_5	1/0	ADCA015
102	APO_4	1/0	ADCA013
103 104	AP0_3 AP0_2	I/O I/O	ADCA013 ADCA012
105	AP0_2 AP0 1	1/0	ADCA012 ADCA011
106	APO_1 APO 0	1/0	ADCA010
107	EVSS	Power	-
108	P9 0	1/0	NMI/PWGA8O/TAUD0I0/TAUD000/ADCA0TRG0/CSIH2CSS0/KR0I4/ADCA0I2S
109	P9_1	1/0	INTP11/PWGA90/TAUD012/TAUD002/KR015/CSIH2CSS1/ADCA013S
110	P9_2	1/0	KR0I6/PWGA200/TAPA0ESO/CSIH2CSS2/ADCA0I9S
111	P9_3	1/0	KR0I7/PWGA210/CSIH2CSS3/TAUJ1I1/TAUJ101/ADCA0I10S
112	P9_4	1/0	CSIH0CSS5/PWGA330/TAUJ1I0/TAUJ100/ADCA0I11S
113	ISOVCL	Power	-
114	ISOVSS	Power	-
115	P20_3	I/O	CAN4TX/PWGA67O/RLIN29TX/CSIG3RYI/CSIG3RYO
116	P20_2	I/O	CAN4RX/INTP4/PWGA66O/RLIN29RX/CSIG3SC
117	P20_1	I/O	RLIN26TX/PWGA65O/CAN6TX/CSIG3SO
118	P20_0	I/O	RLIN26RX/PWGA64O/INTP6/CAN6RX/CSIG3SI
119	P20_5	I/O	RLIN23TX/PWGA60O
120	P20_4	I/O	RLIN23RX/PWGA59O/CSIG3SSI#
121	EVCC	Power	-
122	AP1_11	I/O	ADCA1I11

Pin	Primar	Primary	Alternate Pin Name(s)					
Numb	y Pin	Electric						
er	Name	al Type						
123	AP1_10	1/0	ADCA1I10					
124	AP1_9	I/O	ADCA1I9					
125	AP1_8	I/O	ADCA1I8					
126	AP1_7	I/O	ADCA1I7					
127	AP1_6	I/O	ADCA1I6					
128	AP1_5	I/O	ADCA1I5					
129	AP1_4	1/0	ADCA1I4					
130	AP1_3	1/0	ADCA1I3					
131	AP1_2	I/O	ADCA1I2					
132	AP1_1	I/O	ADCA1I1					
133	AP1_0	I/O	ADCA1IO					
134	AP1_15	I/O	ADCA1I15					
135	AP1_14	I/O	ADCA1I14					
136	AP1_13	I/O	ADCA1I13					
137	AP1_12	I/O	ADCA1I12					
138	A1VREF	Power	-					
139	A1VSS	Power	-					
140	BVCC	Power	-					
141	ISOVCL	Power	•					
142	ISOVSS	Power	-					
143	P18_0	1/0	CSIG1RYI/CSIG1RYO/ETNBOLINK/PWGA61O/ADCA1IOS					
144	P18_1	1/0	PWGA62O/ETNB0TXD0/ETNB0RTXD0/ADCA1i1S					
145	P18_2	1/0	PWGA630/ETNB0TXD1/ETNB0RTXD1/ADCA1i2S					
146	P18_3	1/0	PWGA710/ETNB0TXD2/ADCA113S					
147	P18_4	1/0	CSIH1CSS4/ETNBOTXD3/ADCA1I4S					
148	P18_5	1/0	CSIH1CSS5/ETNBOTXEN/ETNBORTXEN/ADCA1I5S					
149	P18_6	1/0	ETNBOTXERR/ADCA4176					
150	P18_7	I/O	ETNB0TXCLK/ADCA1i7S					
151	BVSS	Power	- TALIDO(42 /TALIDOO(42 /CCICOCO /ENICAOTINO /ADCAOCEL 2 /CANIADY /INITDA /AAFAACOADO					
152 153	P10_6	I/O I/O	TAUD0I13/TAUD0013/CSIG0SO/ENCA0TIN0/ADCA0SEL2/CAN1RX/INTP1/MEMC0AD0 TAUD0I15/TAUD0015/CSIG0SC/ENCA0TIN1/PWGA40/CAN1TX/MEMC0AD1					
154	P10_7 FLMD1	Input	P10 8/TAUD0110/TAUD0010/CSIG0SI/FLXA0TXDB/ENCA0EC/PWGA50/MEMC0AD2					
155	P10 9	I/O	TAUD0I12/TAUD0012/RLIN30RX/INTP10/ENCA0E0/PWGA6O/CSIH0RYI/CSIH0RYO/MEMC0AD3/FLX					
133	F10_9	1/0	AORXDB					
156	P10 10	I/O	TAUD0I14/TAUD0014/RLIN30TX/ENCA0E1/PWGA70/CSIH0CSS1/MEMC0AD4					
157	P10 11	1/0	PWGA16O/RLIN31RX/INTP11/FLXA0TXENA/CSIH1CSS0/TAUB0I1/TAUB001/MEMC0AD5					
158	P10 12	1/0	PWGA17O/FLXAOSTPWT/RLIN31TX/CSIH1CSS1/TAUB0I3/TAUB0O3/MEMC0AD6					
159	P10_13	1/0	CSIHOSSI#/PWGA180/RLIN32RX/INTP12/FLXA0TXENB/TAUB0I5/TAUB005/MEMC0AD7					
160	P10 14	1/0	ADCA1TRG0/PWGA190/FLXA0RXDA/RLIN32TX/CSIH3SSI#/TAUB017/TAUB007/MEMC0AD8					
161	P11_1	1/0	CSIH2SSI#/FLXAOTXDA/RLIN2ORX/CSIH0CSS7/PWGA26O/TAUB0I13/TAUB0013/MEMC0AD9					
162	P11_2	I/O	CSIH2SO/RLIN20TX/PWGA27O/TAUB0I15/TAUB0O15/MEMC0AD10					
163	P11_3	I/O	CSIH2SC/CAN3RX/INTP3/PWGA28O/TAUB1I1/TAUB1O1/MEMC0AD11					
164	P11_4	I/O	CSIH2SI/CAN3TX/PWGA29O/TAUB1I3/TAUB1O3/MEMC0AD12					
165	P11_5	I/O	CAN5RX/INTP5/RLIN33TX/PWGA30O/CSIH3SI/TAUB1I5/TAUB105/MEMC0AD13					
166	P11_6	I/O	RLIN33RX/INTP13/CAN5TX/ADCA1TRG1/PWGA310/CSIH3S0/TAUB117/TAUB107/MEMC0AD14					
167	P11_7	I/O	INTP5/PWGA32O/CSIH3SC/TAUB1I9/TAUB1O9/MEMC0AD15					
168	P11_15	I/O	CAN2RX/INTP2/CSIH2CSS4/PWGA550/TAUB1I8/TAUB108/MEMC0ASTB#					
169	P12_0	I/O	CAN2TX/PWGA56O/TAUB1I10/TAUB1O10/MEMC0A16					
170	P12_1	I/O	RLIN34RX/INTP14/CSIH2CSS5/PWGA57O/TAUB1I12/TAUB1O12/MEMC0A17					
171	P12_2	I/O	RLIN34TX/PWGA58O/TAUB1114/TAUB1O14/MEMC0A18					
172	BVCC	Power	<u>-</u>					
173	BVSS	Power	-					
174	P10_0	I/O	TAUD0I1/TAUD001/CAN0RX/INTP0/CSCXFOUT/PWGA00/TAPA0UP/CSIH1SI/MEMC0A19/ETNB0RXC					
			LK					
175	P10_1	I/O	TAUD013/TAUD003/CAN0TX/PWGA10/TAPA0UN/CSIH1SC/ETNB0RXD0/ETNB0RRXD0/MODE0					
176	P10_2	I/O	TAUD015/TAUD005/RIIC0SDA/KR010/PWGA2O/ADCA0TRG0/TAPA0VP/CSIH1SO/ETNB0RXD1/ETNB0					
			RRXD1/MODE1					

A.2.2 176-LFQFP

Pin	Primar	Primary	Alternate Pin Name(s)				
Numb	y Pin	Electric					
er	Name	al Type					
1	P10 3	1/0	TAUD017/TAUD007/RIIC0SCL/KR011/PWGA30/ADCA0TRG1/TAPA0VN/CSIH1SSI#/MEMC0CLK				
2	P10 4	1/0	TAUDOI9/TAUDOO9/RLIN21RX/KR0I2/ADCA0SEL0/ADCA0TRG2/TAPA0WP/CSIG0SSI#/ETNB0RXD2				
3	P10 5	1/0	TAUD0111/TAUD0011/RLIN21TX/KR0I3/ADCA0SEL1/TAPA0WN/CSIG0RYI/CSIG0RYO/ETNB0RXD3				
4	BVCC	Power	-				
5	BVSS	Power	_				
6	P10_15	I/O	CSIH3RYI/CSIH3RYO/PWGA24O/RLIN22RX/TAUB0I9/TAUB0O9/MEMCORD#				
7	P11_0	1/0	CSIH2RYI/CSIH2RYO/ADCA1TRG2/PWGA25O/RLIN22TX/TAUB0111/TAUB0011/MEMC0WR#				
8	P11_8	1/0	CSIG1SSI#/RLIN35TX/PWGA48O/TAUB1I11/TAUB1011/MEMC0CS0#				
9	P11_9	1/0	CSIG1SO/RLIN35RX/INTP15/PWGA49O/TAUB1I13/TAUB1013/MEMC0CS1#				
10	P11_10	1/0	CSIG1SC/PWGA500/TAUB1I15/TAUB1015/MEMC0CS2#/ETNB0COL				
11	P11 11	1/0	CSIG1SI/RLIN25TX/PWGA510/TAUB1I0/TAUB100/MEMC0CS3#/ETNB0RXDV				
12	P11 12	1/0	RLIN25RX/PWGA520/TAUB1I2/TAUB102/MEMC0WAIT#				
13	P11_13	1/0	RLIN24RX/PWGA530/TAUB114/TAUB104/MEMC0BEN0#/ETNB0CRS/ETNB0CRSDV/CSIG2RYI/CSIG2R				
13	111_13	1,0	YO				
14	P11_14	I/O	RLIN24TX/PWGA54O/TAUB1I6/TAUB1O6/MEMC0BEN1#/ETNB0RXERR/ETNB0RRXERR/CSIG2SSI#				
15	P12 3	1/0	RLIN27RX/ETNBOREFCLK/PWGA68O/CSIG2SI				
16	P12_3	1/0	RLIN27TX/PWGA69O/CSIG2SC/ETNB0MDIO				
17	P12_4 P12_5	1/0	PWGA700/ETNB0MDC/CSIG2SO				
18	P0_0	1/0	TAUD012/TAUD002/RLIN20RX/CAN0TX/PWGA100/CSIH0SSI#/DPO/RESETOUT#				
19	P0_0 P0_1	1/0	TAUD014/TAUD004/CAN0RX/INTP0/RLIN20TX/PWGA110/CSIH0SI/APO				
20	P0_1 P0_2	1/0	TAUD0I6/TAUD006/CAN1RX/INTP1/RLIN30TX/PWGA110/CSIH0SI/APO TAUD0I6/TAUD006/CAN1RX/INTP1/RLIN30TX/PWGA120/CSIH0SC/DP0				
21	P0_2	1/0	TAUDOIS/TAUDOOS/CANTRX/INTF1/ACINSOTX/FWGA12O/CSINOSC/DFO TAUDOIS/TAUDOOS/RLIN3ORX/INTP10/CAN1TX/DPIN1/PWGA13O/CSIHOSO				
22			IAUDUIO/ IAUDUUO/ KLINSUKA/ INTPIU/ CANTTA/ DPINT/ PWGATSU/ CSIRUSU				
23	EVCC	Power	- DLINI24 DV /INITD14 /CAN2TV /DN/CA100 /CCILI4CI /CFI DD0 /DDINI0				
24	P0_4	I/O I/O	RLIN31RX/INTP11/CAN2TX/PWGA100/CSIH1SI/SELDP0/DPIN8 CAN2RX/INTP2/RLIN31TX/DPIN9/SELDP1/CSIH1SO				
	P0_5	•					
25	P0_6	1/0	INTP2/DPIN10/SELDP2/CSIH1SC/PWGA350				
26	P0_11	1/0	RIICOSDA/DPIN12/CSIH1CSS2/TAUBOI8/TAUBOO8/RLIN26RX/PWGA34O				
27	P0_12	1/0	RIICOSCL/DPIN13/PWGA45O/TAUB0I10/TAUB0010/CSIGOSI/RLIN26TX				
28	P0_13	1/0	RLIN32RX/INTP12/PWGA46O/TAUB0I12/TAUB0O12/CSIGOSO/CAN5RX/INTP5				
29	P0_14	1/0	RLIN32TX/PWGA47O/TAUB0114/TAUB0014/CSIG0SC/CAN5TX				
30	P1_0	1/0	RLIN33RX/INTP13				
31	P1_1	1/0	RLIN33TX				
32	P1_2	1/0	CANSTY/DDIVICE				
33	P1_3	1/0	CANSTX/DPIN23				
34	P1_12	1/0	CAN4RX/INTP4				
35	P1_13	1/0	CANATX				
36	P2_6	1/0	ADCA0SEL2				
37	EVSS	Power	TAULOIO (TAULOGO / PRINCE / SCHOOLOGO / NATION / SCHOOLOGO / SCHOO				
38	P8_2	1/0	TAUJ010/TAUJ000/DPIN2/CSIH0CSS0/INTP6/PWGA220/ADCA0I4S				
39	P8_10	1/0	CSIH3CSS3/DPIN14/PWGA420/ADCA0117S				
40	P8_11	1/0	TAUJ112/TAUJ102/DPIN15/PWGA430/CSIH1CSS4/ADCA0118S				
41	P8_12	1/0	TAUJ113/TAUJ103/DPIN16/PWGA440/CSIH1CSS5/ADCA0119S				
42	JP0_5	1/0	NMI/RTCA0OUT/TAUJ0I3/TAUJ003/DCURDY#/LPDCLKOUT				
43	JP0_4	1/0	DCUTRST#				
44	JP0_3	1/0	INTP3/CSCXFOUT/TAUJ0I2/TAUJ002/DCUTMS				
45	JP0_2	1/0	INTP2/TAUJ011/TAUJ001/DCUTCK/LPDCLK				
46	JP0_1	1/0	INTP1/TAUJ0I0/TAUJ000/DCUTDO/LPD0				
47	JP0_0	1/0	INTPO/DCUTDI/LPDIO				
48	P2_1	1/0	RLIN27TX/CAN6TX				
49	P2_0	1/0	RLIN27RX/INTP6/CAN6RX				
50	P1_11	1/0	ADCA1TRG2/RLIN24TX/DPIN22				
51	P1_10	1/0	RLIN24RX/DPIN21				
52	P1_9	1/0	RLIN34TX/DPIN20				
53	P1_8	I/O	RLIN34RX/INTP14				
54	\RESET	Input	-				
55	EVCC	Power	-				
56	XT1	Input	-				
57	XT2	Input	IPO_0				

Pin	Primar	Primary	Alternate Pin Name(s)
Numb	y Pin	Electric	
er	Name	al Type	
58	AWOVS S	Power	-
59	AWOVC L	Power	-
60	REGVC C	Power	-
61	X2	Input	-
62	X1	Input	-
63	FLMD0	Input	-
64	P2_3	I/O	RLIN28TX
65	P2_2	I/O	RLIN28RX
66	JP0_6	1/0	-
67	P0_10	1/0	INTP3/CSIH1CSS1/DPIN11/RLIN22TX/TAUB0I6/TAUB006/CAN4TX
68	P0_9	1/0	INTP12/CSIH1CSS0/DPIN7/RLIN22RX/TAUB0I4/TAUB0O4/CAN4RX/INTP4
69	P0_8	1/0	RLIN21TX/DPIN6/CSIH0CSS6/CSIH1SSI#/TAUB0I2/TAUB0O2/CAN3TX
70	P0_7	I/O	RLIN21RX/DPIN5/CSCXFOUT/CSIH1RYI/CSIH1RYO/TAUB0I0/TAUB0O0/CAN3RX/INTP3
71	EVSS	Power	-
72	P1_7	1/0	ADCA1TRG1/RLIN25TX/DPIN19
73	P1_6	1/0	RLIN25RX/DPIN18
74	P1_5	1/0	ADCA1TRG0/RLIN35TX/DPIN17
75	P1_4	1/0	RLIN35RX/INTP15
76	P2_4	1/0	RLIN29RX/ADCA0SEL0
77	P2_5	1/0	RLIN29TX/ADCA0SEL1
78	P1_14	1/0	RLIN23RX
79	P1_15	1/0	RLIN23TX TAUJ0I0/TAUJ000/DPIN2/PWGA140/INTP4/CSIH0CSS0/ADCA0I0S
80	P8_0	I/O I/O	TAPA0ESO/TAUJ001/DPIN0/PWGA150/INTP5/CSIH1CSS3/ADCA0I1S
81 82	P8_1 P8_3	1/0	TAUJ011/TAUJ001/DPIN0/PWGA150/INTP7/PWGA230/ADCA015S
83	P8 4	1/0	TAUJ012/TAUJ002/DPIN4/CSIH0CSS2/INTP8/PWGA250/ADCA016S
84	P8_5	1/0	TAUJ013/TAUJ003/CSIH0CSS3/INTP9/PWGA370/ADCA017S
85	P8 6	1/0	NMI/CSIH0CSS4/PWGA38O/RTCA0OUT/ADCA0I8S
86	P8 7	1/0	CSIH3CSS0/PWGA390/ADCA0I14S
87	P8_8	1/0	CSIH3CSS1/PWGA400/ADCA0I15S
88	P8 9	1/0	CSIH3CSS2/PWGA410/ADCA0I16S
89	AOVSS	Power	-
90	AOVREF	Power	-
91	AP0_15	1/0	ADCA0115
92	AP0_14	I/O	ADCA0114
93	AP0_13	1/0	ADCA0I13
94	AP0_12	1/0	ADCA0I12
95	AP0_11	1/0	ADCA0111
96	AP0_10	1/0	ADCA0I10
97	AP0_9	I/O	ADCA019
98	AP0_8	1/0	ADCA018
99	AP0_7	1/0	ADCA017
100	AP0_6	1/0	ADCA016
101	AP0_5	1/0	ADCA015
102	APO_4	1/0	ADCA014
103	APO_3	1/0	ADCA013
104	APO_2	1/0	ADCA012
105 106	APO_1	I/O I/O	ADCADIO
	APO_0 EVSS		ADCA010
107 108	P9_0	Power I/O	- NMI/PWGA80/TAUD0I0/TAUD000/ADCA0TRG0/CSIH2CSS0/KR0I4/ADCA0I2S
108	P9_0 P9_1	1/0	INTP11/PWGA90/TAUD0I2/TAUD002/KR0I5/CSIH2CSS1/ADCA0I3S
1109	P9_1 P9_2	1/0	KR0I6/PWGA200/TAPA0ESO/CSIH2CSS2/ADCA0I9S
111	P9_2 P9_3	1/0	KR0IO/PWGA200/TAPA0ESO/CSIH2CSS2/ADCA0I9S KR0I7/PWGA210/CSIH2CSS3/TAUJ111/TAUJ101/ADCA0I10S
112	P9_3 P9_4	1/0	CSIH0CSS5/PWGA330/TAUJ110/TAUJ101/ADCA0110S
113	ISOVCL	Power	
114	ISOVSS	Power	-
114	120422	rower	<u>-</u>

Pin	Primar	Primary	Alternate Pin Name(s)						
Numb	y Pin	Electric							
er	Name	al Type							
115	P20_3	I/O	CAN4TX/PWGA67O/RLIN29TX/CSIG3RYI/CSIG3RYO						
116	P20_2	1/0	CAN4RX/INTP4/PWGA66O/RLIN29RX/CSIG3SC						
117	P20_1	I/O	RLIN26TX/PWGA65O/CAN6TX/CSIG3SO						
118	P20_0	1/0	RLIN26RX/PWGA64O/INTP6/CAN6RX/CSIG3SI						
119	P20_5	1/0	RLIN23TX/PWGA600						
120	P20_4	I/O	RLIN23RX/PWGA59O/CSIG3SSI#						
121 122	EVCC AP1 11	Power I/O	- ADCA1I11						
123	AP1_11 AP1 10	1/0							
124	AP1_10 AP1_9	1/0	ADCA1I10 ADCA1I9						
125	AP1 8	1/0	ADCA1I8						
126	AP1 7	1/0	ADCA117						
127	AP1 6	1/0	ADCA116						
128	AP1 5	1/0	ADCA1I5						
129	AP1_4	1/0	ADCA1I4						
130	AP1_3	1/0	ADCA1I3						
131	AP1_2	I/O	ADCA1I2						
132	AP1_1	I/O	ADCA1I1						
133	AP1_0	I/O	ADCA1IO						
134	AP1_15	I/O	ADCA1I15						
135	AP1_14	I/O	ADCA1I14						
136	AP1_13	I/O	ADCA1I13						
137	AP1_12	I/O	ADCA1I12						
138	A1VREF	Power	-						
139	A1VSS	Power	-						
140 141	BVCC ISOVCL	Power Power	-						
141	ISOVEL	Power	<u>.</u>						
143	P18_0	I/O	CSIG1RYI/CSIG1RYO/ETNB0LINK/PWGA610/ADCA1I0S						
144	P18 1	1/0	PWGA62O/ETNB0TXD0/ETNB0RTXD0/ADCA11S						
145	P18 2	1/0	PWGA63O/ETNB0TXD1/ETNB0RTXD1/ADCA1i2S						
146	P18_3	1/0	PWGA710/ETNB0TXD2/ADCA1I3S						
147	P18_4	1/0	CSIH1CSS4/ETNB0TXD3/ADCA1I4S						
148	P18_5	I/O	CSIH1CSS5/ETNBOTXEN/ETNBORTXEN/ADCA1I5S						
149	P18_6	I/O	ETNBOTXERR/ADCA116S						
150	P18_7	1/0	ETNBOTXCLK/ADCA117S						
151	BVSS	Power	-						
152	P10_6	I/O	TAUD0113/TAUD0013/CSIG0SO/ENCA0TIN0/ADCA0SEL2/CAN1RX/INTP1/MEMC0AD0						
153	P10_7	I/O	TAUD0115/TAUD0015/CSIG0SC/ENCA0TIN1/PWGA4O/CAN1TX/MEMC0AD1						
154	FLMD1	Input	P10_8/TAUD0I10/TAUD0O10/CSIGOSI/FLXAOTXDB/ENCA0EC/PWGA50/MEMC0AD2						
155	P10_9	I/O	TAUD0I12/TAUD0012/RLIN30RX/INTP10/ENCA0E0/PWGA6O/CSIH0RYI/CSIH0RYO/MEMC0AD3/FLX						
156	P10 10	I/O	A0RXDB TAUD0I14/TAUD0O14/RLIN30TX/ENCA0E1/PWGA70/CSIH0CSS1/MEMC0AD4						
157	P10_10 P10_11	1/0	PWGA16O/RLIN31RX/INTP11/FLXA0TXENA/CSIH1CSS0/TAUB0I1/TAUB001/MEMC0AD5						
158	P10_11	1/0	PWGA170/FLXA0STPWT/RLIN31TX/CSIH1CSS1/TAUB0I3/TAUB0O3/MEMC0AD6						
159	P10_12	1/0	CSIHOSSI#/PWGA18O/RLIN32RX/INTP12/FLXAOTXENB/TAUBOI5/TAUBOO5/MEMCOAD7						
160	P10_13	1/0	ADCA1TRG0/PWGA190/FLXA0RXDA/RLIN32TX/CSIH3SSI#/TAUB017/TAUB007/MEMC0AD8						
161	P11_1	1/0	CSIH2SSI#/FLXAOTXDA/RLIN2ORX/CSIH0CSS7/PWGA26O/TAUB0I13/TAUB0013/MEMC0AD9						
162	P11_2	1/0	CSIH2SO/RLIN20TX/PWGA27O/TAUB0I15/TAUB0O15/MEMC0AD10						
163	P11_3	1/0	CSIH2SC/CAN3RX/INTP3/PWGA28O/TAUB1I1/TAUB1O1/MEMC0AD11						
164	P11_4	I/O	CSIH2SI/CAN3TX/PWGA29O/TAUB1I3/TAUB1O3/MEMCOAD12						
165	P11_5	I/O	CAN5RX/INTP5/RLIN33TX/PWGA300/CSIH3SI/TAUB1I5/TAUB105/MEMC0AD13						
166	P11_6	I/O	RLIN33RX/INTP13/CAN5TX/ADCA1TRG1/PWGA310/CSIH3SO/TAUB1I7/TAUB107/MEMC0AD14						
167	P11_7	I/O	INTP5/PWGA32O/CSIH3SC/TAUB1I9/TAUB1O9/MEMC0AD15						
168	P11_15	I/O	CAN2RX/INTP2/CSIH2CSS4/PWGA55O/TAUB1I8/TAUB1O8/MEMCOASTB#						
169	P12_0	1/0	CAN2TX/PWGA56O/TAUB1I10/TAUB1010/MEMC0A16						
170	P12_1	I/O	RLIN34RX/INTP14/CSIH2CSS5/PWGA570/TAUB1I12/TAUB1012/MEMC0A17						
171	P12_2	1/0	RLIN34TX/PWGA58O/TAUB1I14/TAUB1O14/MEMCOA18						
172	BVCC	Power	•						

Pin	Primar	Primary	Alternate Pin Name(s)
Numb	y Pin	Electric	
er	Name	al Type	
173	BVSS	Power	-
174	P10_0	1/0	TAUD0I1/TAUD001/CAN0RX/INTP0/CSCXFOUT/PWGA00/TAPA0UP/CSIH1SI/MEMC0A19/ETNB0RXC
			LK
175	P10_1	1/0	TAUD013/TAUD003/CAN0TX/PWGA10/TAPA0UN/CSIH1SC/ETNB0RXD0/ETNB0RRXD0/MODE0
176	P10_2	1/0	TAUD015/TAUD005/RIIC0SDA/KR010/PWGA2O/ADCA0TRG0/TAPA0VP/CSIH1SO/ETNB0RXD1/ETNB0
			RRXD1/MODE1

A.2.3 100-LFQFP

Pin Number	Primary Pin	Primary	Alternate Pin Name(s)				
	Name	Electrical					
		Type					
1	P10_3	I/O	TAUD017/TAUD007/RIIC0SCL/KR011/PWGA30/ADCA0TRG1/CSIH1SSI#				
2	P10_4	I/O	TAUD019/TAUD009/RLIN21RX/KR012/ADCA0TRG2/CSIG0SSI#/CAN6RX/INTP6				
3	P10_5	I/O	TAUD0111/TAUD0011/RLIN21TX/KR013/CSIG0RYI/CSIG0RYO/CAN6TX				
4	P10_15	I/O	RLIN22RX/TAUB0I9/TAUB009				
5	P11 0	I/O	CSIH2RYI/CSIH2RYO/RLIN22TX/TAUB0I11/TAUB0O11				
6	P0 0	I/O	TAUD012/TAUD002/RLIN20RX/CAN0TX/PWGA100/CSIH0SSI#/DPO/RESETOUT#				
7	P0 1	I/O	TAUD0I4/TAUD0O4/CANORX/INTPO/RLIN20TX/PWGA110/CSIH0SI/APO				
8	P0 2	I/O	TAUD0I6/TAUD006/CAN1RX/INTP1/RLIN30TX/PWGA120/CSIH0SC/DPO				
9	P0 3	I/O	TAUD018/TAUD008/RLIN30RX/INTP10/CAN1TX/DPIN1/PWGA130/CSIH0S0				
10	EVCC	Power	-				
11	PO 4	I/O	RLIN31RX/INTP11/CAN2TX/PWGA100/CSIH1SI/SELDP0/DPIN8				
12	P0 5	1/0	CAN2RX/INTP2/RLIN31TX/DPIN9/SELDP1/CSIH1SO				
13	P0_6	1/0	INTP2/DPIN10/SELDP2/CSIH1SC				
14	PO 11	1/0	RIICOSDA/DPIN12/CSIH1CSS2/TAUB0I8/TAUB008				
15	PO 12	1/0	RIICOSCL/DPIN13/TAUB0I10/TAUB0010/CSIG0SI				
16	PO 13	1/0	INTP12/TAUB0I12/TAUB0012/CSIG0SO/CAN5RX/INTP5				
17	PO 14	1/0	TAUB0114/TAUB0014/CSIG0SC/CANSTX				
18	EVSS	Power	IAUBUI14/ IAUBUU14/CSIGUSC/CANSTX				
19	P8 2	I/O	TAUJ0I0/TAUJ000/DPIN2/CSIH0CSS0/INTP6				
20	_	1/0	DPIN14				
21	P8_10	1/0	TAUJ1I2/TAUJ1O2/DPIN15				
	P8_11	<u>_</u>					
22	P8_12	1/0	TAUJ13/TAUJ103/DPIN16				
23	JP0_5	1/0	NMI/TAUJ0I3/TAUJ0O3/DCURDY#/LPDCLKOUT				
24	JP0_4	1/0	DCUTRST#				
25	JP0_3	1/0	INTP3/TAUJ0I2/TAUJ002/DCUTMS				
26	JP0_2	1/0	INTP2/TAUJ011/TAUJ001/DCUTCK/LPDCLK				
27	JP0_1	1/0	INTP1/TAUJ0I0/TAUJ000/DCUTDO/LPDO				
28	JP0_0	1/0	INTPO/DCUTDI/LPDIO				
29	\RESET	Input	-				
30	EVCC	Power	-				
31	AWOVSS	Power	-				
32	AWOVCL	Power	-				
33	REGVCC	Power	-				
34	X2	Input	-				
35	X1	Input	-				
36	FLMD0	Input	-				
37	P0_10	1/0	INTP3/CSIH1CSS1/DPIN11/RLIN22TX/TAUB0I6/TAUB0O6/CAN4TX				
38	P0_9	I/O	INTP12/CSIH1CSS0/DPIN7/RLIN22RX/TAUB0I4/TAUB0O4/CAN4RX/INTP4				
39	P0_8	I/O	RLIN21TX/DPIN6/CSIH1SSI#/TAUB0I2/TAUB0O2/CAN3TX				
40	P0_7	I/O	RLIN21RX/DPIN5/CSIH1RYI/CSIH1RYO/TAUB0I0/TAUB000/CAN3RX/INTP3				
41	EVSS	Power	-				
42	P8_0	I/O	TAUJ010/TAUJ000/DPIN2/PWGA140/INTP4/CSIH0CSS0				
43	P8_1	I/O	TAUJ001/DPIN0/INTP5/CSIH1CSS3				
44	P8_3	I/O	TAUJ011/TAUJ001/DPIN3/CSIH0CSS1/INTP7				
45	P8_4	I/O	TAUJ012/TAUJ002/DPIN4/CSIH0CSS2/INTP8				
46	P8_5	I/O	TAUJ013/TAUJ003/CS1H0CSS3				
47	P8_6	I/O	NMI				
48	P8_7	I/O	-				

Pin Number	Primary Pin Name	Primary Electrical	Alternate Pin Name(s)				
		Туре					
49	P8_8	1/0	-				
50	P8_9	1/0	-				
51	A0VSS	Power	-				
52	AOVREF	Power	-				
53	AP0_15	I/O	ADCA0I15				
54	AP0_14	1/0	ADCA0I14				
55	AP0_13	1/0	ADCA0I13				
56	AP0_12	I/O	ADCA0I12				
57	AP0_11	I/O	ADCA0I11				
58	AP0_10	I/O	ADCA0I10				
59	AP0_9	I/O	ADCA019				
60	AP0_8	1/0	ADCA018				
61	AP0_7	I/O	ADCA017				
62	AP0_6	I/O	ADCA016				
63	AP0_5	1/0	ADCA015				
64	APO 4	I/O	ADCA014				
65	APO 3	I/O	ADCA0I3				
66	APO 2	I/O	ADCA013				
67	APO 1	I/O	ADCA0I1				
68	P9 0	1/0	NMI/PWGA80/TAUD0I0/TAUD000/ADCA0TRG0/CSIH2CSS0/KR0I4				
69	P9 1	1/0	INTP11/PWGA9O/TAUD0I2/TAUD002/KR0I5/CSIH2CSS1				
70	EVSS	Power	-				
71	ISOVCL	Power	-				
72	ISOVSS	Power	-				
73	P20 5	1/0	RLIN23TX				
74	P20 4	1/0	RLIN23RX				
75	EVCC	Power					
76	EVSS	Power	-				
77	EVCC	Power	-				
78	ISOVCL	Power	-				
79	ISOVSS	Power	-				
80	EVSS	Power	-				
81	P10 6	I/O	TAUDOI13/TAUDOO13/CSIGOSO/CAN1RX/INTP1				
82	P10 7	1/0	TAUD0I15/TAUD0015/CSIG0SC/PWGA40/CAN1TX				
83	FLMD1	Input	P10_8/TAUD0I10/TAUD0O10/CSIG0SI/PWGA50				
84	P10 9	I/O	TAUD0112/TAUD0012/RLIN30RX/INTP10/PWGA6O/CSIH0RYI/CSIH0RYO				
85	P10 10	1/0	TAUD0114/TAUD0014/RLIN30TX/PWGA70/CSIH0CSS1				
86	P10_11	1/0	RLIN31RX/INTP11/CSIH1CSS0/TAUB011/TAUB001				
87	P10_12	1/0	RLIN31TX/CSIH1CSS1/TAUB0I3/TAUB0O3				
88	P10_13	1/0	CSIH0SSI#/INTP12/TAUB0I5/TAUB005/CAN7TX				
89	P10_14	1/0	TAUB017/TAUB007/CAN7RX/INTP9				
90	P11_1	1/0	CSIH2SSI#/RLIN20RX/TAUB0I13/TAUB0O13				
91	P11_2	1/0	CSIH2SO/RLIN2OTX/TAUB0115/TAUB0015				
92	P11 3	1/0	CSIH2SC/CAN3RX/INTP3				
93	P11_4	1/0	CSIH2SI/CAN3TX				
94	P11 5	1/0	CANSRX/INTP5				
95	P11_6	1/0	INTP13/CAN5TX				
96	EVCC	Power	- INTP13/CANSTX				
97	EVSS	Power	_				
98	P10_0	I/O	TAUD0I1/TAUD001/CAN0RX/INTP0/PWGA00/CSIH1SI				
99	P10_1	1/0	TAUD013/TAUD003/CAN0TX/PWGA10/CSIH1SC/MODE0				
100	P10_1	1/0	TAUDOIS/TAUDOOS/CANOTA/FWGA10/CSIT15C/MODE0 TAUDOIS/TAUDOOS/RIICOSDA/KROI0/PWGA20/ADCA0TRG0/CSIH1SO/MODE1				

A.3 Symbol Parameters

Orderable Part Number	Min Input Voltage	Max Input Voltage	Max Output Frequency	Min Operating Temperature	Max Operating Temperature	RAM Size	Memory Size	Interface	Number of ADC Channels	Number of I2C Channels	Number of SPI Channels	Number of UART Channels	Number of Timers/Counters
R7F7015013A FD-C#AA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015013A FD-C#BA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015013A FD-C#KA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015013A FE-C#AA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015013A FE-C#BA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015013A FE-C#KA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015014A FD-C#AA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015014A FD-C#BA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015014A FD-C#KA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015014A FE-C#AA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015014A FE-C#BA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch

R7F7015014A FE-C#KA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	320 KB	3 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FD-C#AA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FD-C#BA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FD-C#KA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FE-C#AA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FE-C#BA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015023A FE-C#KA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FD-C#AA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FD-C#BA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FD-C#KA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FE-C#AA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FE-C#BA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015024A FE-C#KA2	3 V	5.5 V	120 MHz	–40° C	+125 °C	384 KB	4 MB	SCI, UART, LIN, CAN, I2C	10-bit X 26- Ch, 12-bit X 32-Ch	1	8	6	16-bit X 48-Ch, 32- bit X 8-Ch
R7F7015303A FD-C#AA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch

R7F7015303A FD-C#BA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015303A FD-C#KA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015303A FE-C#AA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015303A FE-C#BA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015303A FE-C#KA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	3 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FD-C#AA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FD-C#BA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FD-C#KA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FE-C#AA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FE-C#BA2	3 V	5.5 V	120 MHz	–40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch
R7F7015313A FE-C#KA2	3 V	5.5 V	120 MHz	−40° C	+105 °C	512 KB	4 MB	SCI, UART, LIN, CAN, I2C	12-bit X 15- Ch	1	4	2	16-bit X 32-Ch, 32- bit X 8-Ch

A.4 Footprint Design Information

A.4.1 **176-LFQFP**

IPC Footprint Type	Package Code/ POD number	Number of Pins
QFP	PLQP0176KH-A	176

Description	Dimension	Value (mm)	Diagram
Minimum lead span (vertical side)	Dmin	25.8	
Maximum lead span (vertical side)	Dmax	26.2	
Minimum lead span (horizontal side)	Emin	25.8	
Maximum lead span (horizontal side)	Emax	26.2	
Minimum body span (vertical side)	D1min	24.2	Arnex
Maximum body span (vertical side)	D1max	24.2	
Minimum body span (horizontal side)	E1min	24.2	Almin
Maximum body span (horizontal side)	E1max	24.2	
Minimum Lead Width	Bmin	0.17	
Maximum Lead Width	Bmax	0.27	
Minimum Lead Length	Lmin	0.45	
Maximum Lead Length	Lmax	0.75	
Maximum Height	Amax	1.6	
Minimum Standoff Height	A1min	0.05	
Minimum Lead Thickness	cmin	0.09	[B1]
Maximum Lead Thickness	cmax	0.2	PEO
Number of pins (vertical side)	PinCountD	44	
Number of pins (horizontal side)	PinCountE	44	
Distance between the center of any two adjacent pins	Pitch	0.5	
Location of pin 1; S2 = corner of D side, C1 = center of E side	Pin1	S2	
Minimum thermal pad size (vertical side)	D2min	-	
Maximum thermal pad size (vertical side)	D2max	-	
Minimum thermal pad size (horizontal side)	E2min	-	E.
Maximum thermal pad size (horizontal side)	E2max	-	

Recomm			
Description	Dimension	Value (mm)	Diagram
Distance between left pad toe to right pad toe (horizontal side)	ZE	-	[N]
Distance between top pad toe to bottom pad toe (vertical side)	ZD	-	
Distance between left pad heel to right pad heel (horizontal side)	GE	-	ω <u>ω</u> <u>π</u>
Distance between top pad heel to bottom pad heel (vertical side)	GD	-	
Pad Width	Х	-	
Pad Length	Υ	-	<u> </u>

A.4.2 **176-LFQFP**

IPC Footprint Type	Package Code/ POD number	Number of Pins	
QFP	PLQP0176KG-A	176	

Description	Dimension	Value (mm)	Diagram
Minimum lead span (vertical side)	Dmin	25.8	
Maximum lead span (vertical side)	Dmax	26.2	
Minimum lead span (horizontal side)	Emin	25.8	
Maximum lead span (horizontal side)	Emax	26.2	
Minimum body span (vertical side)	D1min	24.2	Amax
Maximum body span (vertical side)	D1max	24.2	
Minimum body span (horizontal side)	E1min	24.2	Atmin
Maximum body span (horizontal side)	E1max	24.2	
Minimum Lead Width	Bmin	0.17	
Maximum Lead Width	Bmax	0.27	
Minimum Lead Length	Lmin	0.45	
Maximum Lead Length	Lmax	0.75	
Maximum Height	Amax	1.6	
Minimum Standoff Height	A1min	0.05	
Minimum Lead Thickness	cmin	0.09	[3]
Maximum Lead Thickness	cmax	0.2	Pto
Number of pins (vertical side)	PinCountD	44	
Number of pins (horizontal side)	PinCountE	44	0 02
Distance between the center of any two adjacent pins	Pitch	0.5	
Location of pin 1; S2 = corner of D side, C1 = center of E side	Pin1	S2	
Minimum thermal pad size (vertical side)	D2min	-	
Maximum thermal pad size (vertical side)	D2max	-	£2
Minimum thermal pad size (horizontal side)	E2min	-	I.
Maximum thermal pad size (horizontal side)	E2max	-	

Recom			
Description	Dimension	Value (mm)	Diagram
Distance between left pad toe to right pad toe (horizontal side)	ZE	-	[N]
Distance between top pad toe to bottom pad toe (vertical side)	ZD	-	
Distance between left pad heel to right pad heel (horizontal side)	GE	-	40 US 100
Distance between top pad heel to bottom pad heel (vertical side)	GD	-	
Pad Width	Х	-	
Pad Length	Υ	-	w]

A.4.3 **100-LFQFP**

IPC Footprint Type	Package Code/ POD number	Number of Pins	
QFP	PLQP0100KM-A	100	

Description	Dimension	Value (mm)	Diagram
Minimum lead span (vertical side)	Dmin	16.2	
Maximum lead span (vertical side)	Dmax	16.2	
Minimum lead span (horizontal side)	Emin	16.2	
Maximum lead span (horizontal side)	Emax	16.2	
Minimum body span (vertical side)	D1min	14.2	Amex
Maximum body span (vertical side)	D1max	14.2	
Minimum body span (horizontal side)	E1min	14.2	Atmi
Maximum body span (horizontal side)	E1max	14.2	
Minimum Lead Width	Bmin	0.17	
Maximum Lead Width	Bmax	0.27	
Minimum Lead Length	Lmin	0.45	
Maximum Lead Length	Lmax	0.75	
Maximum Height	Amax	1.6	
Minimum Standoff Height	A1min	0.05	
Minimum Lead Thickness	cmin	0.09	[81]
Maximum Lead Thickness	cmax	0.2	PRO
Number of pins (vertical side)	PinCountD	25	
Number of pins (horizontal side)	PinCountE	25	
Distance between the center of any two adjacent pins	Pitch	0.5	
Location of pin 1; S2 = corner of D side, C1 = center of E side	Pin1	S2	
Minimum thermal pad size (vertical side)	D2min	-	
Maximum thermal pad size (vertical side)	D2max	-	E2 E
Minimum thermal pad size (horizontal side)	E2min	-	T.
Maximum thermal pad size (horizontal side)	E2max	-	

Recom			
Description	Dimension	Value (mm)	Diagram
Distance between left pad toe to right pad toe (horizontal side)	ZE	-	[M]
Distance between top pad toe to bottom pad toe (vertical side)	ZD	-	
Distance between left pad heel to right pad heel (horizontal side)	GE	-	± 10 m
Distance between top pad heel to bottom pad heel (vertical side)	GD	-	
Pad Width	х	-	
Pad Length	Υ	-	[4]

A.4.4 **100-LFQFP**

IPC Footprint Type	Package Code/ POD number	Number of Pins
QFP	PLQP0100KL-A	100

Description	Dimension	Value (mm)	Diagram
Minimum lead span (vertical side)	Dmin	16.2	
Maximum lead span (vertical side)	Dmax	16.2	
Minimum lead span (horizontal side)	Emin	16.2	
Maximum lead span (horizontal side)	Emax	16.2	
Minimum body span (vertical side)	D1min	14.2	Amax
Maximum body span (vertical side)	D1max	14.2	
Minimum body span (horizontal side)	E1min	14.2	Atmi
Maximum body span (horizontal side)	E1max	14.2	
Minimum Lead Width	Bmin	0.17	
Maximum Lead Width	Bmax	0.27	
Minimum Lead Length	Lmin	0.45	
Maximum Lead Length	Lmax	0.75	
Maximum Height	Amax	1.6	
Minimum Standoff Height	A1min	0.05	
Minimum Lead Thickness	cmin	0.09	[6]
Maximum Lead Thickness	cmax	0.2	Pto
Number of pins (vertical side)	PinCountD	25	
Number of pins (horizontal side)	PinCountE	25	
Distance between the center of any two adjacent pins	Pitch	0.5	
Location of pin 1; S2 = corner of D side, C1 = center of E side	Pin1	S2	
Minimum thermal pad size (vertical side)	D2min	-	
Maximum thermal pad size (vertical side)	D2max	-	£2
Minimum thermal pad size (horizontal side)	E2min	-	E
Maximum thermal pad size (horizontal side)	E2max	-	

Recommended Land Pattern (NSMD Design)				
Description	Dimension	Value (mm)	Diagram	
Distance between left pad toe to right pad toe (horizontal side)	ZE	-	[N]	
Distance between top pad toe to bottom pad toe (vertical side)	ZD	-		
Distance between left pad heel to right pad heel (horizontal side)	GE	-	40 US 100	
Distance between top pad heel to bottom pad heel (vertical side)	GD	-		
Pad Width	х	-		
Pad Length	Υ	-	w]	