

# Review Questions for Self Evaluation

## Lecture 9 - 13



# Review Questions - (Lecture 9 - 11)

- 1) What is the motivation for providing bank switching in 8051 microcontroller?
- 2) If I am adopting bank switching strategy, then which register/registers should be pushed into stack? And comment on the order in which push and pop operations should be performed.
- 3) Why do we write LJMP <address> at the start of ISR ? What will happens if I am not writing it?
- 4) What will happen if I perform unequal number of pushes and pops while I am in ISR ( say, I do 4 pushes and 2 pops before executing RETI )

# Review Questions (Lecture 12 and 13)

- 1) Given that we have an external 12 MHz crystal, and each machine cycle is 12 clock cycles. What should be the count value that should be loaded into registers TH, TL so that the timer times out after 10 ms? (Timer is operating in Mode - 1 ).
- 2) In 8052 microcontrollers, an additional timer T2 is present, what are the limitations of timer T0 and timer T1, that timer T2 address?