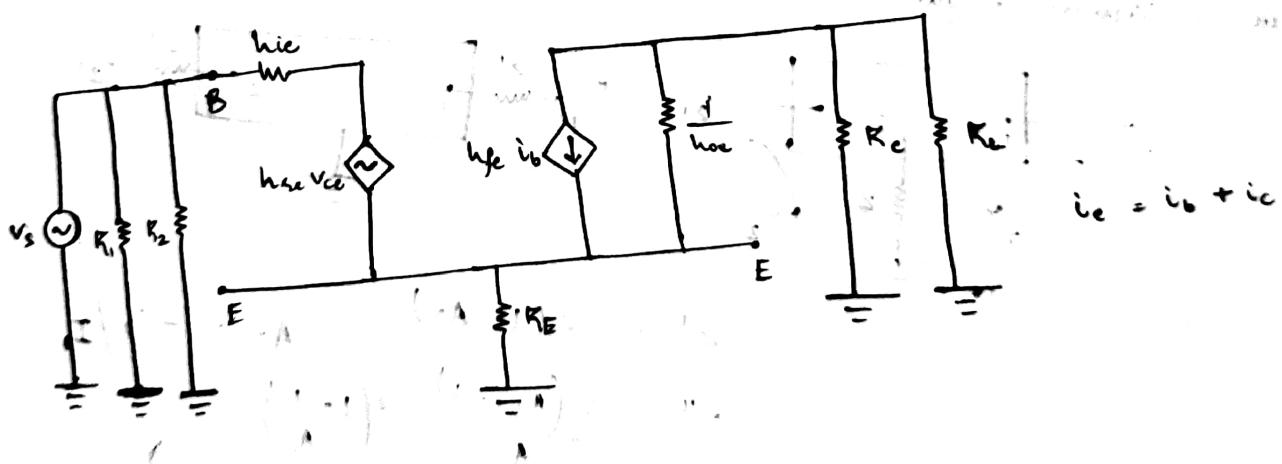
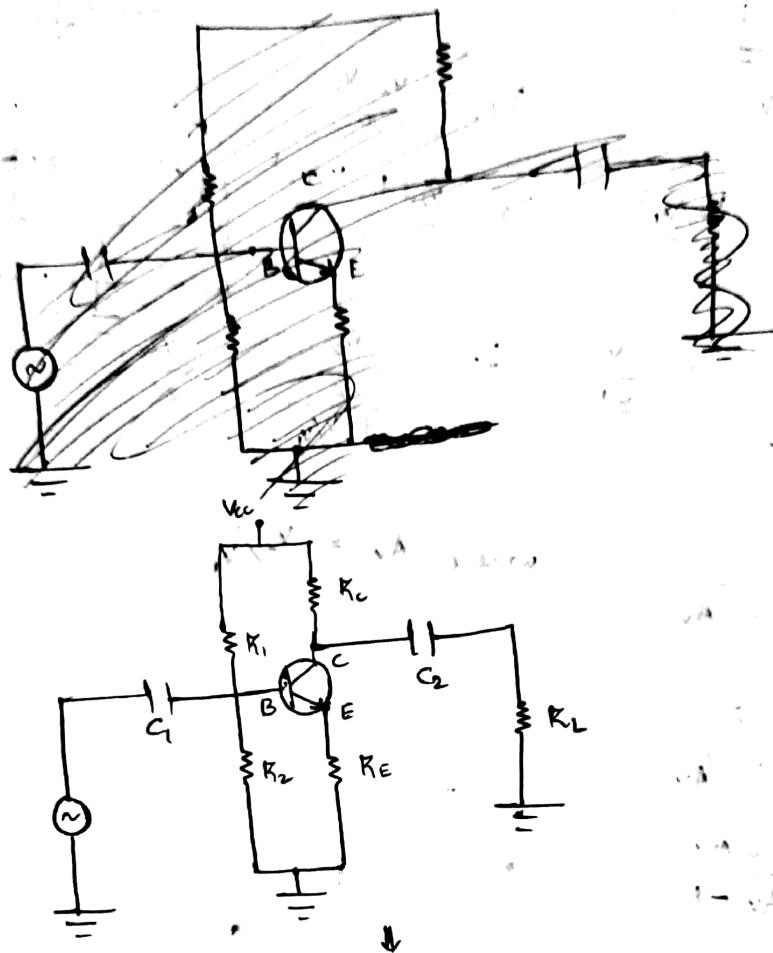
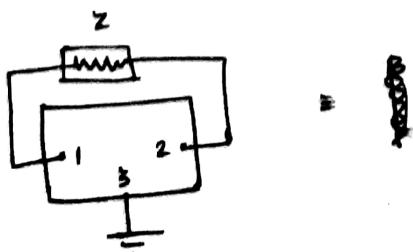


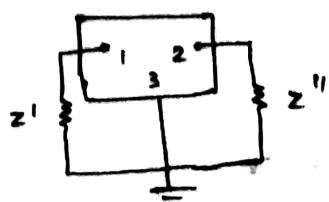
Feedback \Rightarrow A part of output voltage / current feeded back to input.



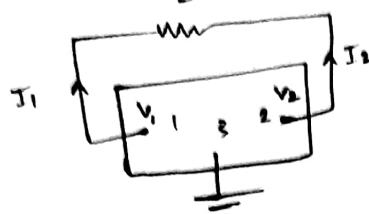
Miller's Theorem



Impedance $\Rightarrow z, z', z''$



$$z' \& z'' \Rightarrow$$



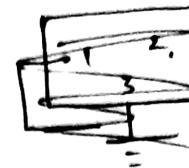
$$I_1 = \frac{V_1 - V_2}{Z}$$

$$I_2 = \frac{V_1(1 - V_2/V_1)}{Z}$$

$$I_1 = \frac{V_1}{\frac{Z}{1 - V_2/V_1}} = \frac{V_1}{Z'} \quad Z' = \frac{Z}{1 - V_2/V_1}$$

$$I_2 = \frac{V_2 - V_1}{Z}$$

$$I_2 = \frac{V_2(1 - V_1/V_2)}{Z} = \frac{V_2}{Z''} \quad Z'' = \frac{Z}{1 + V_1/V_2}$$



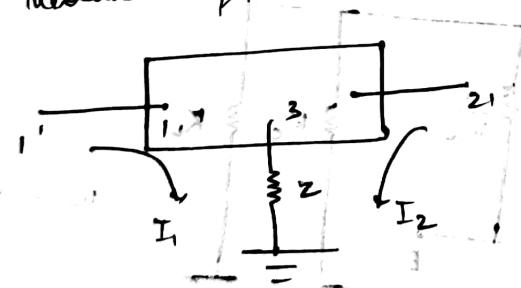
$$Z' = \frac{Z}{1 - Av}$$

where $Av = V_2/V_1$

$$Z'' = \frac{Z}{1 - 1/Av}$$

$$= \frac{ZAv}{Av - 1}$$

Miller's Theorem Sequel

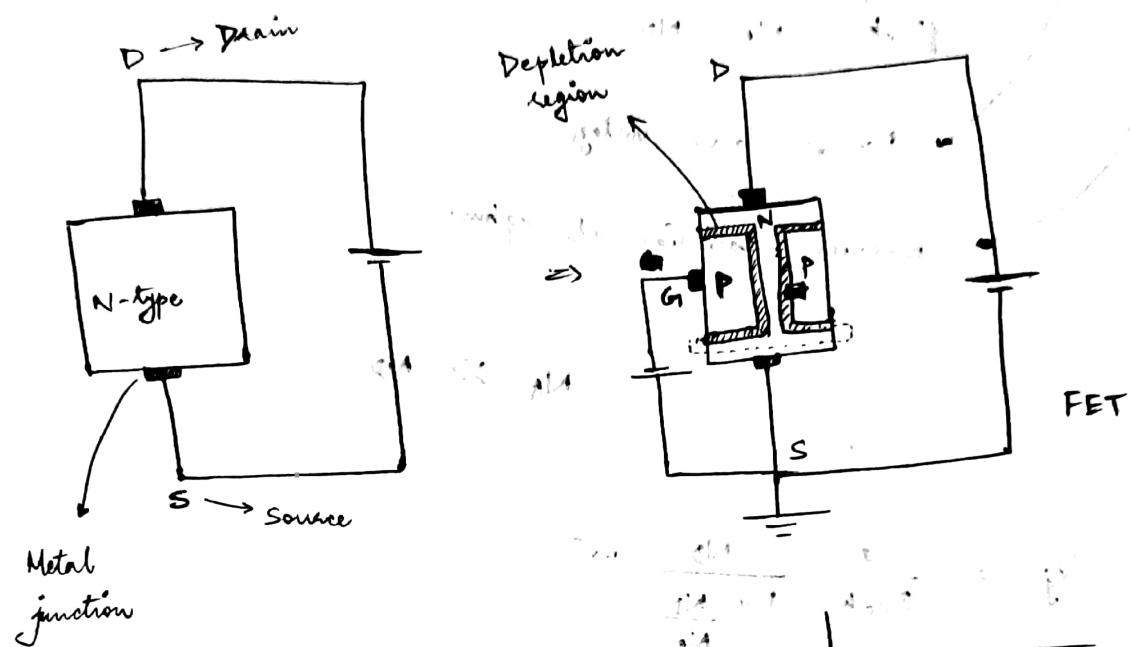


$$Z' = Z(1 - A_I)$$

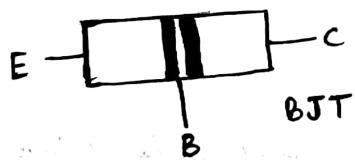
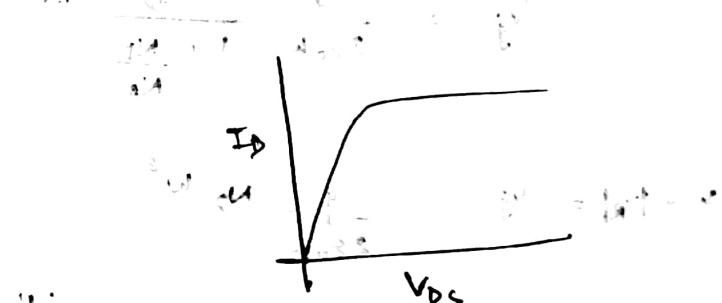
$$A_I = -I_2/I_1$$

$$Z'' = \frac{Z(A_I - 1)}{A_I} = Z\left(1 - \frac{1}{A_I}\right)$$

Field - Effect Transistor (FET)



$$V_B = \frac{q}{2\epsilon_0 k} \frac{N_{A\bar{D}}}{N_A + N_D} w^2$$



BJT

Current controlled device Field controlled device

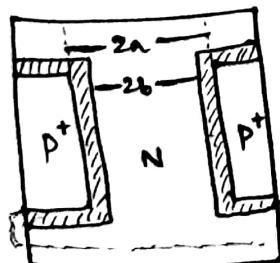
Niche slower

Difficult to construct

Very fast device

Easy to construct

High power device Low power device



Effective channel width = $2b$

Metalurgical channel width = $2a$

Width of depletion range = $a-b$
in one side

$$V_B + |V_R| = \frac{q}{2\epsilon_0 k} \frac{N_A N_D}{N_A + N_D} w^2, V_j$$

(1.37)

Reverse bias voltage

Barrier potential at eqbm.

$$N_A \gg N_D$$

$$V_j = \frac{q}{2\epsilon_0 k} \frac{N_D}{1 + \frac{N_D}{N_A}} w^2$$

$$V_B + |V_R| = V_j = \frac{q}{2\epsilon_0 k} N_D w^2$$

$$(a-b) = w = \left[\frac{2\epsilon_0 k}{q N_D} (V_B + |V_R|) \right]^{1/2}$$

Pinch-off voltage (V_p) is the reverse bias gate voltage at which channel ~~splits~~ closes (completely covered with depletion region)

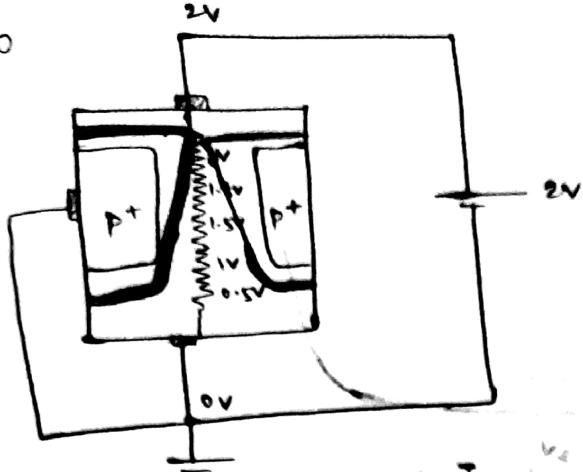
$$a = \left[\frac{2\epsilon_0 k}{q N_D} (V_B + |V_p|) \right]^{1/2}$$

$$a^2 = \frac{2\epsilon_0 k}{q N_D} (V_B + |V_p|)$$

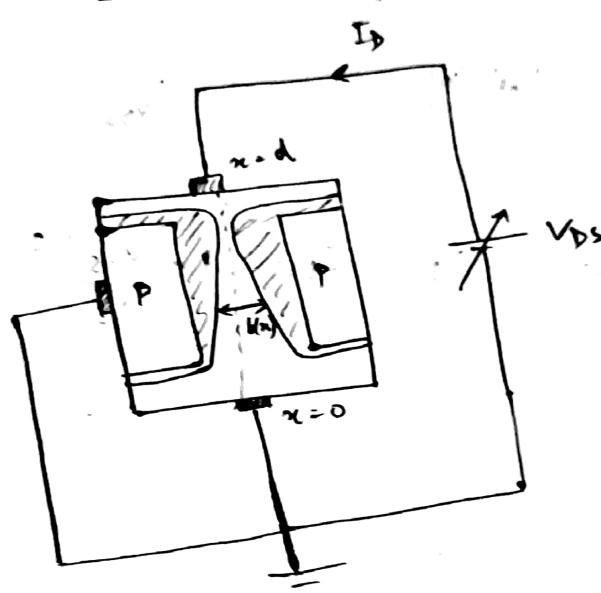
$$\cancel{V_B + |V_p|} = \cancel{\frac{2\epsilon_0 k}{q N_D} a^2}$$

$$V_B + |V_p| = \frac{q N_D a^2}{2\epsilon_0 k}$$

case - I
 $V_{GS} = 0$



The channel acts as a resistor (voltage drops across it)



~~Let width of FET be w.~~

$$I_D = J_D \times A$$

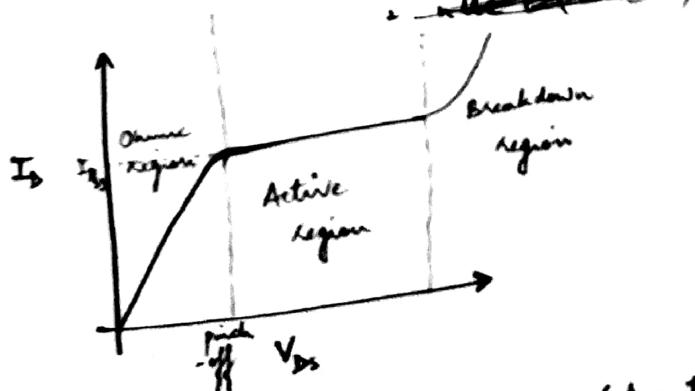
$$= J_D \times (w \cdot 2b(w))$$

$$= (\sigma E) \times (w \cdot 2b(w))$$

$$I_D = (\mu n e) \left(\frac{V_{GS}}{L} \right) (2w b(w)) \approx \mu n e E \cdot (2w b(w))$$

$$\sigma = n \mu e e + p \mu n e$$

$$\approx n \mu e e$$



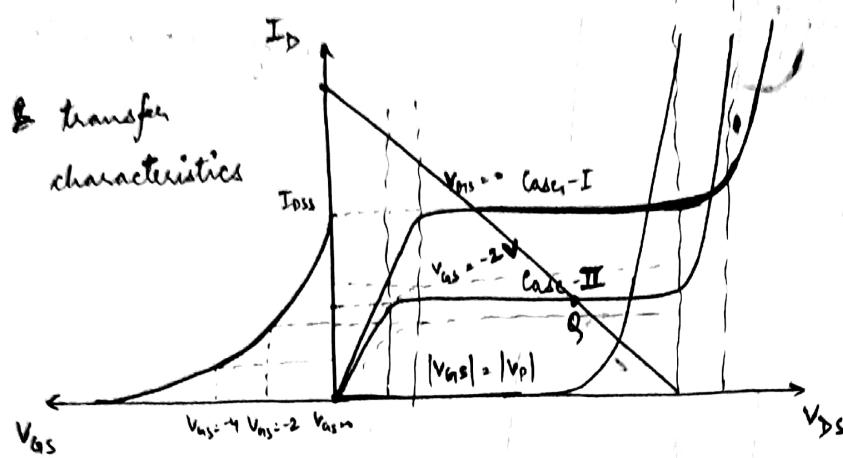
$$n \propto \frac{1}{E^{1/2}} \quad (\text{Moderate voltage})$$

$$n \propto \frac{1}{E} \quad (\text{Higher voltage})$$

$I_{DSS} \Rightarrow$ Saturation Drain current

Case - II

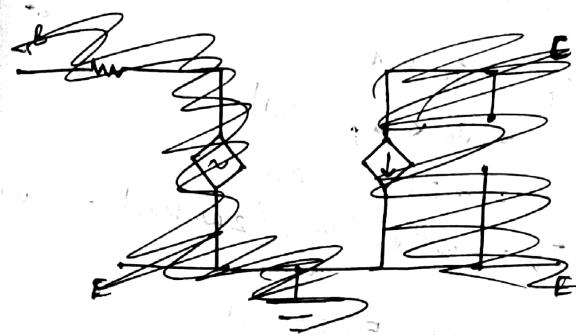
$$V_{GS} < 0$$



$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_p} \right)^2$$

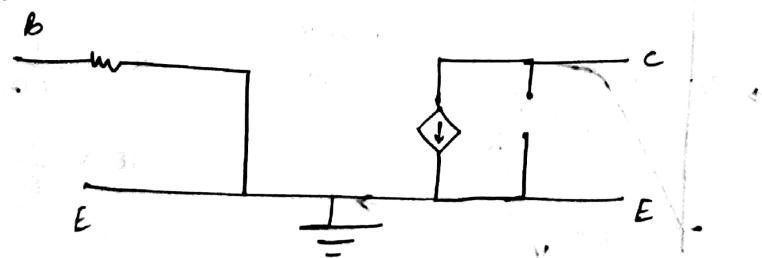
Approximate h-parameter model

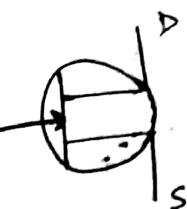
If $h_{oe} R_L \leq 0.1$



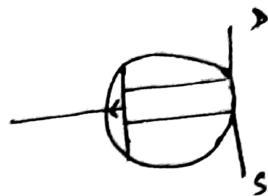
$\frac{1}{h_{oe}}$ will be
open circuited
& hence can
be neglected

Small signal analysis will be like this \Rightarrow

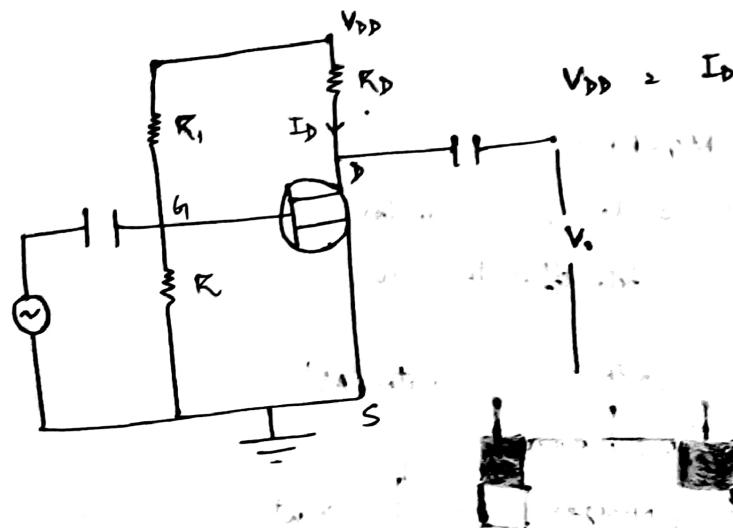




n-channel
FET



p-channel
FET



$$V_{DD} = I_D R_D + V_{DS}$$

$$V_{GS} = V_{GS0} + \Delta V_{GS}$$

$$i_D = I_D + i_d$$

$$V_{DS0} = V_{DS} + \Delta V_{DS}$$

$$i_D = f(V_{DS}, V_{GS})$$

$$i_D = I_{D0} + \left(\frac{\partial i_D}{\partial V_{DS}} \right)_{\text{at } q\text{-point}} \Delta V_{DS} + \left(\frac{\partial i_D}{\partial V_{GS}} \right)_{\text{at } q\text{-point}} \Delta V_{GS}$$

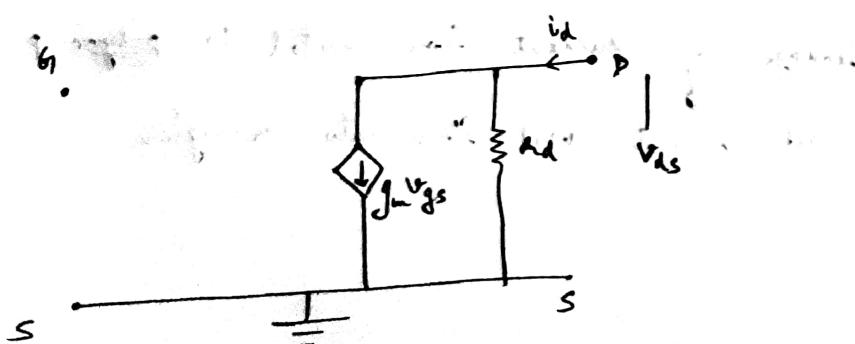
$$(V_{DS} - \Delta V_{DS} = V_{DS0} - \Delta V_{DS})$$

$$i_D - I_D = \left(\frac{\partial i_D}{\partial V_{DS}} \right) \Delta V_{DS} + \left(\frac{\partial i_D}{\partial V_{GS}} \right) \Delta V_{GS}$$

$$i_d = \left(\frac{1}{R_d} \right) \Delta V_{DS} + (g_m) V_{GS}$$

$R_d \rightarrow$ drain resistance

$g_m \rightarrow$ transfer conductance



Voltage amplification factor : $A_V = -\left(\frac{\partial V_{DS}}{\partial V_{GS}}\right)_{I_D} \approx -\left(\frac{V_{DS}}{V_{GS}}\right)_{I_D}$

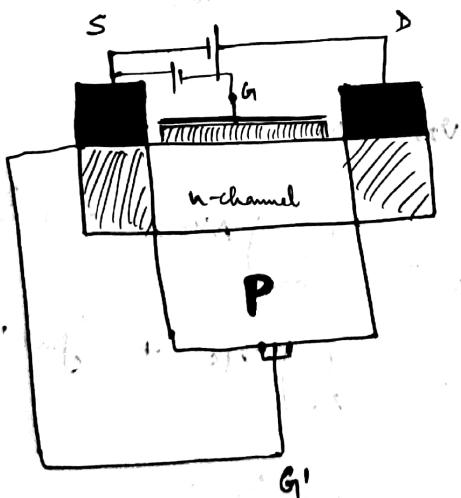
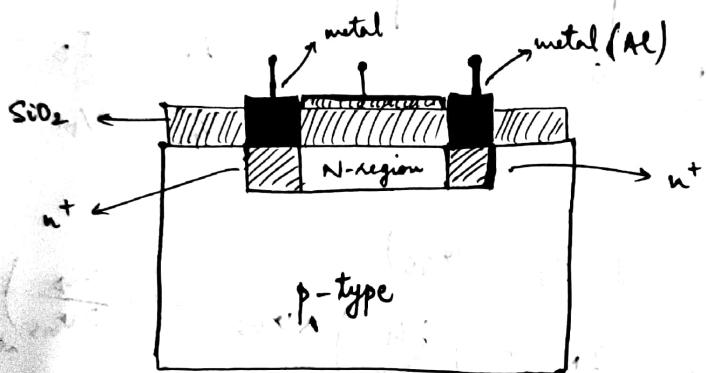
$$A_V = k_d g_m$$

MOSFET

Metal-oxide semiconductor

Input impedance $> 100 \text{ M}\Omega$

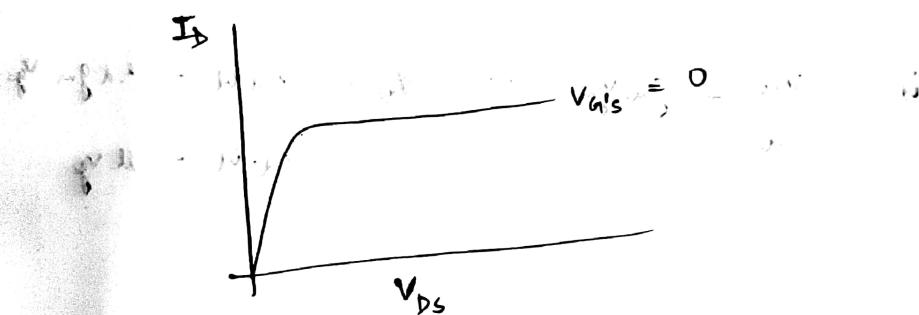
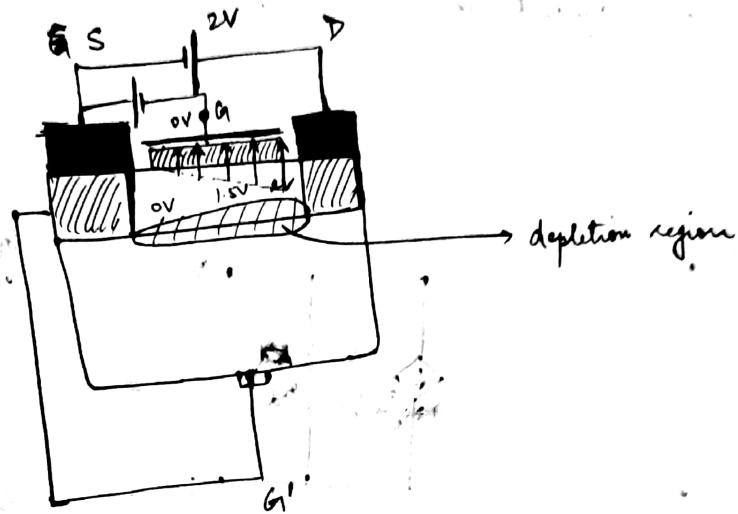
field-effect transistor

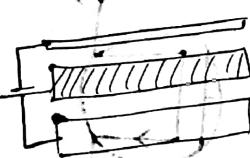


n^+ will create a depletion region & will resist any leakage of current from metal to ~~p~~ p
all current will flow into n-region

n^+ will ensure the ohmic junction b/w metal - $n^+ - n$.

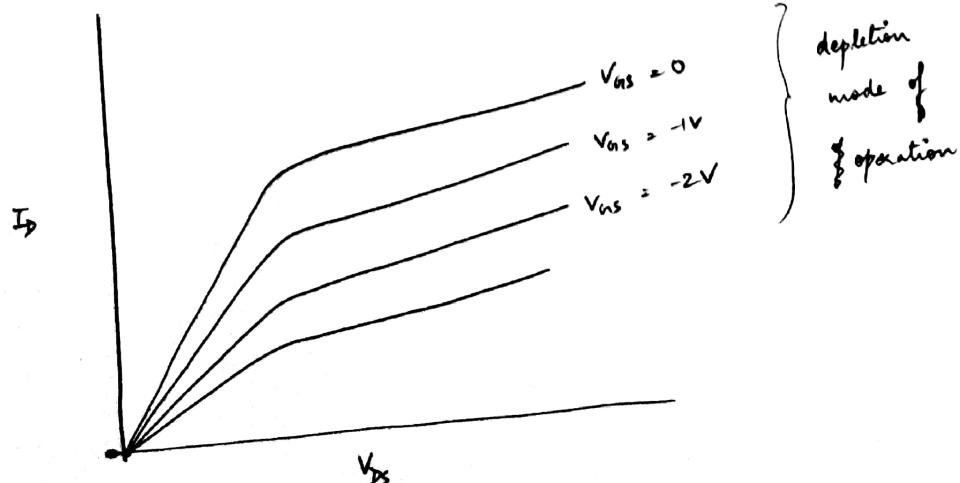
If n^+ was not there, then there would have been a non-ohmic \Rightarrow region b/w metal & n -channel.



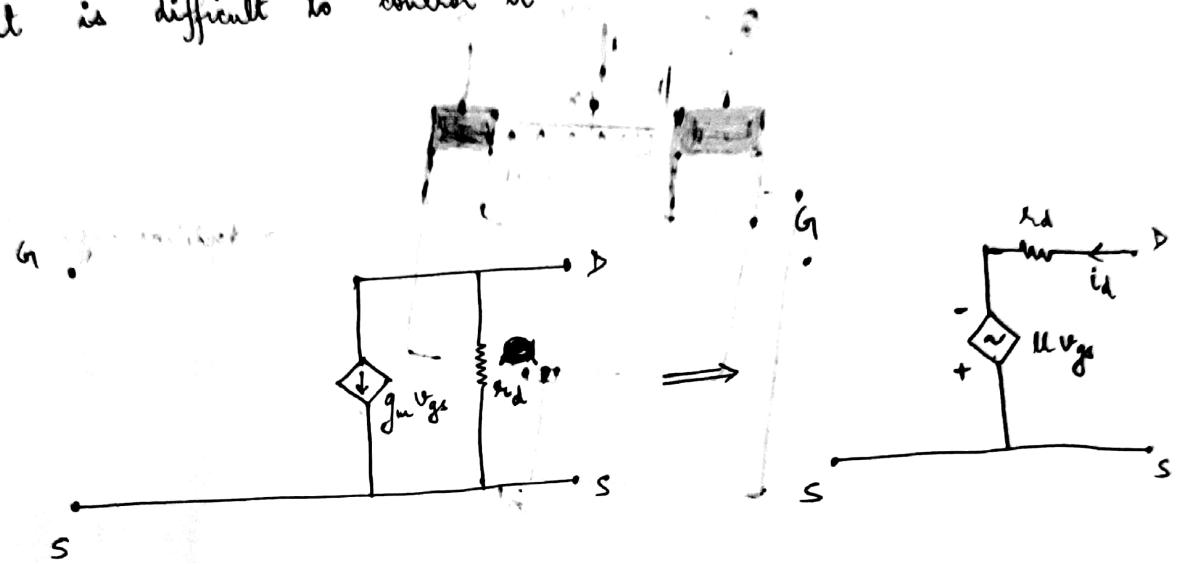
 metal
 SiO_2
semiconductor

Input impedance is high in MOSFET due to the capacitive effect

effect

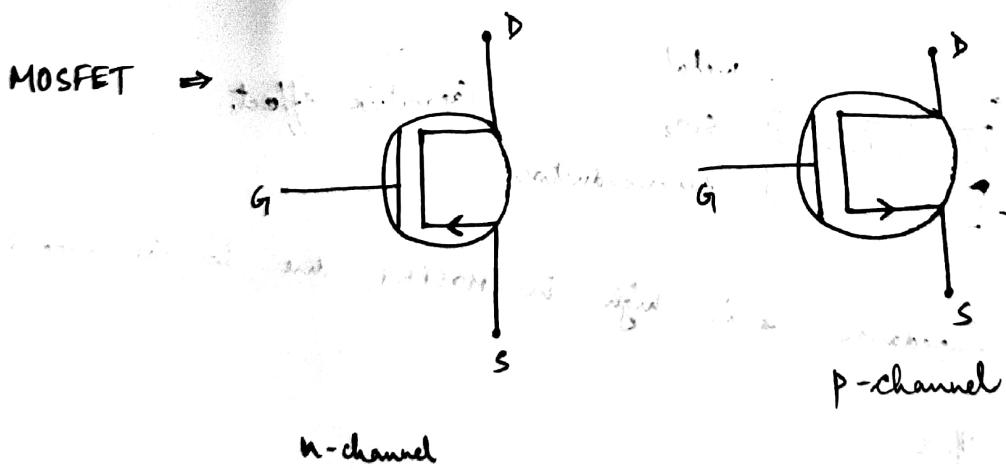


In enhancement mode of operation, the n-channel was not created previously, but it is created automatically & it is difficult to control it.



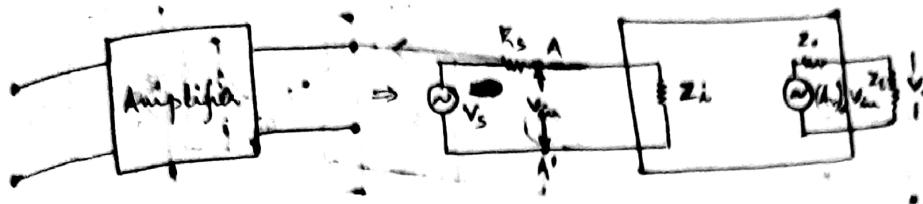
$$id = \frac{v_{ds}}{R_d} + g_m v_{gs} \Rightarrow v_{ds} = R_d id - R_d g_m v_{gs}$$

$$= R_d \cdot id - u v_{gs}$$



Feedback Amplifiers

1) Voltage Amplifier



$$V_{in} = \frac{v_s}{R_s + Z_L} Z_i = \frac{v_s}{1 + \frac{R_s}{Z_i}}$$

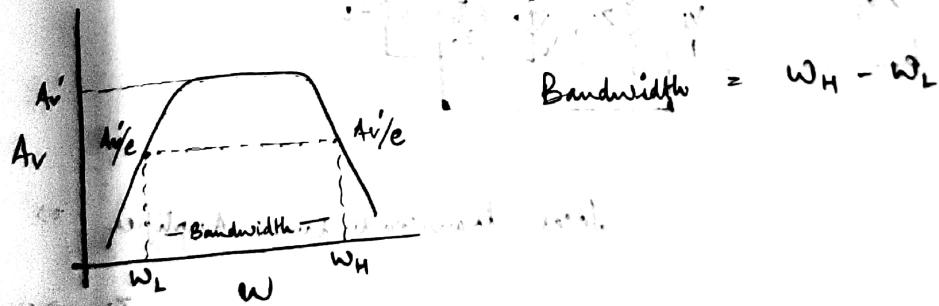
(A_v) \Rightarrow Intrinsic voltage amplification

factor \approx of amplifier when
 R_L is open

if $Z_i \gg R_s$
 $V_{in} \approx v_s$
we need high
input impedance

Issues with practical amplifiers \Rightarrow

- 1) Input impedance is moderate
- 2) Stability of gain
- * 3) Gain-Bandwidth product is not satisfactory

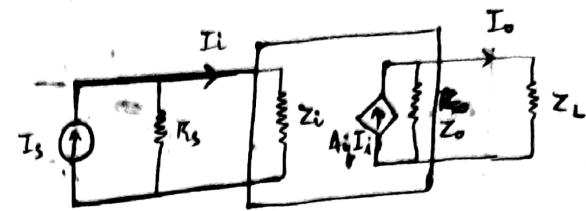
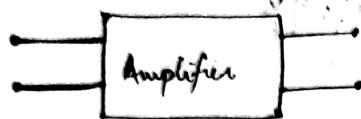


$$\text{Bandwidth} = \omega_H - \omega_L$$

Ideal voltage amplifier $\Rightarrow Z_i \rightarrow \infty$
 ~~$Z_o \rightarrow 0$~~

B

2) Current Amplifier



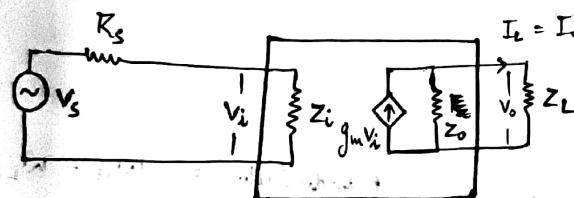
$$I_o = A_i I_s$$

Ideal Current Amplifier

$$\Rightarrow Z_i \rightarrow 0$$

$$Z_o \rightarrow \infty$$

3) Transconductance Amplifier

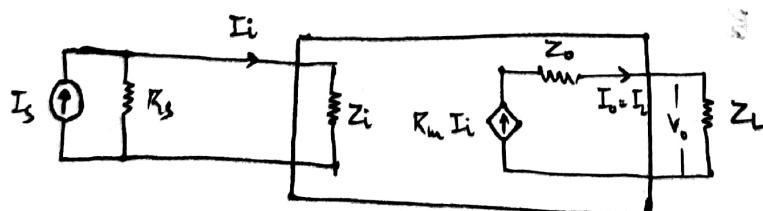


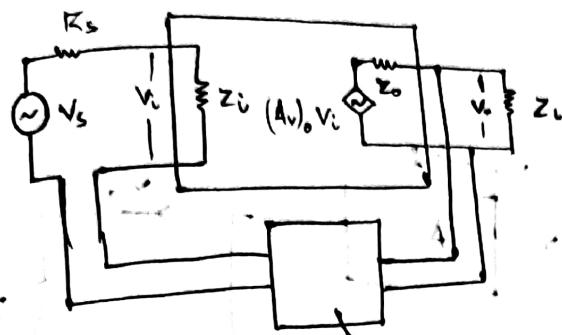
Ideal transconductance Amplifier \Rightarrow

$$Z_i \rightarrow \infty$$

$$Z_o \rightarrow \infty$$

4) Transresistance Amplifier

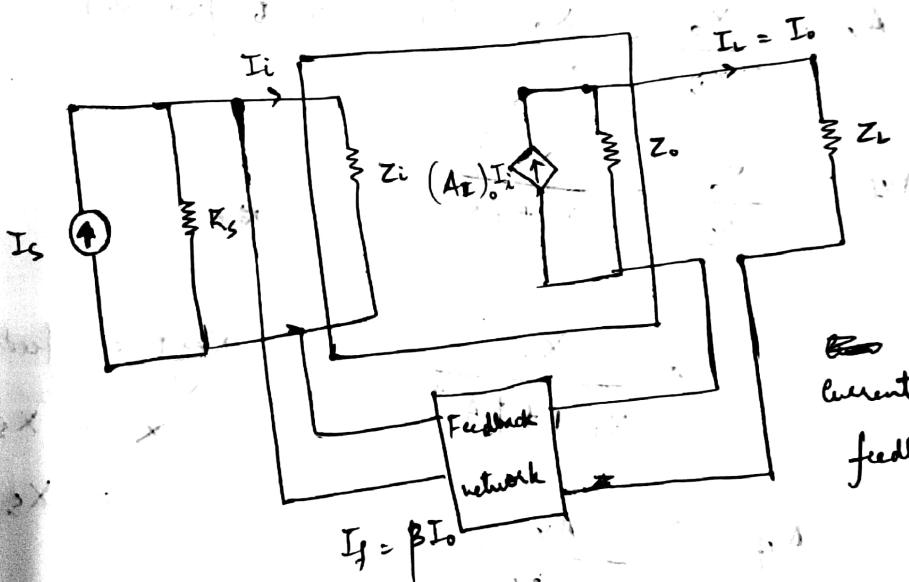




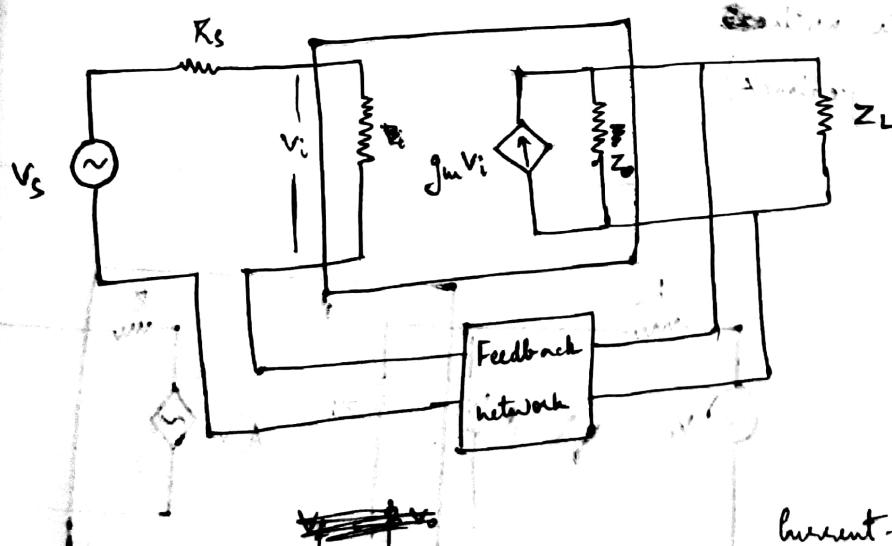
$V_i = V_s - \beta V_o$
Voltage-series feedback

$$V_f = \beta V_o \quad \text{Feedback network}$$

feedback voltage

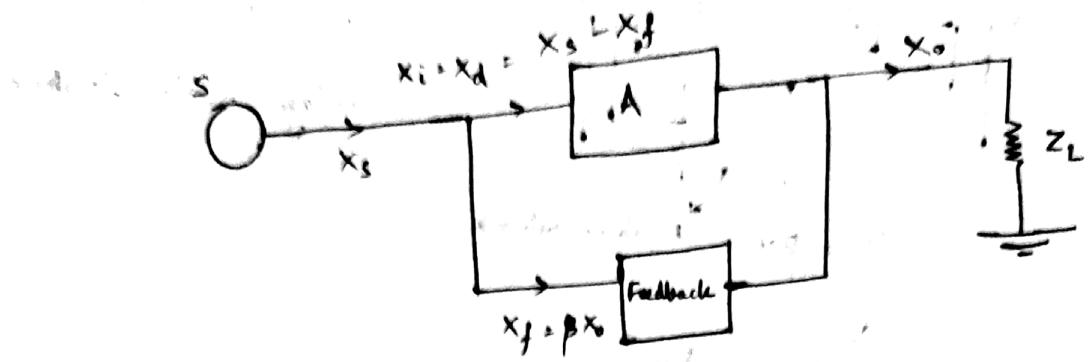


Current-shunt feedback



Current-series feedback

$$V_f = \beta I_o$$



$$A_v = \frac{x_o}{x_i} = \frac{x_o}{x_s} \quad (\text{gain without feedback})$$

$$A_{vf} = \frac{x_o}{x_s} = \frac{x_o}{x_i + \beta x_o}$$

$$= \frac{x_o}{x_i(1 + \beta A_v)}$$

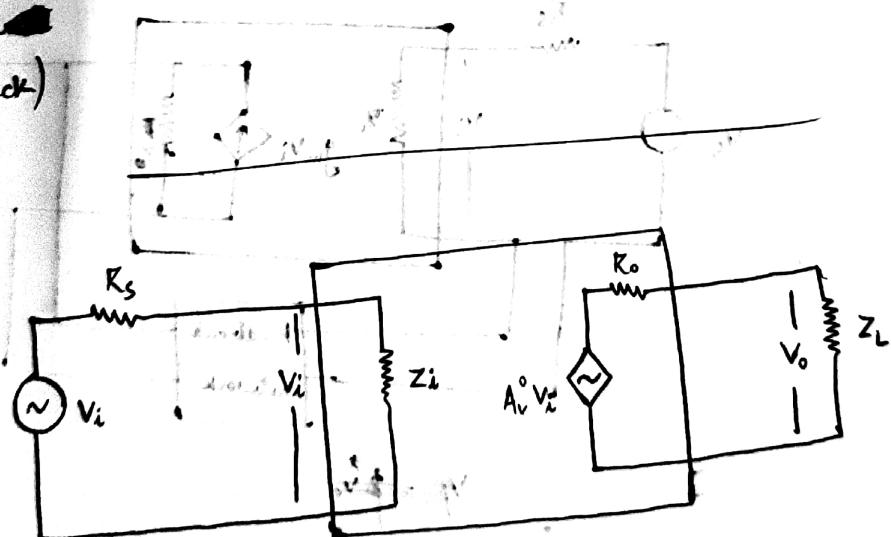
Negative feedback

$$x_i = x_s - x_f$$

$$= x_s - \beta x_o$$

$$A_{vf} = \frac{A_v}{1 + \beta A_v}$$

(gain with feedback)



Input impedance (seen from source) $\Rightarrow z_i' = R_s + z_i$

Output impedance = R_o (without considering Z_L as part of transistor)

$$R_o' = R_o \parallel Z_L \quad (\text{with } \dots \dots \dots \dots \dots \dots)$$

$$A_v = \frac{V_o}{V_s}$$

$$V_o = \frac{A_v \cdot V_i}{R_o + Z_L} \times Z_L$$

$$V_i = \frac{V_s}{R_s + Z_i} \times Z_i$$

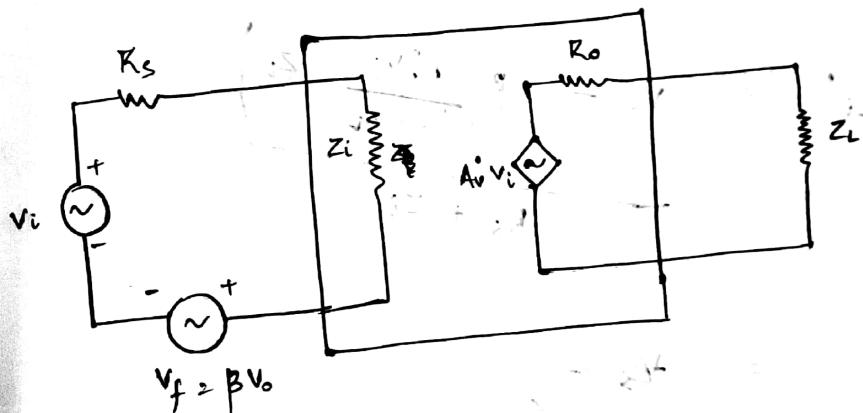
$$V_o = \frac{A_v}{R_o + Z_L} Z_L - \frac{V_s}{R_s + Z_i} Z_i$$

$$= \left[A_v \frac{Z_i}{R_s + Z_i} \right] \cdot \frac{Z_L V_s}{R_o + Z_L} = A_v \cdot \frac{Z_L}{R_o + Z_L} \cdot V_s$$

Input impedance $Z_i' = R_s + Z_i$

Output impedance $= R_o$

gain $A_v = A_v \frac{Z_L}{R_o + Z_L}$



$$A_{vf} = \frac{V_o}{V_s}$$

$$V_o = \frac{A_v V_i}{R_o + Z_L} Z_L$$

$$V_i = \frac{V_s - \beta V_o}{R_s + Z_i} \cdot Z_i$$

$$V_o = \frac{A_v}{R_o + Z_L} \cdot Z_L \cdot \frac{V_s - \beta V_o}{R_s + Z_i} \cdot Z_i$$

$$V_o = \left[A_v \frac{Z_i}{R_s + Z_i} \right] \cdot \frac{Z_L}{R_o + Z_L} (V_s - \beta V_o)$$

$$V_o = \underbrace{A_v \cdot \frac{Z_L}{R_o + Z_L}}_{A_v} (V_s - \beta V_o)$$

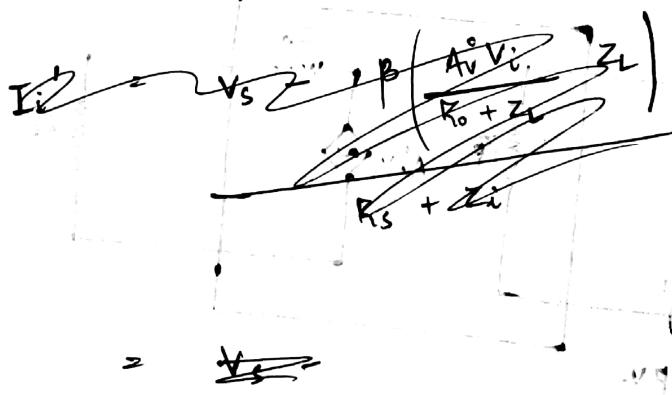
$$\rightarrow V_o = A_v (V_s + \beta V_o)$$

$$V_o (1 + \beta A_v) = A_v V_s$$

$$A_{vf} = \frac{A_v}{1 + \beta A_v} = \frac{V_o}{V_s} = \frac{A_v}{D} \quad D = 1 + \beta A_v$$

$Z_{if}' = \frac{V_s}{I_i'}$ (Input impedance seen by source
when feedback is on)

$$I_i' = \frac{V_s - \beta V_o}{R_s + Z_i}$$



$$I_i' = \frac{V_s - \beta \left(\frac{V_s A_v}{1 + \beta A_v} \right)}{R_s + Z_i}$$

$$I_i' = \frac{V_s}{(R_s + Z_i)(1 + \beta A_v)}$$

$$Z_{if}' = \underbrace{(R_s + Z_i)(1 + \beta A_v)}_D = Z_i' \cdot D$$

$$D = 1 + \beta A_v$$

$$\text{Output impedance} = R_{of} = \frac{V_o}{I_o}$$

$$I_o = \frac{V_o - A_v V_{in}''}{R_o} = \frac{V_o - A_v \left(\frac{-B V_o}{R_s + Z_i} \right) \times Z_i}{R_o}$$

$$V_{in}'' = \frac{V_f}{R_s + Z_i} \times Z_i \\ = \frac{-B V_o}{R_s + Z_i} \times Z_i$$

$$I_o = \frac{V_o}{R_o} \left(1 + \frac{B A_v Z_i}{R_s + Z_i} \right)$$

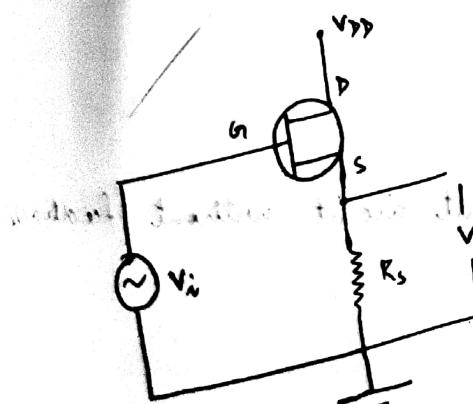
$$R_{of} = \frac{V_o}{I_o} = \frac{R_o}{1 + \frac{B A_v Z_i}{R_s + Z_i}} = \frac{R_o}{1 + B A_v}$$

where

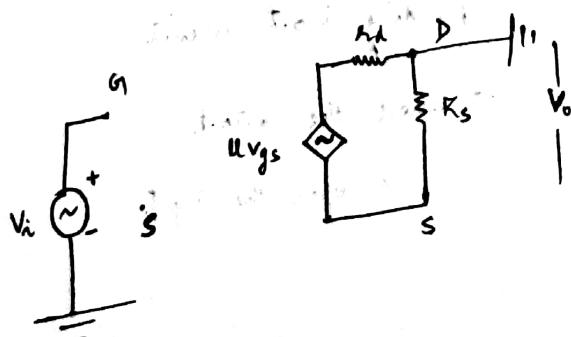
$$A_v = \frac{A_v Z_i}{R_s + Z_i}$$

$$R_{of}' = R_{of} \parallel Z_L$$

$$= \frac{R_o'}{1 + B A_v} = \frac{R_o'}{D}$$



$$V_{GS} = V_{Gd} - V_s \\ V_{in} = V_{GS} = V_i - V_s \\ = V_i - V_o \quad (\beta = 1)$$



$$A_{vf} = \frac{i_d R_s}{V_i}$$

$$u = \frac{u v_g}{R_d + R_s}$$

$$A_{vf} = \frac{u R_s}{R_d + (u+1)R_s}$$

$$= \frac{u (v_g - v_o)}{R_d + R_s}$$

$$= \frac{u (V_i - V_o)}{R_d + R_s}$$

$$u = \frac{u (V_i - i_d R_s)}{R_d + R_s}$$

$$i_d R_d + i_d R_s = u V_i - u i_d R_s$$

$$i_d = \frac{u V_i}{R_d + (u+1)R_s}$$

$$\frac{i_d}{V_i} = \frac{u}{R_d + (u+1)R_s}$$

After

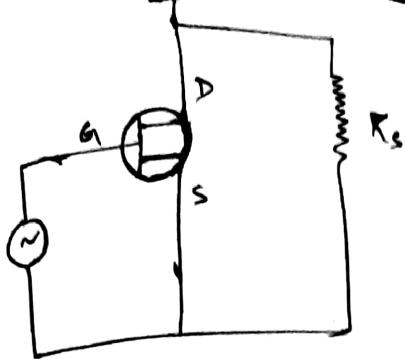
$$A_{vf} = \frac{Av}{1 + \beta Av} \quad \beta = 1$$

$$= \frac{Av}{1 + Av}$$

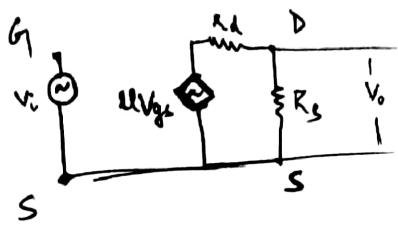
Rules for finding equivalent circuit without feedback

Finding input circuit

- v \Rightarrow short the output
- $\&$ open the input



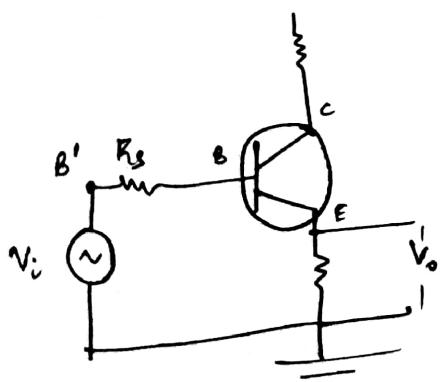
Equivalent circuit without feedback



$$\begin{aligned} A_v &= \frac{V_o}{V_i} \\ &= \frac{I_d R_s}{V_i} \\ &= \frac{U_{V_{ge}} R_s}{R_d + R_s} \\ &= \frac{V_{ge}}{V_i} \quad V_{ge} = V_i \end{aligned}$$

$$A_v = \frac{U R_s}{R_d + R_s}$$

$$\begin{aligned} A_{vf} &= \frac{A_v}{1 + A_v} \\ &= \frac{\frac{U R_s}{R_d + R_s}}{1 + \frac{U R_s}{R_d + R_s}} \\ &= \frac{U R_s}{R_d + (U+1) R_s} \end{aligned}$$



$$V_i = i_b R_s + V_{be} + v$$

$$V_i = V_{be} + v$$

$$V_{in} = V_{be} = V_i - v$$

Current Feedback

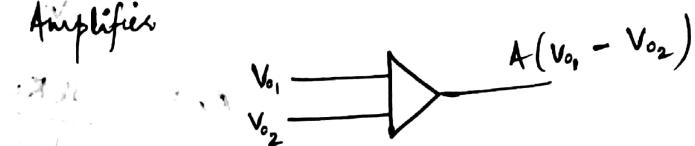
$$I_f = \beta I_o$$

$$A_{if} = \frac{A_I}{1 + \beta A_I}$$

$$R_{if} = \frac{R_i}{1 + \beta A_i}$$

Operational Amplifier (OP-AMP)

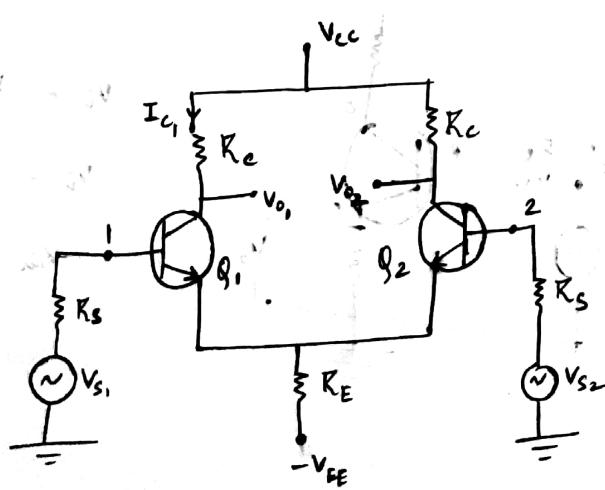
1) Differential Amplifier



2) Direct coupled amplifier

- 3) Infinite input impedance ($> 100 \text{ M}\Omega$)
- 4) Infinite gain (practically > 1000)
- 5) Infinite bandwidth (ideally)
- 6) zero output impedance

Differential Amplifier



$$V_{o_1} = V_{cc} - I_{c_1} R_c$$

$$V_{o_1} = V_{cc} - I_{c_1} R_A$$

$$V_{o_1} = A_1 V_{s_1}$$

Gain of amplifier

when input is given to
base of Q₁

$$V_{o_2} = V_{cc} - I_{c_2} R_c$$

$$V_{o_2} = V_{cc} - I_{c_2} R_A$$

$$V_{o_1} = A_1 \times V_{s_1}'$$

$V_{s_1}' \Rightarrow$ effect of V_{s_2}

when there is
no V_{s_2}

$$\text{if } V_{o_1} = \left(A_1 \frac{V_{s_1}'}{V_{s_2}} \right) V_{s_2}$$

$$V_{o_1} = A'_1 V_{s_2}$$

$A'_1 \Rightarrow$ Gain of Q₁
w.e.t input at Q₂

by principle of superposition

$$V_{o_1} = A_1 V_{s_1} + A'_1 V_{s_2}$$

1 \Rightarrow Inverting } when V_{o_1} is considered as output
2 \Rightarrow Non-Inverting }

$$\text{Input voltage } V_d = V_{s_1} - V_{s_2}$$

$$\text{mid voltage } V_c = \frac{1}{2} (V_{s_1} + V_{s_2})$$

$$V_{s_1} = \frac{1}{2} (V_d + 2V_c)$$

$$V_{s_2} = \frac{1}{2} (2V_c - V_d)$$

$$V_{o_1} = \frac{1}{2} A_1 (V_d + 2V_c) + \frac{1}{2} A'_1 (2V_c - V_d)$$

$$= V_d \left(\frac{A_1 - A'_1}{2} \right) + V_c (A_1 + A'_1)$$

$$V_{o_1} = Ad V_d + V_c A_c$$

$$Ad = \frac{A_i - A'_i}{2}$$

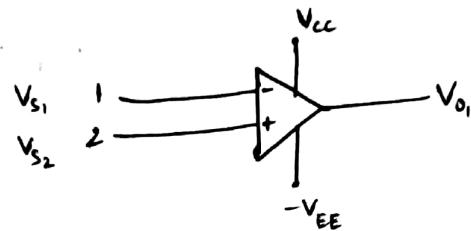
$$Ad \gg A_c$$

$$A_c = A_i + A'_i$$

$$V_{o_1} = Ad V_d$$

\therefore CMRR \rightarrow Common Mode Rejection Ratio (3)

$$\xi = \frac{|Ad|}{|A_c|} \quad \xi \uparrow \Rightarrow \text{Better Amplifier}$$



Case - I

$$\text{When } V_{S1} = V_{S2} = V_s$$

$$\text{Result: } V_{o_1} = A_c V_c$$

$$\frac{V_{o_1}}{V_s} = A_c$$

$$V_d = 0 \quad V_c = \frac{1}{2} (V_{S1} + V_{S2}) = V_s$$

$$Ad = 0$$

Case - II

$$\text{When } V_{S1} = -V_{S2} = V_s$$

$$V_{o_1} = Ad V_d$$

$$\frac{V_{o_1}}{V_s} = 2Ad$$

$$A_c = 0$$

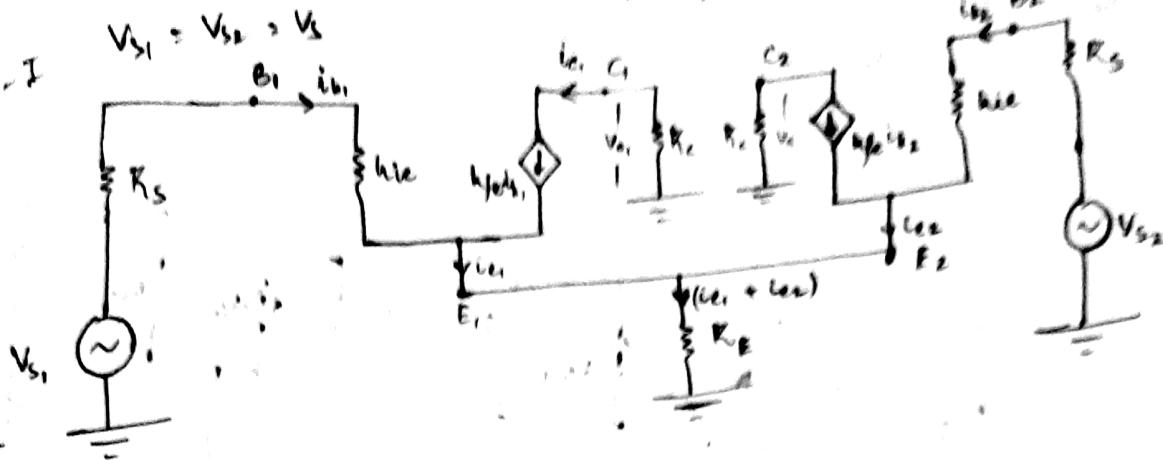
$$V_c = 0$$

$$\therefore V_d = 2V_{S1} = 2V_s$$

$$Ad = \frac{V_{o_1}}{2V_s} = (A_i + A'_i) \cdot \frac{1}{2}$$

Small Signal Equivalent Model for Differential Amplifier

Case - I



$$V_{o1} = -i_{e1} R_E = 1 - h_{FE} i_{b1} R_E \\ = \cancel{h_{FE} i_{b1}}$$

$$\cancel{\text{KCL}} \Rightarrow i_{e1} + i_{e2} = (i_{b1} + h_{FE} i_{b1}) + (i_{b2} + h_{FE} i_{b2})$$

\therefore All resistances are same in KVL, $i_{b1} = i_{b2}$

$$i_{e1} + i_{e2} = 2(1 + h_{FE}) i_{b1}$$

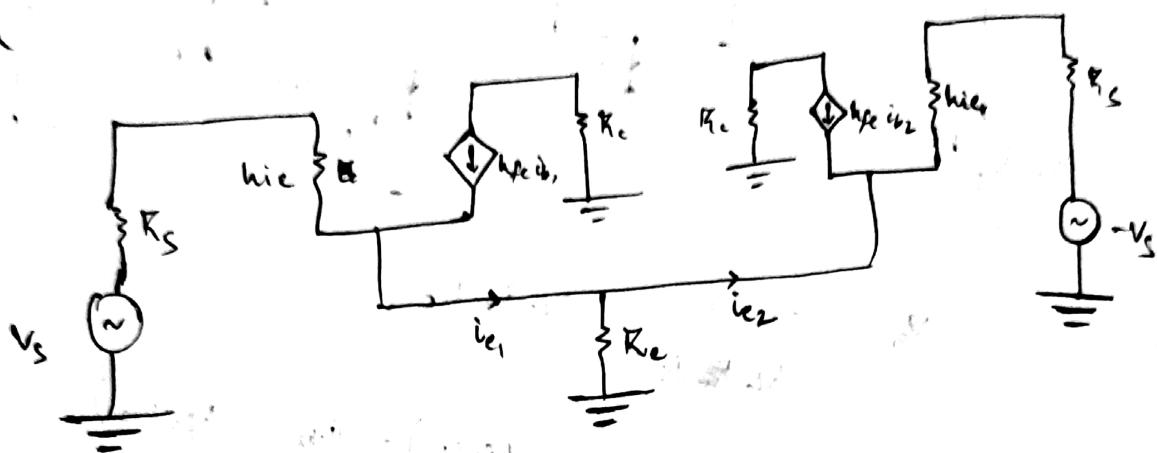
$$\cancel{\text{KVL}} \Rightarrow \cancel{V_S} = (R_S + h_{IE}) i_b + R_E [2(1 + h_{FE}) i_b] \\ = i_b [R_S + h_{IE} + 2(1 + h_{FE}) \cancel{R_E}]$$

$$V_{o1} = -h_{FE} R_E \frac{V_S}{(R_S + h_{IE}) + 2(1 + h_{FE}) R_E}$$

$$A_C = \frac{V_{o1}}{V_S} \\ = \frac{-h_{FE} R_E}{(R_S + h_{IE}) + 2(1 + h_{FE}) R_E}$$

Case - II

$$V_{S_1} = -V_{S_2} = V_S$$



$$|i_{e1}| = |i_{e2}|$$

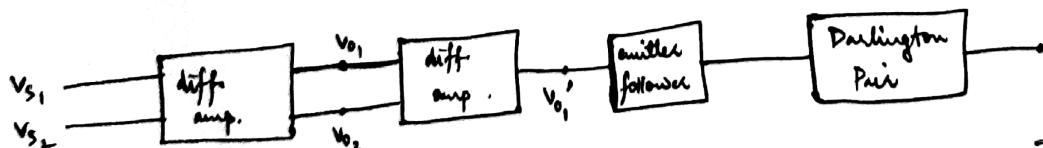
$$i_{e1} + i_{e2} = 0$$

$$V_{O_1} = \cancel{i_{c1}} R_C = -i_{c1} R_C \\ = -h_{fe} i_{b1} R_C$$

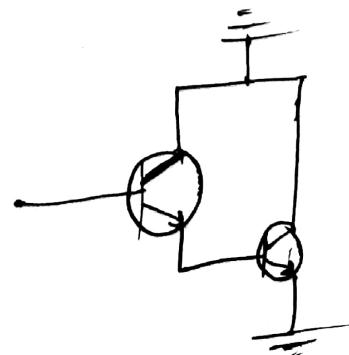
$$\therefore -V_{O_1} = \frac{-h_{fe} R_C V_S}{R_S + h_{ie}}$$

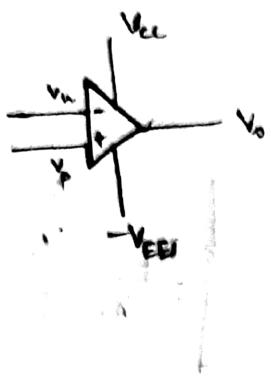
$$A_d = \frac{V_{O_1}}{2V_S} = \frac{-h_{fe} R_C}{2(R_S + h_{ie})}$$

OP-AMP



Darlington pair \Rightarrow





Need of diff. amp. with $A_c \ll A_d$



$$V_o = A_d [\text{signal} + \text{noise} + \text{noise}]$$

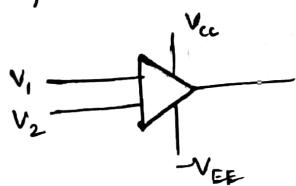
$$+ A_c \left[\frac{\text{signal} + \text{noise} + \text{noise}}{2} \right]$$

$$V_o = A_d \cdot \text{signal} + A_c \left(\frac{\text{signal} + \text{noise}}{2} \right)$$

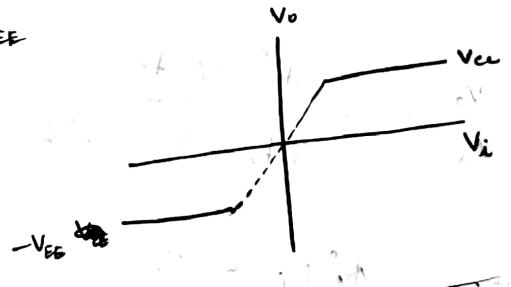
$A_c \ll A_d$

$$V_o \approx A_d \cdot \text{signal}$$

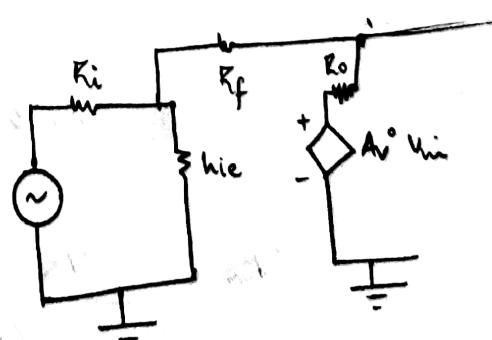
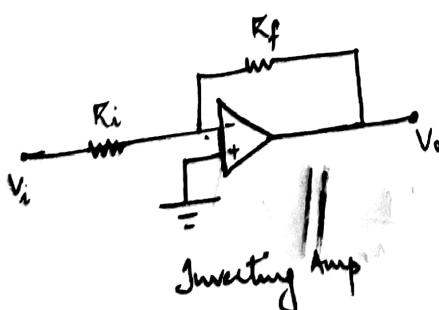
Open loop OP-AMP

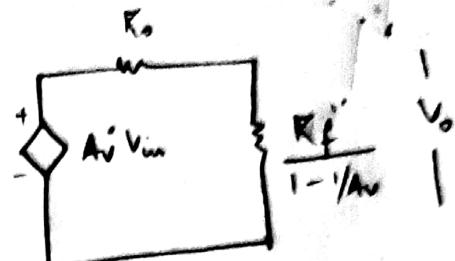
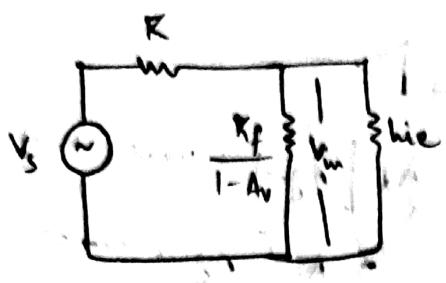


$$V_o = A_v (V_1 - V_2)$$



Inverting Amp





$$A_{v^*} = \frac{V_o}{V_{in}} \quad \text{and} \quad A_{v^*} = \frac{V_o}{V_s}$$

$$V_o = \frac{Av^* V_{in}}{\frac{R_o + R_f}{1 - \frac{1}{Av}}} \cdot \frac{R_f}{1 - \frac{1}{Av}}$$

$$= \left(\frac{Av^* V_{in} (1 - \frac{1}{Av})}{R_o + \frac{R_o}{Av} + R_f} \right) \cdot \frac{R_f}{1 - \frac{1}{Av}}$$

$$Av^* = \frac{V_o}{V_{in}} = \frac{Av^* R_f Av}{R_o Av - R_o + Av R_f}$$

$$Av^* = \frac{Av^* R_f + R_o}{R_o + R_f}$$

$R_o \rightarrow 0$

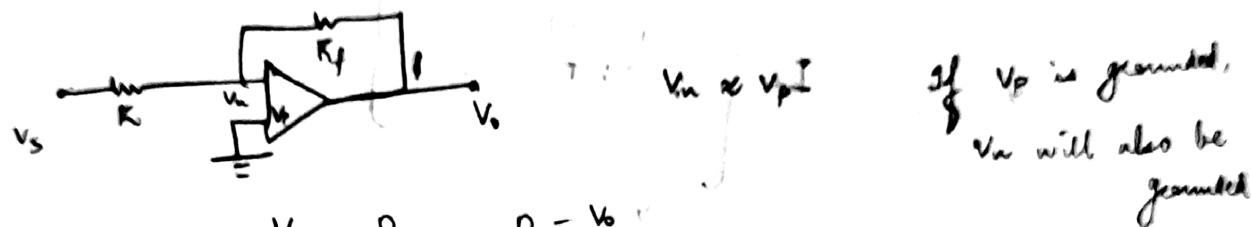
$Av \approx Av^*$

$$V_{in} = \frac{V_s}{R_i + \left(\frac{R_f}{1 - Av} \parallel z_i \right)}$$

$$\left(\frac{R_f}{1 - Av} \parallel z_i \right)$$

$$A_{vf} = \frac{-Y}{Y_f + \frac{1}{R_f} (Y + Y_f + Y_b)} \quad Y = Y_R$$

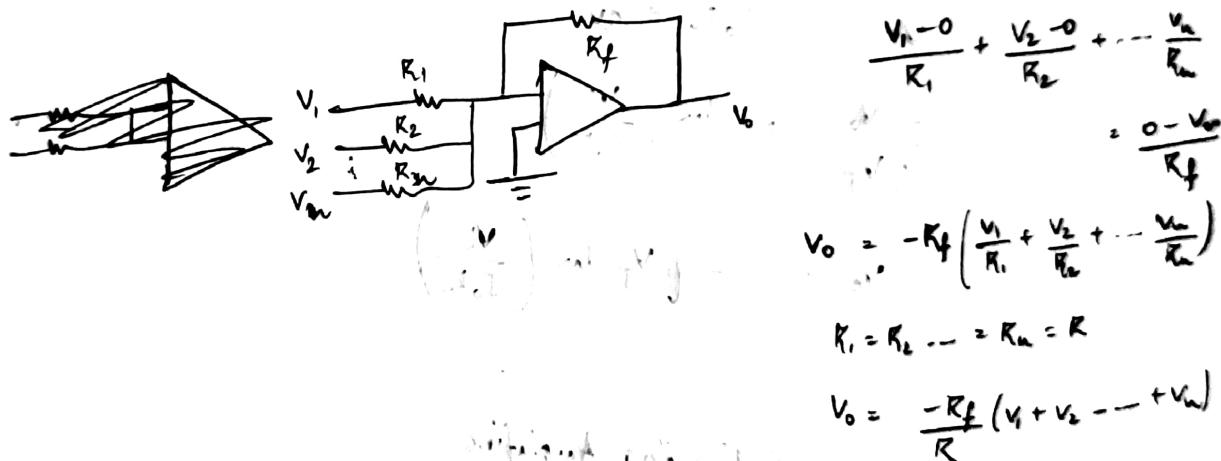
$$A_{vf} = \frac{-Y}{Y_f} \quad (\text{As } R_f \rightarrow \infty) \quad | \quad Y_f = \frac{1}{R_f}$$



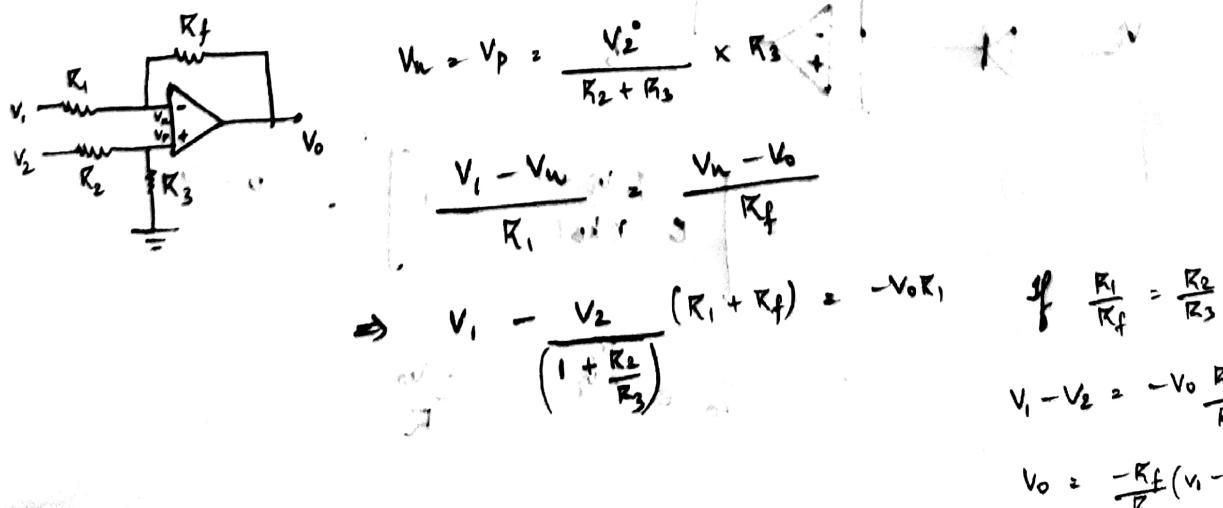
$$\frac{V_s - 0}{R} = \frac{0 - V_o}{R_f}$$

$$A_v = \frac{V_o}{V_s} = -\frac{R_f}{R}$$

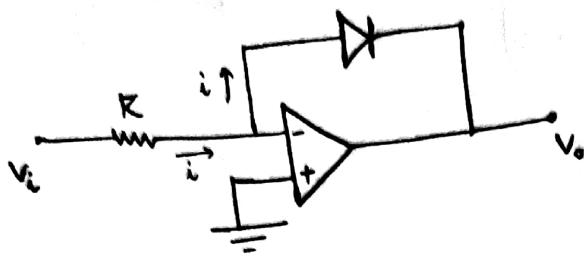
Adder circuit



Subtractor circuit



Log Amplifier



$$\frac{V_i - \theta}{R} = I_o \left[e^{\frac{q(V_o - \theta)}{\eta k_B T}} - 1 \right]$$

$$= I_o e^{\frac{-q V_o}{\eta k_B T}} - I_o$$

$$\approx I_o e^{\frac{-q V_o}{\eta k_B T}} \quad V_T = \frac{k_B T}{q}$$

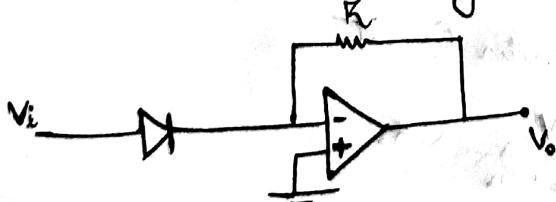
$$\frac{V_i}{R} \approx I_o e^{\frac{-V_o}{\eta V_T}}$$

$$V_T = I_o R e^{\frac{-V_o}{\eta V_T}}$$

$$\frac{-V_o}{\eta V_T} = \ln \left(\frac{V_i}{I_o R} \right)$$

$$V_o = -\eta V_T \ln \left(\frac{V_i}{I_o R} \right)$$

Anti-Log Amplifier

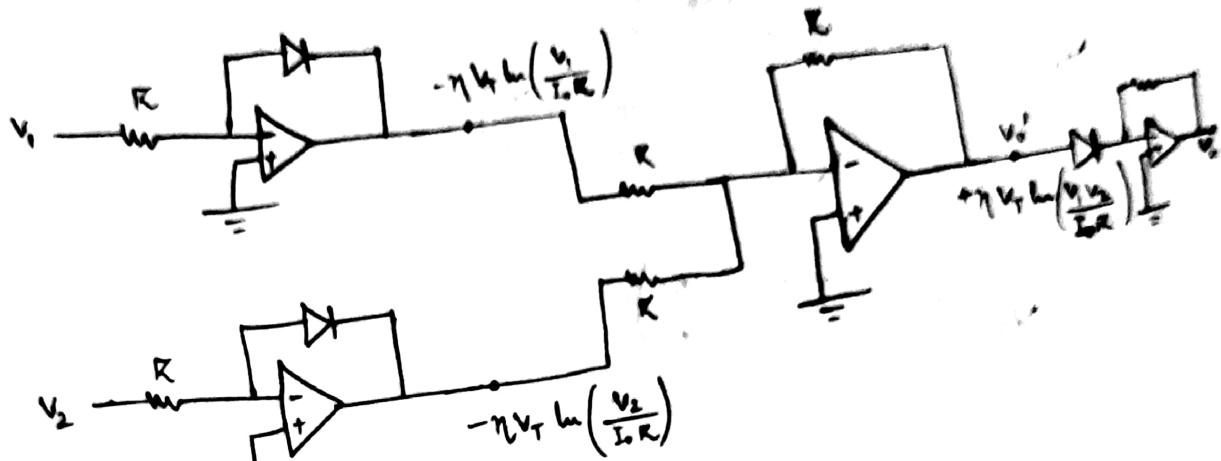


$$I_o \left[e^{\frac{q(V_i - \theta)}{\eta k_B T}} - 1 \right] = \frac{\theta - V_o}{R}$$

$$I_o e^{\frac{V_i}{\eta V_T}} = \frac{-V_o}{R}$$

$$V_o = -I_o R e^{\frac{V_i}{nV_T}}$$

Multiplication Circuit



$$V_0' = +nV_t \ln\left(\frac{V_1}{I_o R}\right) + nV_t \ln\left(\frac{V_2}{I_o R}\right)$$

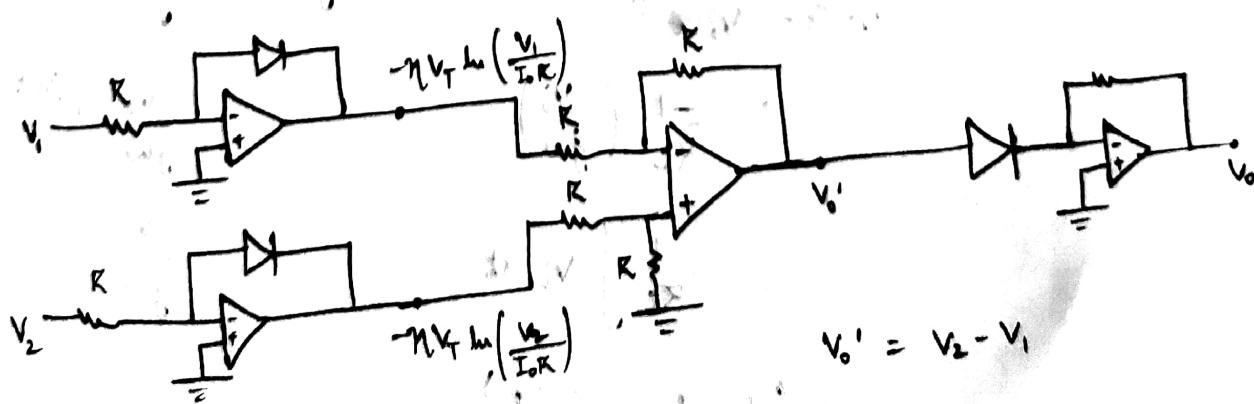
$$= +nV_t \ln\left(\frac{V_1 V_2}{(I_o R)^2}\right)$$

$$V_0 = -I_o R e^{\frac{nV_t}{nV_t} \ln\left(\frac{V_1 V_2}{(I_o R)^2}\right)}$$

$$= -\frac{I_o R}{e} \cdot \frac{V_1 V_2}{(I_o R)^2}$$

$$V_0 = \frac{-V_1 V_2}{(I_o R)^2}$$

Division Circuit



$$V_0' = V_2 - V_1$$

$$V_o' = -\eta V_T \ln \frac{V_2}{I_o R} + \eta V_T \ln \left(\frac{V_1}{I_o R} \right)$$

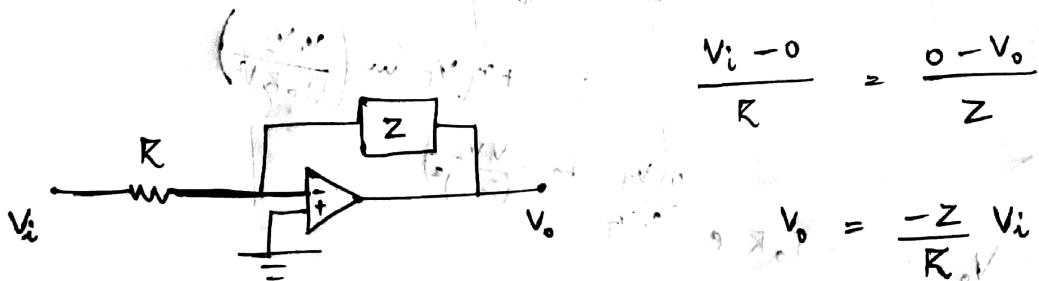
$$= +\eta V_T \ln \left(\frac{V_1}{V_2} \right)$$

$$V_o = -I_o R e^{\frac{V_o'}{\eta V_T}}$$

$$= -I_o R e^{\frac{\eta V_T \ln \left(\frac{V_1}{V_2} \right)}{\eta V_T}}$$

$$V_o = -I_o R \left(\frac{V_1}{V_2} \right)$$

Differentiator/Integrator Circuit



If $Z = C$

$$Vo = \frac{-1}{(j\omega C)R} Vi$$

$$Vi = Vo e^{j\omega t}$$

$$\int Vi dt = \int Vo e^{j\omega t} dt$$

$$= \frac{-1}{RC} \left(\frac{Vi}{j\omega} \right)$$

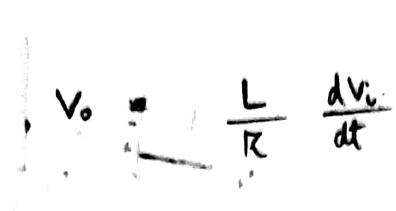
$$= \frac{Vi}{j\omega}$$

$$= \frac{-1}{RC} \int Vi dt$$

$$= \frac{Vi}{j\omega}$$

(Integrator circuit)

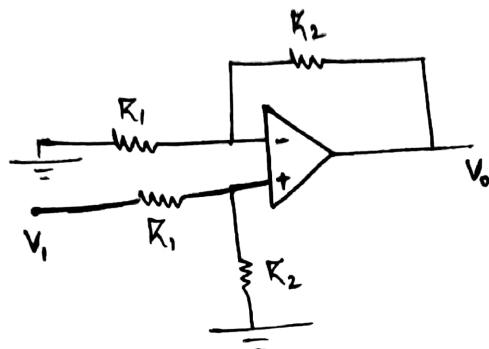
$$Z = L$$



$$V_o = \frac{L}{R} \frac{dV_o}{dt}$$

(Differentiator Circuit)

Non-Inverting OP-AMP



$$V_n + V_p = \frac{V_i}{R_1 + R_2} R_2$$

$$\frac{0 - V_n}{R_1} = \frac{V_n - V_o}{R_2}$$

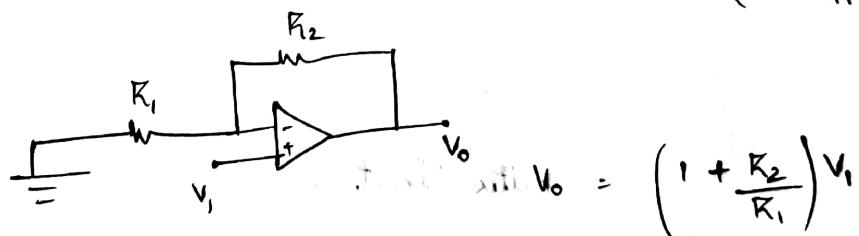
$$-R_2 V_n = R_1 V_n - R_1 V_o$$

$$R_1 V_o = (R_1 + R_2) V_n$$

$$R_1 V_o = (R_1 + R_2) \frac{V_i}{R_1 + R_2} R_2$$

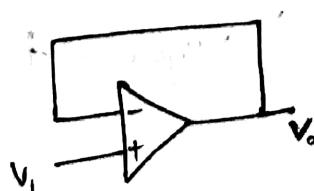
$$V_o = \frac{R_2}{R_1} V_n$$

$$V_o = \left(1 + \frac{R_2}{R_1}\right) V_n$$

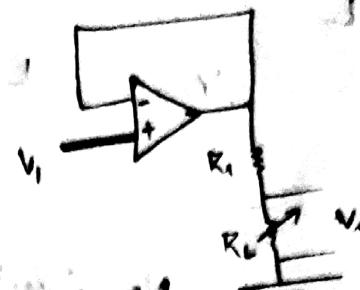
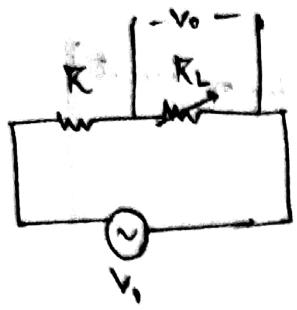


$$A_v = 1 + \frac{R_2}{R_1}$$

Unit Gain Amplifier

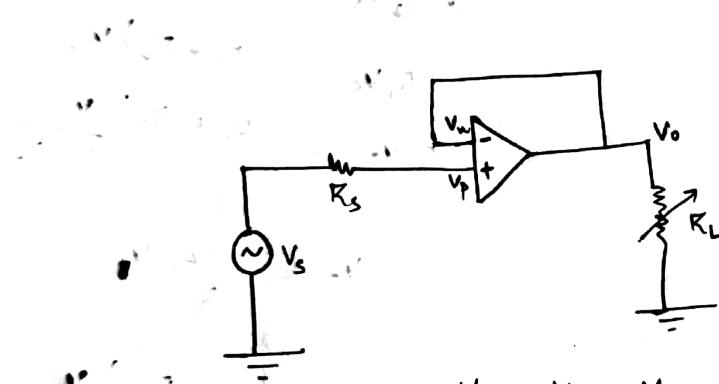
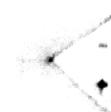


$$V_o = V_i$$

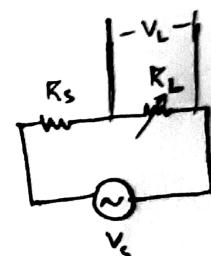


$$V_L = \frac{V_i}{R_i + R_L} R_L$$

$$= \frac{V_i}{1 + R_i/R_L}$$



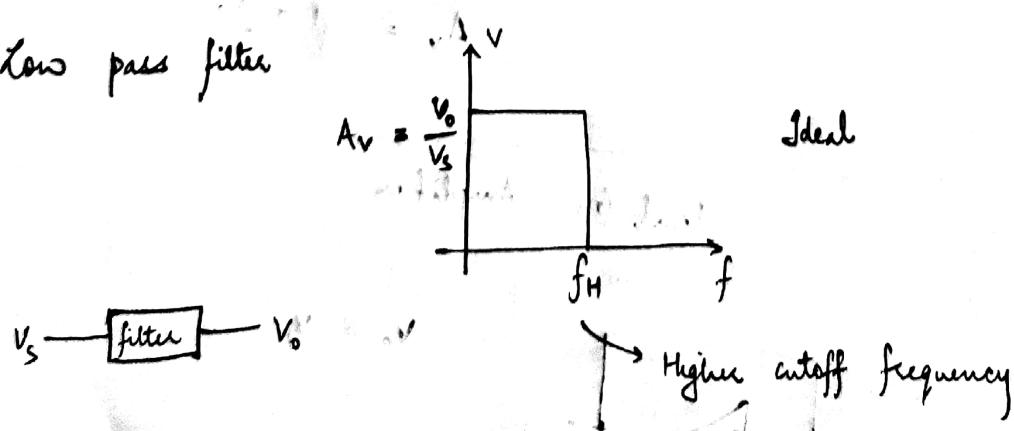
$$V_o = V_n = V_p$$



$$V_L = \frac{V_s}{R_s + R_L} R_L$$

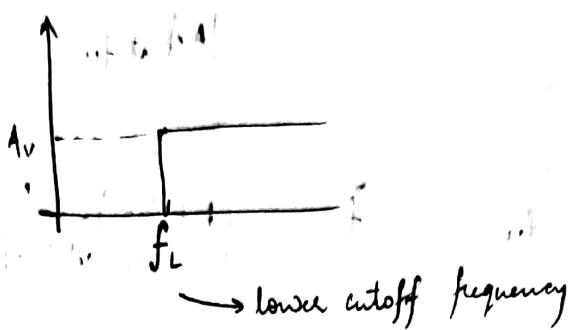
Filter circuit

Low pass filter

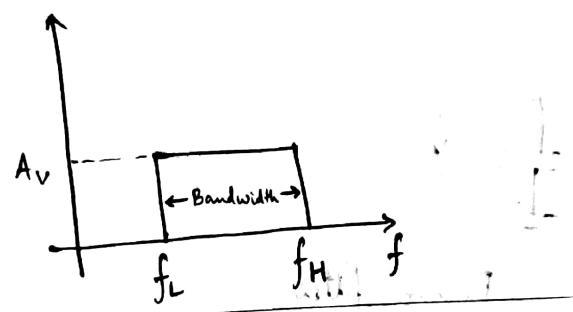


Higher cutoff frequency

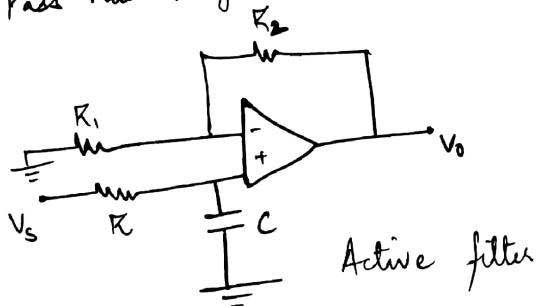
High pass filter



Band pass filter



Low Pass Filter Design



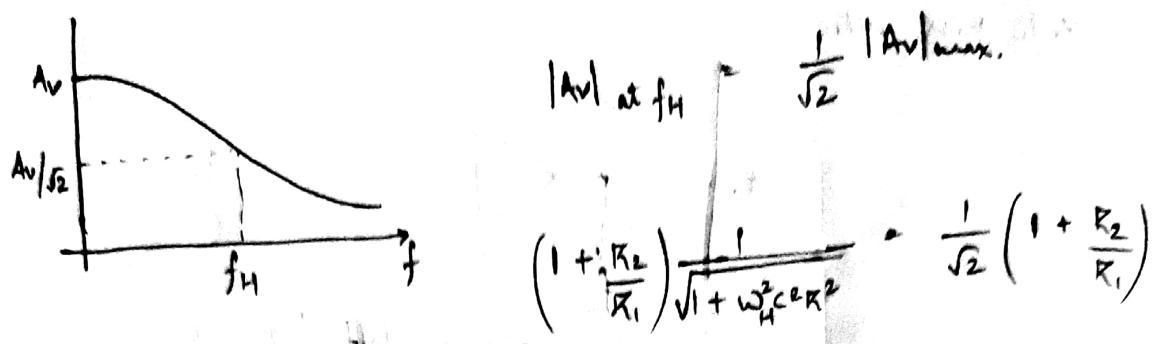
$$\begin{aligned}
 V_o &= V_p = \frac{V_s}{R + Z} z \\
 &= \frac{V_s}{R + \frac{1}{j\omega C}} \times \frac{1}{j\omega C} \\
 &= \frac{V_s}{1 + j\omega CR}
 \end{aligned}$$

$$\begin{aligned}
 V_o &= \left(1 + \frac{R_2}{R_1}\right) V_p \\
 &= \left(1 + \frac{R_2}{R_1}\right) \frac{V_s}{1 + j\omega CR}
 \end{aligned}$$

$$|V_o| = \left(1 + \frac{R_2}{R_1}\right) \frac{|V_s|}{\sqrt{1 + \omega^2 C^2 R^2}}$$

$$|A_v| = \frac{|V_o|}{|V_s|} = \left(1 + \frac{R_2}{R_1}\right) \frac{1}{\sqrt{1 + \omega^2 C^2 R^2}}$$

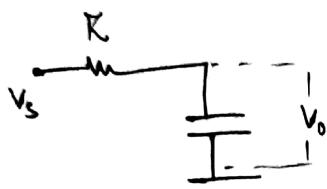
$$|A_v|_{max} = 1 + \frac{R_2}{R_1}$$



$$\left(1 + \frac{R_2}{R_1}\right) \frac{1}{\sqrt{1 + \omega_H^2 C^2 R^2}} = \frac{1}{\sqrt{2}} \left(1 + \frac{R_2}{R_1}\right)$$

$$\omega_H = \frac{1}{RC}$$

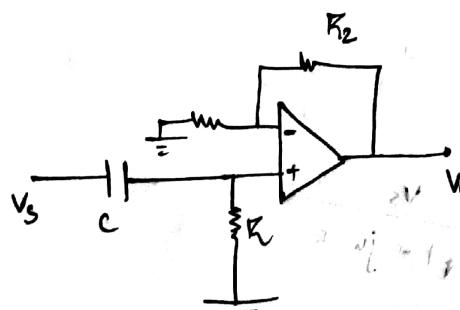
$$f_H = \frac{1}{2\pi RC}$$



Passive filter

$$V_o = V_p = \frac{V_s}{1 + j\omega RC}$$

High Pass Filter Design



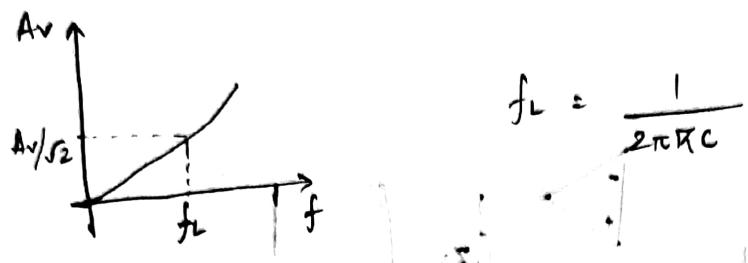
$$V_o = \left(1 + \frac{R_2}{R_1}\right) V_p$$

$$= \left(1 + \frac{R_2}{R_1}\right) \frac{V_s}{R + \frac{1}{j\omega C}}$$

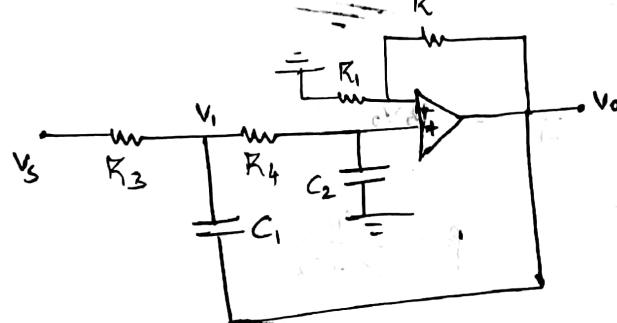
$$V_o = \left(1 + \frac{R_2}{R_1}\right) \frac{V_s R}{\left(R + \frac{1}{j\omega C}\right)}$$

$$|Av| = \frac{|V_o|}{|V_s|} = \left(1 + \frac{R_2}{R_1}\right) \frac{R}{\sqrt{R^2 + \frac{1}{\omega^2 C^2}}} \quad |V_o| \rightarrow |V_s| \quad \omega \rightarrow \infty \Rightarrow |Av|_{\text{max}}$$

$$\omega \rightarrow 0 \Rightarrow Av \rightarrow 0$$



Second-Order Filter



$$\frac{V_s - V_1}{R} = \frac{V_1 - V_o}{1/j\omega C_2} + \frac{V_1}{R_4 + \frac{1}{j\omega C_2}}$$

$$V_p = \frac{V_1}{R_4 + \frac{1}{j\omega C_2}} \times \frac{1}{j\omega C_2}$$

$$A_{uf} = \frac{Av}{1 + \beta Av}$$

negative feedback

$$A_{uf} > \frac{Av}{1 - \beta Av}$$

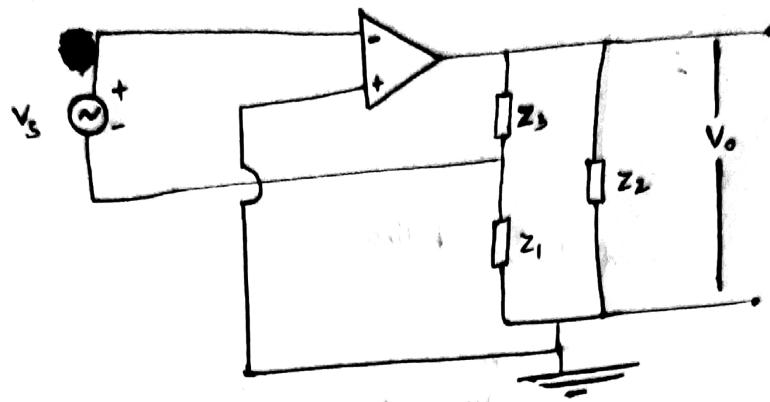
positive feedback

$$A_{uf} = \frac{Av}{1 - \beta Av} = \frac{V_o}{V_s}$$

loop gain $\Rightarrow \beta Av$

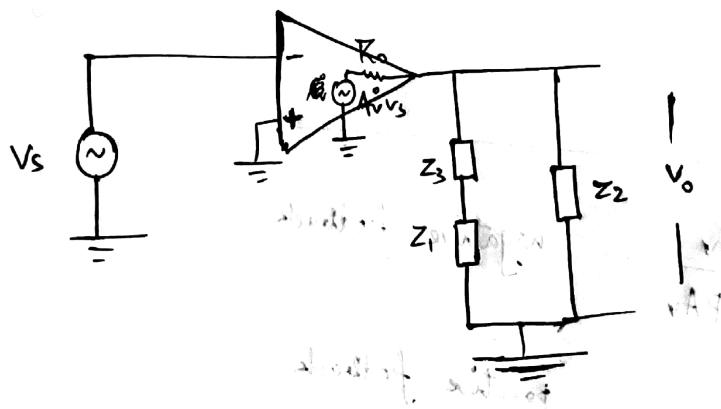
$\beta Av = 1$ (Barkhausen criterion)

Usually βAv taken greater than 1 due to losses in circuit -



$$V_f = \frac{V_o}{Z_1 + Z_3} \cdot Z_1 = \beta V_o$$

$$\beta = \frac{Z_1}{Z_1 + Z_3}$$



$$V_o = \frac{A_v^o V_s}{R_o + Z_L} \times Z_L$$

$$Z_L = \frac{(Z_1 + Z_3) \parallel Z_2}{(Z_1 + Z_3) \cdot Z_2}$$

$$= \frac{(Z_1 + Z_3) \cdot Z_2}{Z_1 + Z_2 + Z_3}$$

$$A_v \frac{V_o}{V_s} = \frac{\frac{A_v^o}{Z_1 + Z_2 + Z_3} \cdot (Z_1 + Z_3) Z_2}{R_o + \frac{(Z_1 + Z_3) \cdot Z_2}{Z_1 + Z_2 + Z_3}}$$

$$= \frac{A_v^o Z_2 (Z_1 + Z_3)}{R_o (Z_1 + Z_2 + Z_3) + (Z_1 + Z_3) Z_2}$$

$$BAv = \frac{z_1}{z_1 + z_3} \cdot \frac{Av^0 z_2 (z_1 + z_3)}{R_o(z_1 + z_2 + z_3) + (z_1 + z_3) z_2}$$

$$= \frac{Av^0 z_1 z_2}{R_o(z_1 + z_2 + z_3) + (z_1 + z_3) z_2}$$

~~$z_i = jx_i$~~ $i = 1, 2, 3$

$$BAv = \frac{-|Av^0| z_1 z_2}{R_o(z_1 + z_2 + z_3) + z_2 (z_1 + z_3)}$$

$$= \frac{\cancel{|Av^0|} x_1 x_2}{jR_o(x_1 + x_2 + x_3) - x_2 (x_1 + x_3)}$$

$$\therefore BA_v = \text{Real} \Rightarrow x_1 + x_2 + x_3$$

$$x_1 + x_3 = -x_2 \quad (\text{For } BA_v \text{ to be positive})$$

$$BAv = \frac{|Av^0| x_1 x_2}{x_2^2} = \frac{|Av^0| \frac{x_1}{x_2}}{x_2}$$

$x_1, x_2 \Rightarrow$ Capacitors $\left. \begin{array}{l} \text{Colpitt} \\ \text{Oscillator} \end{array} \right\}$
 $x_3 \Rightarrow$ Inductors

$x_1, x_2 \Rightarrow$ Inductors $\left. \begin{array}{l} \text{Hartley} \\ \text{Oscillator} \end{array} \right\}$
 $x_3 \Rightarrow$ Capacitors

