



School of Computer Engineering and Technology

SSC lab -Assignment No.1

Date of Submission: 9th-April-2022

Assignment Title: Design of Pass 1 of Two Pass Assembler.

Aim: Design suitable data structures and implement Pass 1 of 2 Pass Assembler for pseudo machine.

Objective: Design suitable data structures and implement Pass 1 of 2 Pass Assembler for pseudo machine. Subset should consist of a few instructions from each category & few assembler directives.

Theory:

1. Design Specification of an Assembler:
Analysis Phase
2. Design of a Two Pass Assembler:
Algorithm for Pass 1
3. Contents of OPTAB
4. Error Listing & Error Handling

Input: Assembly Language Program for pseudo machine

Output: Symbol Table, Intermediate Code.

Conclusion:

Platform: JAVA