

VER A / 290108

PT7320-51(2)-1W(+)

GbE 1000Base-LX 10 km SFP Transceiver

1 Features

- 1.1 Transceiver unit with independent
 - 1310nm MQW FP Laser diode transmitter
 - InGaAs PIN photodiode receiver
- 1.2 Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- 1.3 Compliant with 1.25 Gbps 1000Base-LX Ethernet
- 1.4 Compatible with 1.063 Gbps Fiber Channel
- 1.5 Digital diagnostic monitoring
- 1.6 Hot-pluggable
- 1.7 Metal enclosure for lower EMI
- 1.8 +3.3V Single power supply
- 1.9 Operation data rate from 100Mb/s to 1.27Gb/s
- 1.10 Qualified to meet the intent of Bellcore reliability practices
- 1.11 LVPECL logic interface simplifies interface to external circuitry
- 1.12 LVTTTL logic level RX LOS
- 1.13 With pull de-latch
- 1.14 Links of 10 km with 9/125 μ m single mode fiber (SMF) of maximum interconnect distances
- 1.15 Complies with RoHS directive (2002/95/EC)

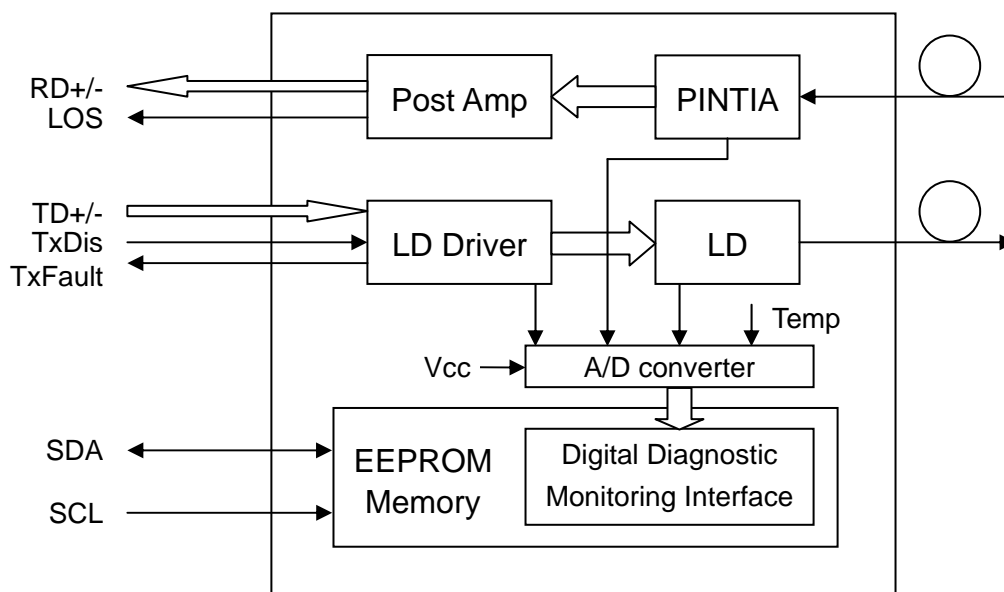


2 Applications

- 2.1 Gigabit Ethernet
- 2.2 Fiber Channel

3 General

The optical transceiver is compliant with the Small Form-Factor Pluggable (SFP) Multi-Source Agreement (MSA) and SFF-8472. It offers previously unavailable system cost, upgrade, and reliability benefits by virtue of being hot-pluggable.



3.1 Transmitter Section

Transmitter is designed for single mode fiber and operates at a nominal wavelength of 1310nm. The transmitter module uses a MQW FP laser diode and full IEC825 and CDRH class 1 eye safety. The output power can be disabled via the single TxDis pin. Logic LVTTTL HIGH level disables the transmitter. It contains APC function, temperature compensation circuit, PECL data inputs, LVTTTL Txdis input and Tx fault Output interface.

3.2 Receiver Section

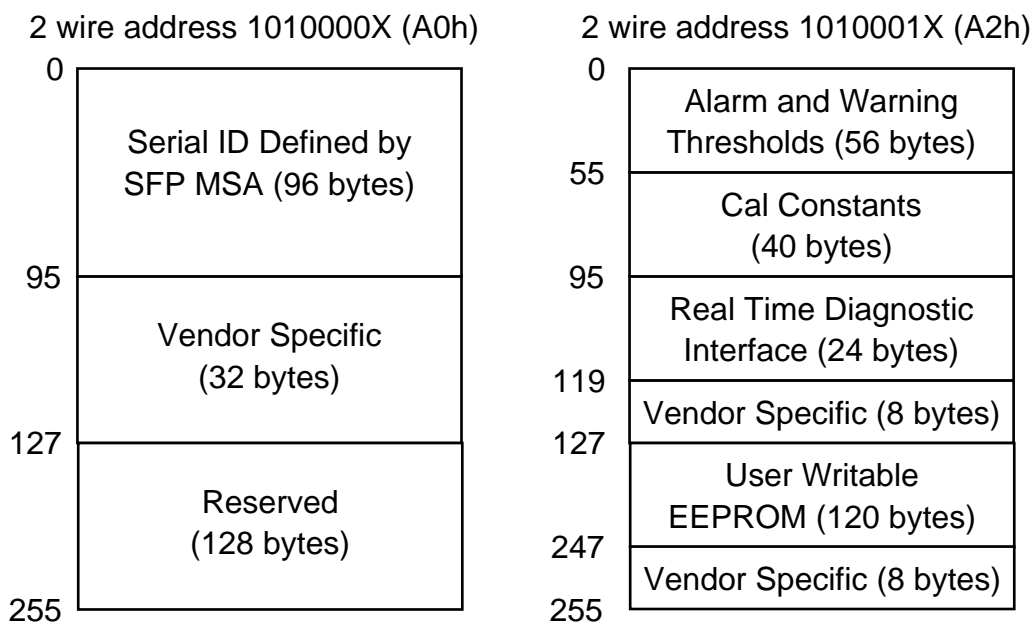
The receiver section uses a hermetic packaged front end receiver (InGaAs PIN and preamplifier). The postamplifier is ac coupled to preamplifier through a capacitor and a low pass filter. The capacitor and LPF are enough to pass the signal from 5Mb/s to 1270Mb/s without significant distortion or performance penalty. The LPF limits the preamplifier bandwidth to improve receiver sensitivity. As the input optical is decreased, LOS will switch from low to high. As the input optical power is increased from very low levels, LOS will switch back from high to low.

3.3 EEPROM Section

The optical transceiver contains an EEPROM. It provides access to sophisticated identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C01A/02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL, Mod Def 1). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field defines as following.



4 Performance Specifications

4.1 Absolute Maximum Ratings

Parameter	Symbol		Min.	Max.	Unit
Storage Temperature	T _{stg}		-40	+85	°C
Operating Temperature	T _{OP}	PT7320-51-1W(+)	0	+70	°C
		PT7320-52-1W(+)	-40	+85	
Input Voltage	-		GND	V _{cc}	V
Power Supply Voltage	V _{cc} -V _{ee}		-0.5	+3.6	V

4.2 Operating Environment

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage	V_{cc}	+3.1	+3.3	+3.5	V	-
Power Supply Current	I_{cc}	-	200	300	mA	-
Case Temperature (Operating)	T_C	PT7320-51-1W(+)	0	-	+70	°C
		PT7320-52-1W(+)	-40	-	+85	
Data Rate	-	-	1.25	-	Gb/s	-

4.3 Transmitter E-O Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Center Wavelength	λ	1270	1310	1355	nm	1
Spectral Width(RMS)	$\Delta\lambda$	-	-	2.8	nm	1
Average Optical Output Power	P _o	-10	-	-3	dBm	-
Extinction Ratio	E _r	9	-	-	dB	-
Transmitter Enable Voltage	V _{EN}	0	-	0.8	V	-
Transmitter Disable Voltage	V _D	2.0	-	V _{cc}	V	-
Single Ended Data Input Swing	V _{INpp}	250	-	1200	mV	-
Optical Rise/Fall Time (20%-80%)	tr/tf	-	-	0.26	ns	-

4.4 Receiver O-E Characteristics

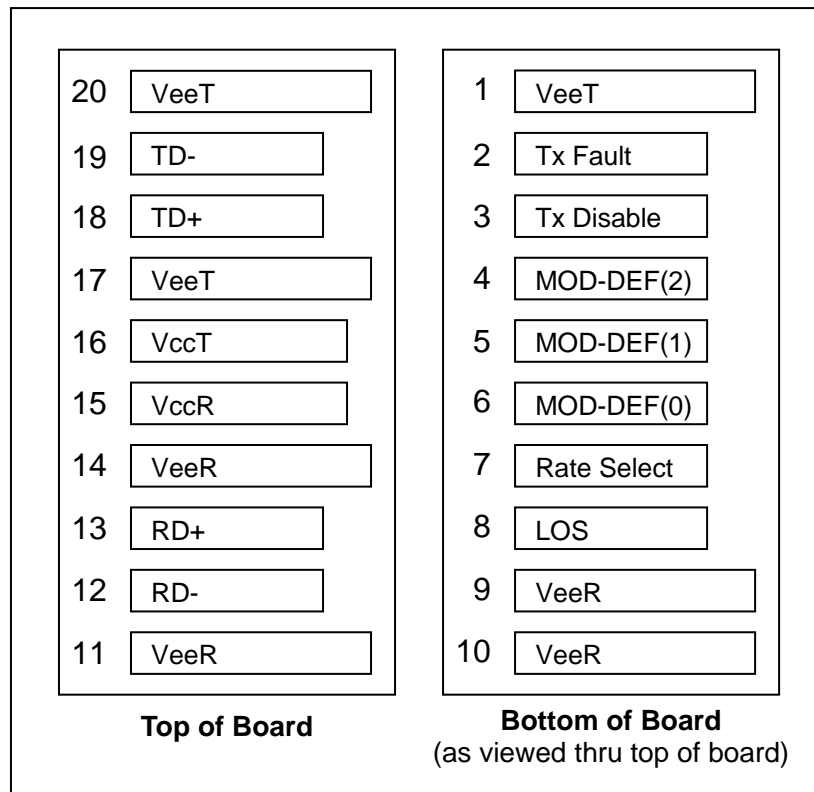
Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Operate Wavelength	-	1260	-	1580	nm	-
Sensitivity	S _{en}	-	-25	-22	dBm	2
Saturation	P _{sat}	-3	-	-	dBm	2
LOS Asserted	-	-40	-	-	dBm	High level: Alarm
LOS De-Asserted	-	-	-	-22	dBm	
LOS Hysteresis	-	-	1.5	-	dB	
Single Ended Data Output Swing	V _{OUTPP}	185	-	1000	mV	-
LOS Low Voltage	V _{Lout}	-	-	0.8	V	-
LOS High Voltage	V _{Hout}	2.0	-	-	V	-

Notes:

- Also specified to meet curves in FC-PI 13.0 Figures 18 and 19, which allow trade-off between wavelength, spectral width and OMA.
- Measured with PRBS 2⁷-1 at 10⁻¹² BER.

5 Pin Definitions

5.1 Pin Diagram



5.2 Pin Descriptions

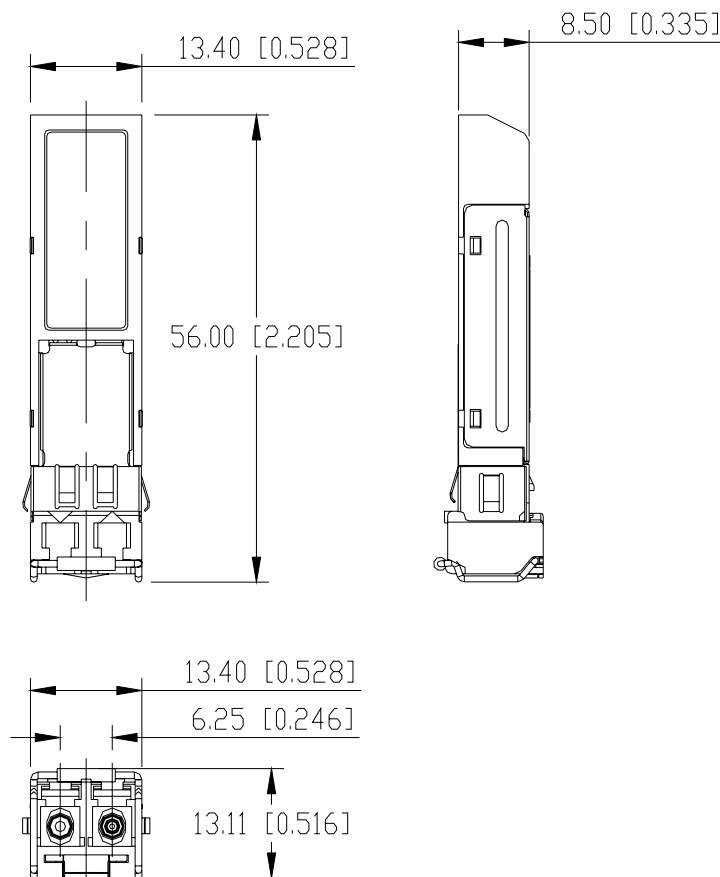
Pin#	Name	Function	Notes
1	VeeT	Transmitter Ground	-
2	TX Fault	Transmitter Fault Indication	Note 1
3	TX Disable	Transmitter Disable	Note 2, Module disables on high or open
4	MOD-DEF2	Module Definition 2	Note 3, 2 wire serial ID interface
5	MOD-DEF1	Module Definition 1	Note 3, 2 wire serial ID interface
6	MOD-DEF0	Module Definition 0	Note 3, Grounded in Module
7	Rate Select	Not Use	-
8	LOS	Loss of Signal	Note 4
9	VeeR	Receiver Ground	Note 5
10	VeeR	Receiver Ground	Note 5
11	VeeR	Receiver Ground	Note 5
12	RD-	Inv. Received Data Out	Note 6
13	RD+	Received Data Out	Note 6
14	VeeR	Receiver Ground	Note 5
15	VccR	Receiver Power	Note 7, 3.3V± 5%
16	VccT	Transmitter Power	Note 7, 3.3V± 5%
17	VeeT	Transmitter Ground	Note 5

Pin#	Name	Function	Notes
18	TD+	Transmit Data In	Note 8
19	TD-	Inv. Transmit Data In	Note 8
20	VeeT	Transmitter Ground	Note 5

Notes:

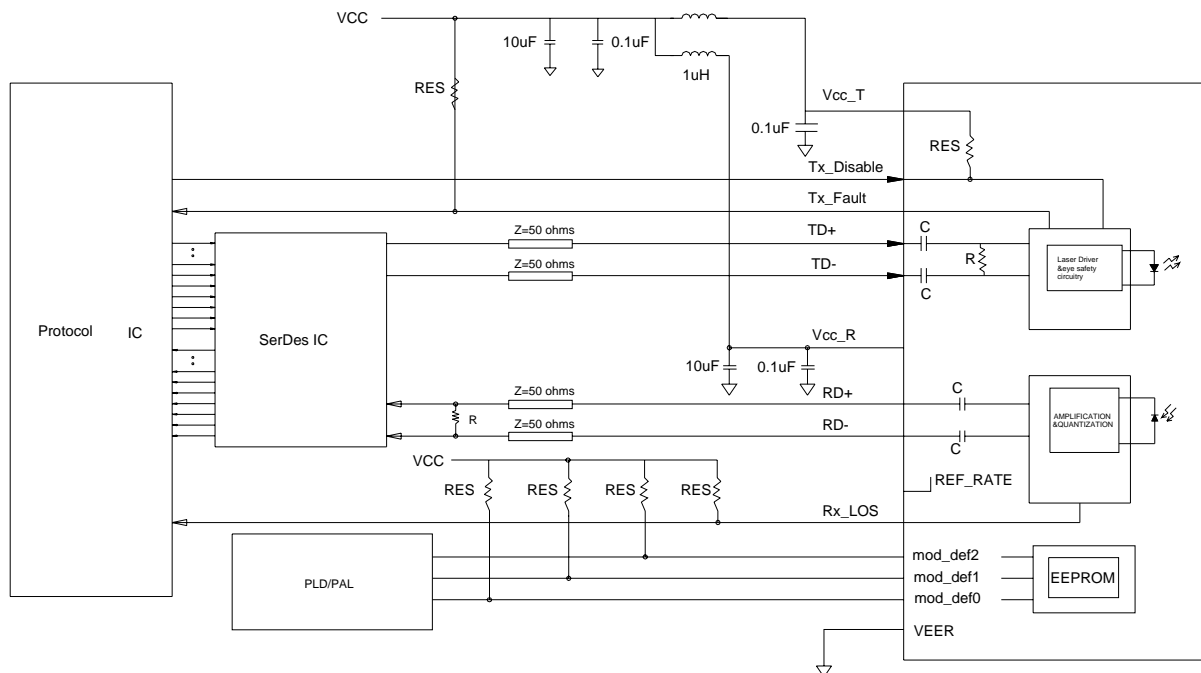
1. TX Fault is an open collector/drain output, which should be pulled up with a 4.7K–10K Ω resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
2. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7–10 K Ω resistor. Its states are:
Low (0 – 0.8V): Transmitter on
(>0.8, < 2.0V): Undefined
High (2.0 – 3.465V): Transmitter Disabled
Open: Transmitter Disabled
3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10K Ω resistor on the host board. The pull-up voltage shall be VccT or VccR.
Mod-Def 0 is grounded by the module to indicate that the module is present
Mod-Def 1 is the clock line of two wire serial interface for serial ID
Mod-Def 2 is the data line of two wire serial interface for serial ID
4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10K Ω resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
5. VeeR and VeeT may be internally connected within the SFP module.
6. RD-/+: These are the differential receiver outputs. They are AC coupled 100 Ω differential lines which should be terminated with 100 Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board.
7. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V \pm 5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.
8. TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100 Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board.

6 Mechanical Specification



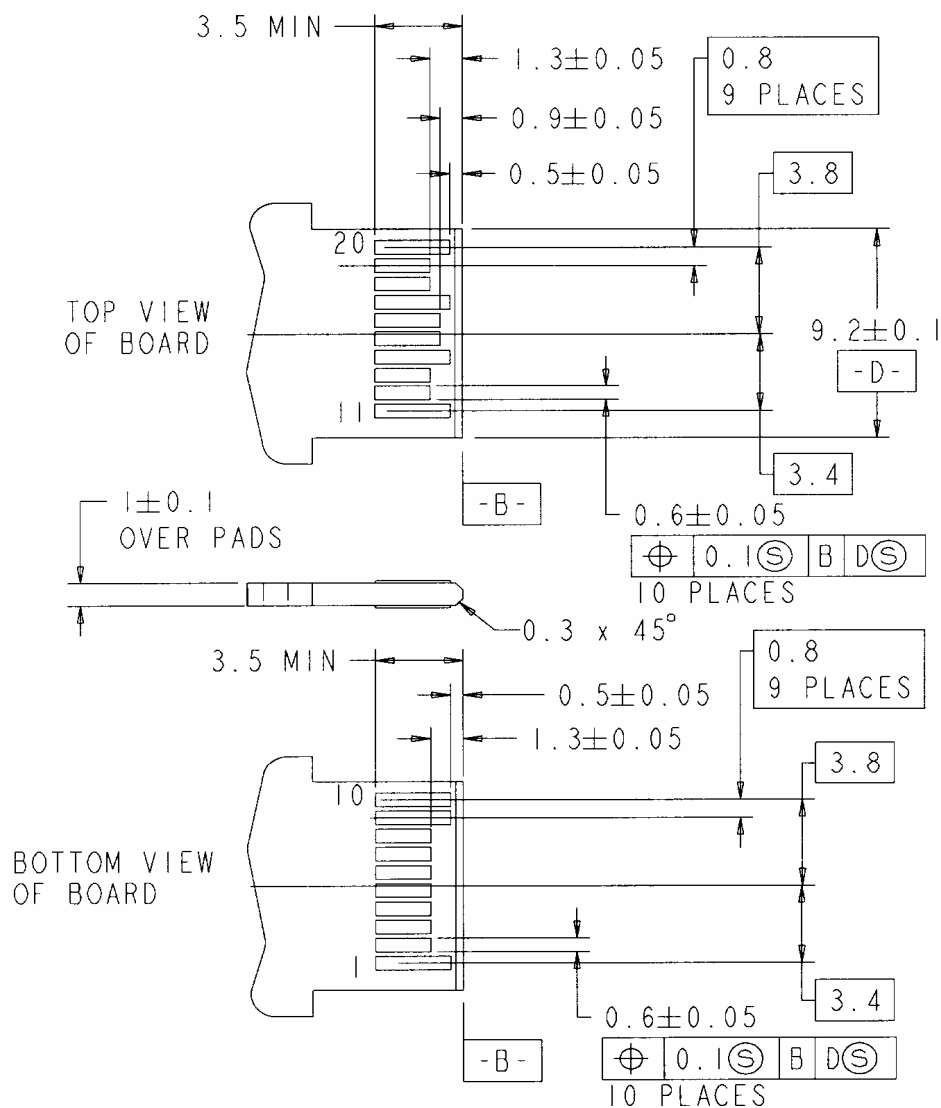
(Unit: mm [inch])

7 Recommended Circuit



NOTE: 4.7K ohms<RES<10K ohms

8 Pattern Layout of SFP Printed Circuit Board



(Unit: mm)

9 Specification for Environmental Protection

9.1 RoHS-5 compliant product:

Qualified as RoHS compliant product for network application based on the lead(Pb) RoHS exemption clause of "lead in solders for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunications".

RoHS-5 compliant products 2nd level interconnect material contained lead (Pb) and Pb concentration>1000ppm, e.g. soldering material, or the terminals, Pins of electronic components, or pads of printed circuit board, exempted by RoHS(2002/95/EC) directive.

Other materials (excluded exempted material) comply with threshold value of RoHS banned substance in homogenous material.

9.2 RoHS-6 compliant product:

Qualified as RoHS compliant and lead-free product based on lead-free soldering process, all materials (excluded exempted material) comply with threshold value of RoHS banned substance in homogenous material.

10 Ordering Information

Part Number	Operating Case Temperature	RoHS Compliant
PT7320-51-1W	0 ~ +70°C	RoHS-5
PT7320-52-1W	-40 ~ +85°C	RoHS-5
PT7320-51-1W+	0 ~ +70°C	RoHS-6
PT7320-52-1W+	-40 ~ +85°C	RoHS-6