# 2023 超大型積體電路電腦輔助設計概論

### 2023 Introduction to VLSI CAD Lab 9

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※作業要求的圖請使用電腦截圖程式截取,請勿用手機拍照的方式繳交 ※Report 檔請以 pdf 的格式繳交

#### A. NAND

- 1. Presim
  - i. 請截取 terminal 顯示 job concluded 的圖

vlsicad9:/home/user2/vlsi23/vlsi23101/Desktop/Lab8\_total % hspice testbench.sp -o test.lis
Using: /usr/cad/synopsys/hspice/cur/hspice/linux64/hspice 'testbench.sp' -o test.lis
>info: \*\*\*\*\* hspice job concluded \_\_\_\_\_

vlsicad9:/home/user2/vlsi23/vlsi23101/Desktop/Lab8\_total %

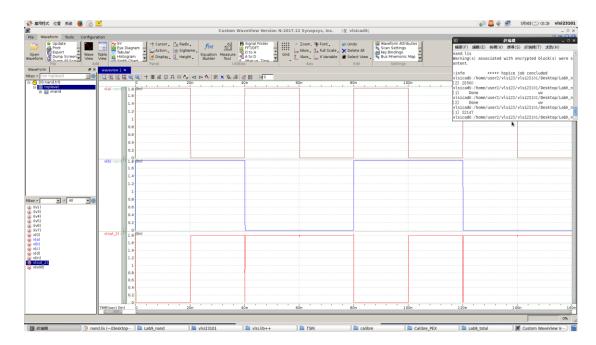
ii. 請截取 WaveView 中的波形



#### 2. Post-sim

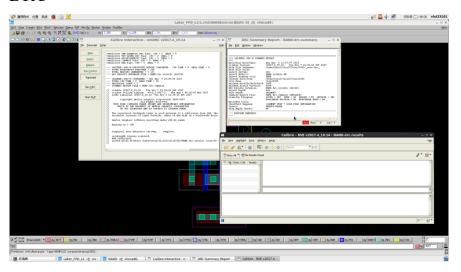
i. 請截取 terminal 顯示 job concluded 的圖

ii. 請截取 WaveView 中的波形

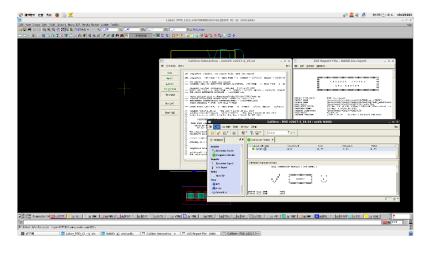


#### iii. DRC/LVS 結果

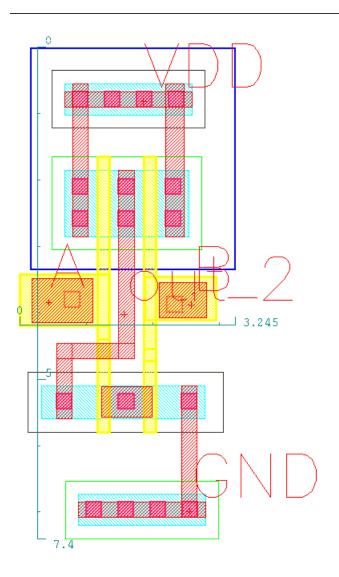
#### **DRC**



#### LVS



# iv. Layout 截圖



3. 嘗試簡單說明 Presim 與 Post-sim 結果比較

放大後仔細看,Post-sim 有一點 delay

#### B. NOR

- 1. Presim
  - i. 請截取 terminal 顯示 job concluded 的圖

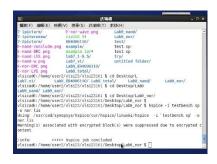
vlsicad9:/home/user2/vlsi23/vlsi23101/Desktop/Lab8\_total % hspice testbench.sp -o test.lis Using: /usr/cad/synopsys/hspice/cur/hspice/linux64/hspice 'testbench.sp' -o test.lis

>info: \*\*\*\*\* hspice job concluded vlsicad9:/home/user2/vlsi23/vlsi23101/Desktop/Lab8\_total % | ii. 請截取 WaveView 中的波形

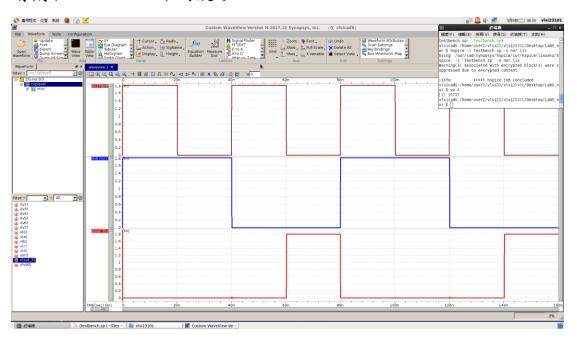


# 2. Post-sim

i. 請截取 terminal 顯示 job concluded 的圖

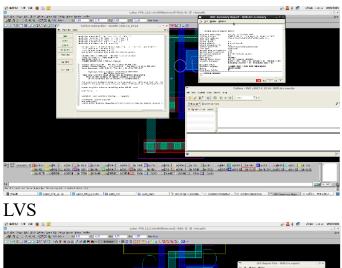


ii. 請截取 WaveView 中的波形



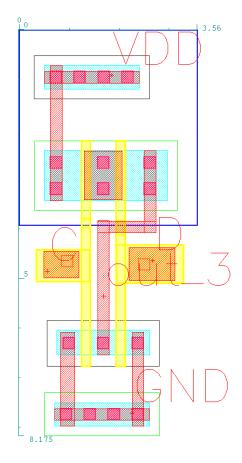
iii. DRC/LVS 結果

# DRC



# Company of the compan

# iv. Layout 截圖



3. 嘗試簡單說明 Presim 與 Post-sim 結果比較 放大後仔細看, Post-sim 有一點 delay

# C. 心得討論

學習到如何使用 laker,並上網查找相關資料