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Your puzzle answer was 7440382076205.

--- Part Two ---

A version 2 decoder chip doesn't modify the values being written at all. Instead, it acts as a **memory address decoder**. Immediately before a value is written to memory, each bit in the bitmask modifies the corresponding bit of the destination memory address in the following way:

- A floating bit is not connected to anything and instead fluctuates unpredictably. In practice, this means the floating bits will take on all possible values, potentially causing many memory addresses to be written all at once!

```
mask = 00000000000000000000000000000000X1001X
mem[42] = 100
mask = 00000000000000000000000000000000X0XX
mem[26] = 1
```

```
address: 00000000000000000000000000000000101010 (decimal 42)
mask:    00000000000000000000000000000000X1001X
result:  00000000000000000000000000000000X1101X
```

After applying the mask, four bits are overwritten, three of which are different, and two of which are floating. Floating bits take on every possible combination of values; with two floating bits, four actual memory addresses are written:

[illegible]

```
address: 000000000000000000000000000000000011010 (decimal 26)
mask:    0000000000000000000000000000000000X0XX
result:  00000000000000000000000000000000001X0XX
```

2/3

[illegible]

The entire 36-bit address space still begins initialized to the value 0 at every address, and you still need the sum of all values left in memory at the end of the program. In this example, the sum is 208.

Execute the initialization program using an emulator for a version 2 decoder chip. What is the sum of all values left in memory after it completes?

Answer: [Submit]

Although it hasn't changed, you can still **get your puzzle input**.

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