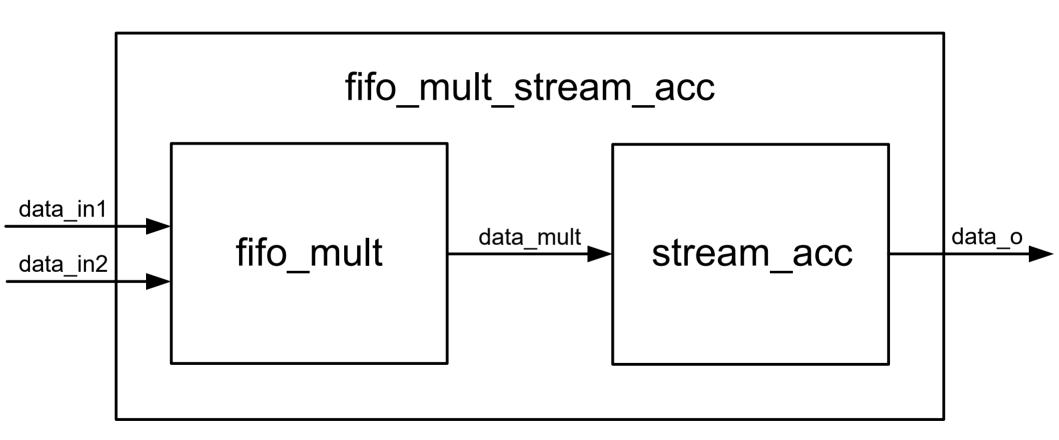
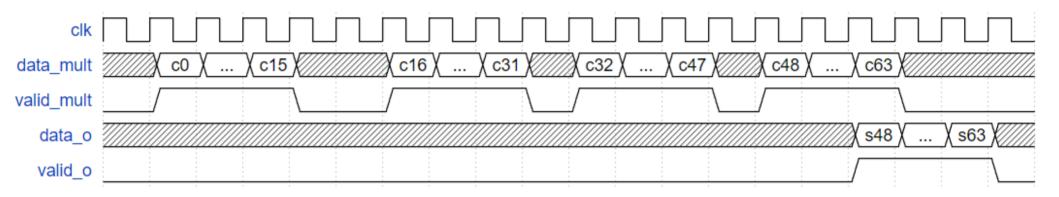
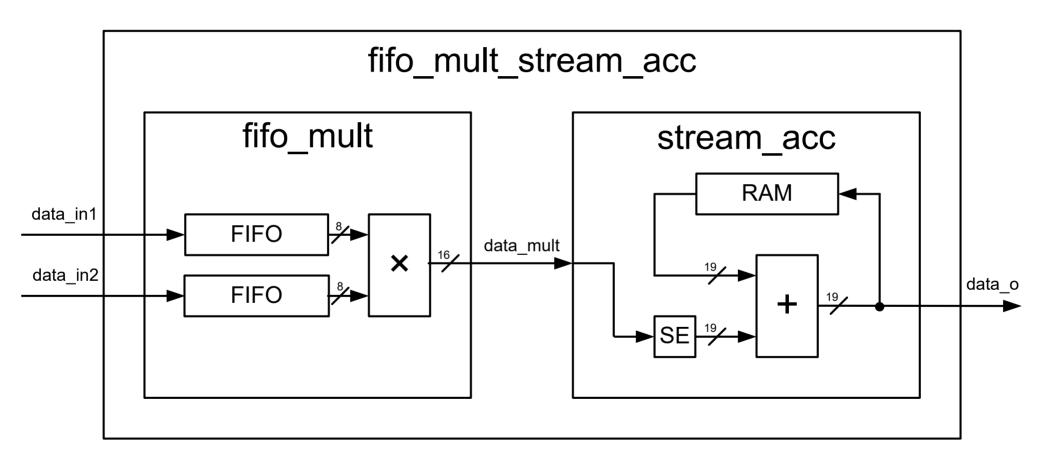
# Модуль fifo\_mult\_stream\_acc



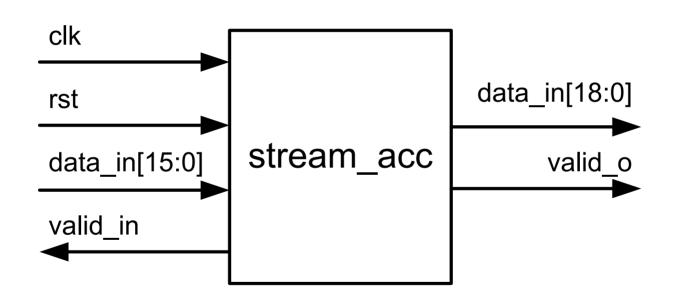
### Временная диаграмма stream\_acc



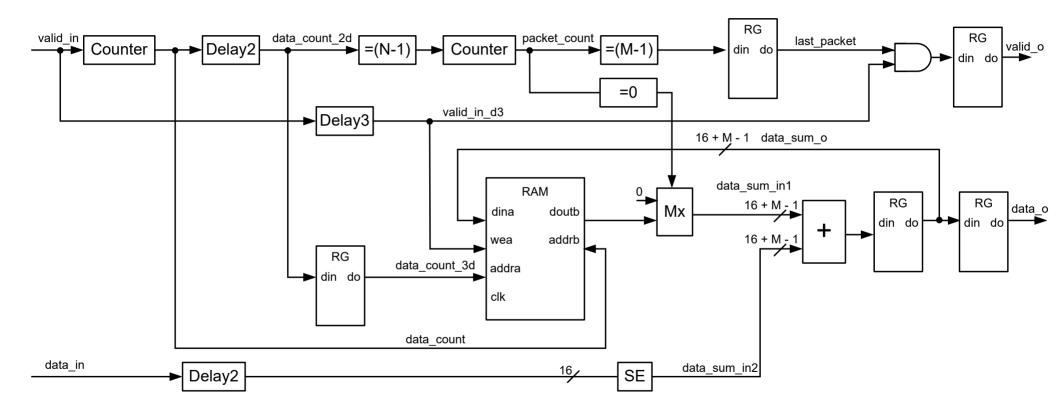
#### Moдуль fifo\_mult\_stream\_acc / stream\_acc



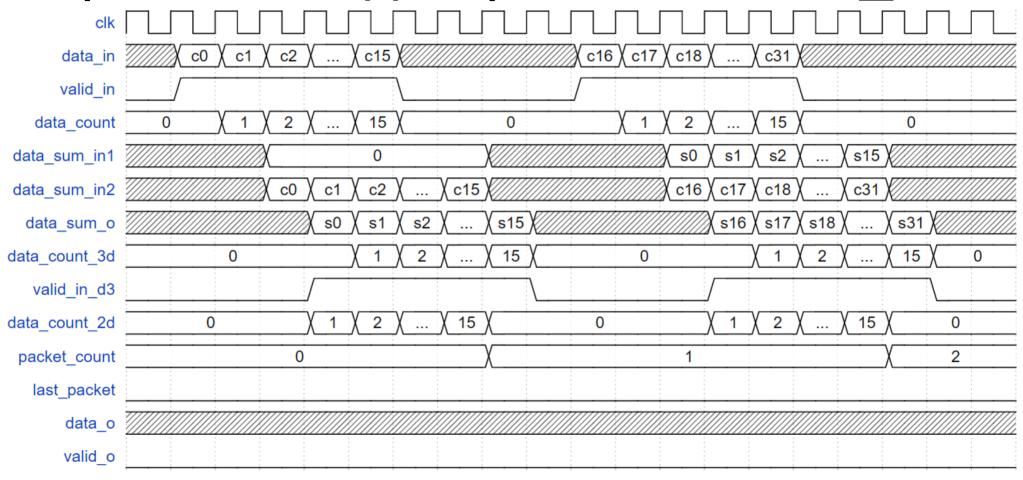
# Интерфейсы stream\_acc



# Структурная схема stream\_acc



#### Временная диаграмма stream\_acc



#### Временная диаграмма stream\_acc

