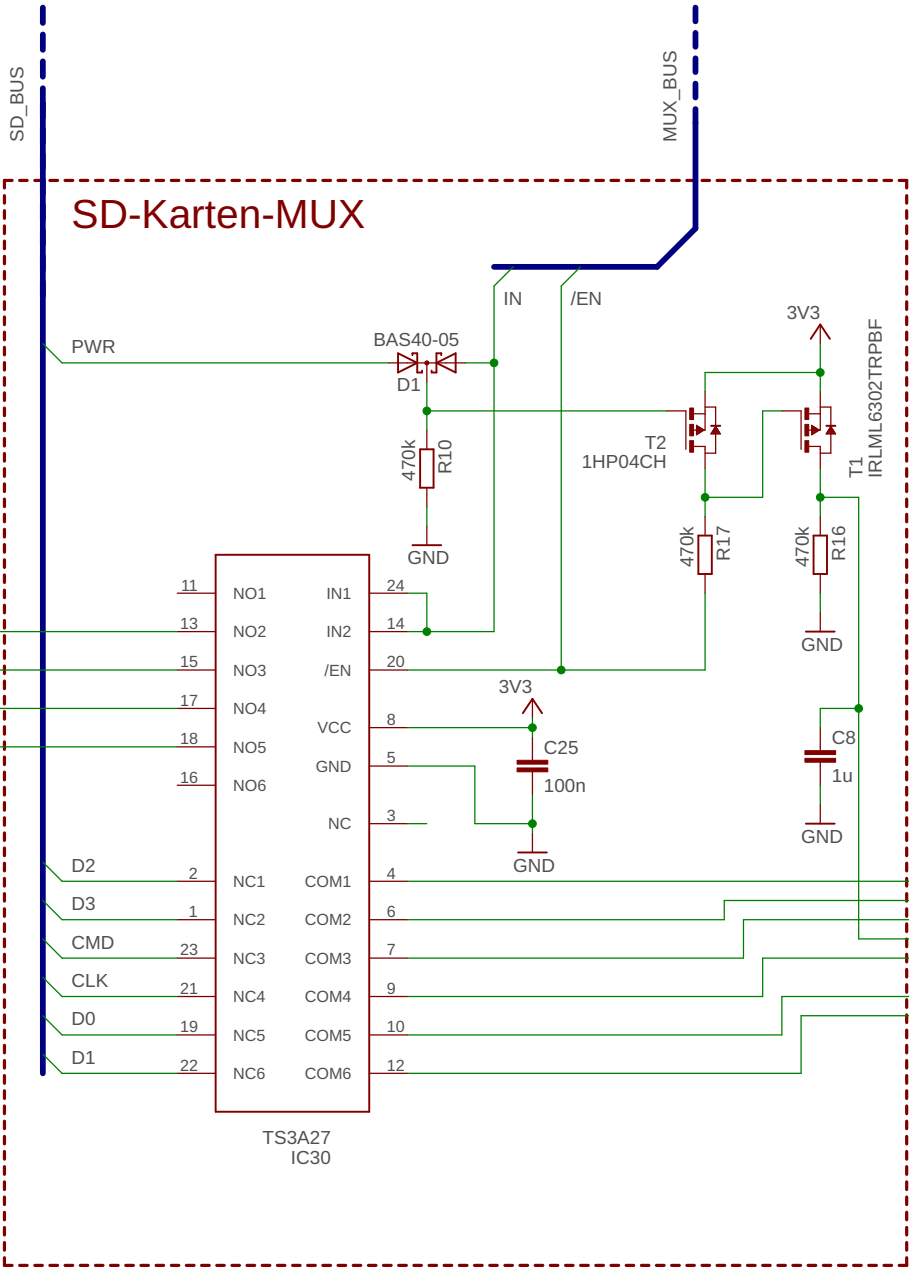
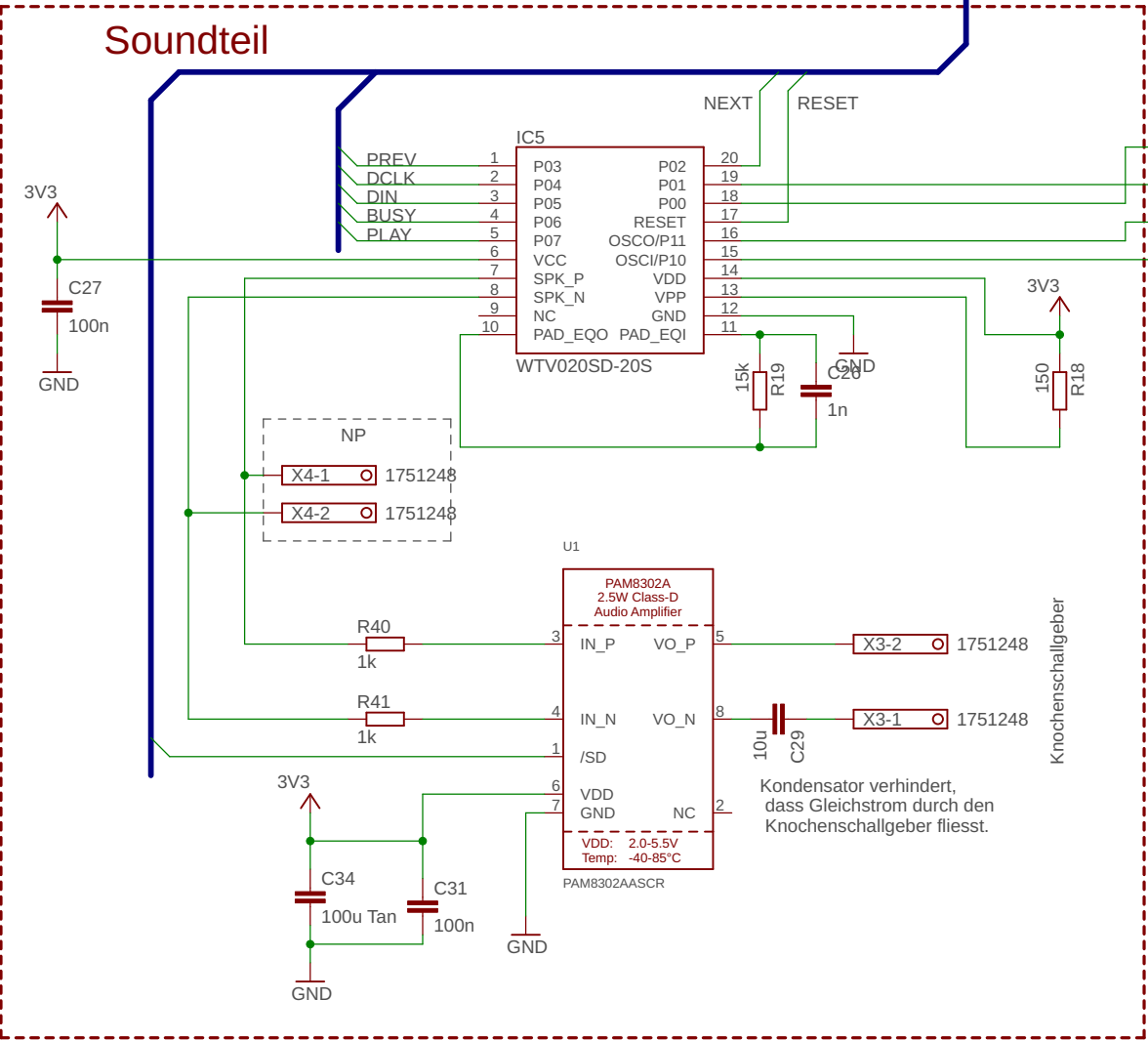
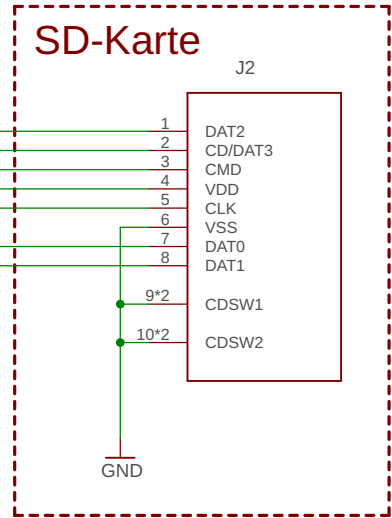


The diagram shows a circuit powered by a 5V source. IC1 (MCP73831) is connected with its VDD/VBAT pin (4) to the 5V source via capacitor C1 (10uF). Its PROG pin (5) is connected to a 2k resistor (R1) which is then connected to the STAT/VSS pin (1). The VBAT pin (3) is connected to a common rail that also includes capacitor C2 (10uF) and two 1751248 components. This common rail is also connected to the IN pin (1) of IC2 (LT1963EST-1.5) via capacitor C6 (10uF). The OUT pin (3) of IC2 is connected to the 3V3 output, which is also connected to capacitor C5 (100nF). The GND pin (2) of IC2 is connected to a common ground rail that includes capacitors C3 (10uF) and C4 (1uF).

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/EN	IN	VUB_SD_PWR	PWR
0	0	0	0
0	0	1	1
0	1	X	1
1	X	X	0



Soundsystem inkl. Speicher

TITLE: Board_2

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