DEPARTMENT OF MATHEMATICS AND COMPUTING V-M.Tech. (M&C) Monsoon Semester 2022-2023

GPU Computing Lab MCC302

LAB-5
Makefile

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Experiment 1.1: Use of Makefile with Main program, Distance Kernel, and Header Kernel.

Objectives: Use of Makefile

CUDA Sample Program:

distanceMain.cpp

```
#include "DistKernel.h"
#include <stdio.h>

#define TPB 16
   __device__ float distance (float x1, float x2)
{
    return sqrt ((x2 - x1) * (x2 - x1));
}
   __global__ void distanceKernel (float *d_out, float *d_in, float ref)

        const int i = blockIdx.x * blockDim.x + threadIdx.x;
        const float x = d_in[i];
        d_out[i] = distance (x, ref);
        printf ("i = %2d: distance from %f to %f is %f.\n", i, ref, x, d_out[i]);
        return;
}

void distanceArray (float *out, float *in, float ref, int len)
{
        // declare pointers to device arrays
        float *d_in = 0;
}
```

```
Page | 2
    float *d_out = 0;
    // allocate memory for device arrays
    cudaMalloc (&d_in, len * sizeof (float));
    cudaMalloc (&d_out, len * sizeof (float));
    // copy input data from host to device
    cudaMemcpy (d_in, in, len * sizeof (float), cudaMemcpyHostToDevice);
    // launch kernel to compute and store distance values
    distanceKernel <<<len / TPB, TPB>>> (d_out, d_in, ref);
    cudaDeviceSynchronize ();
    cudaMemcpy (out, d_out, len * sizeof (float), cudaMemcpyDeviceToHost);
    // free the memory allocated for device arrays
    cudaFree (d_in);
    cudaFree (d_out);
}
```

DistKernel.cu

```
#ifndef KERNEL_H
#define KERNEL_H
void distanceArray (float *out, float *in, float ref, int len);
#endif
```

DistKernel.h

Makefile

Output:

```
0.500000 to
0.500000 to
0.500000 to
0.500000 to
0.500000 to
0.500000 to
                                                                                                                                                                                                 0.500000.
0.433333.
0.366667.
0.300000.
0.233333.
0.166667.
                                                                                                                                        0.000000
0.066667
0.133333
                           stance
                                                           0.133333
0.200000
0.266667
0.333333
0.400000
0.466667
0.533333
0.600000
0.666667
0.733333
0.800000
                                                                                                                                                                                                  0.100000.
                                                                                0.500000 to 0.400000

0.500000 to 0.466667

0.500000 to 0.533333

0.500000 to 0.600000

0.500000 to 0.733333

0.500000 to 0.800000

0.500000 to 0.866667

0.500000 to 0.933333

0.500000 to 0.933333
                                                                                                                                                                                                 0.100000.

0.0333333.

0.100000.

0.166667.

0.233333.

0.300000.

0.366667.

0.433333.
                distance
distance
distance
distance
distance
11:
12:
13:
                                                                                 0.500000
                                                                                                                                                                                                  0.500000.
                                                                                                                                           1.000000
```

Lab Exercise 1.1: Write a CUDA program to demonstrate the followings:

- 1) Write a header file for declaring functions (device and global).
- 2) Write a header file to transpose of Matrix A in GPU.
- 3) Then find the product of A and A^T using global functions.
- 4) Transfer result from device to host.
- 5) Print the result.

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CODE:

```
#include <stdio.h>
#include "Matrix.cuh"
int main ()
{
    srand (time (NULL));
        Matrix M1 (3, 5), M2 (3, 5);
        M1.init (), M2.init ();
        Matrix Sum = M1 + M2;
        printf ("Matrix M1:\n");
        M1.display ();
        printf ("Matrix M2:\n");
        M2.display ();
        printf ("Matrix Sum:\n");
        Sum.display ();
        cudaDeviceSynchronize ();
        return 0;
}
```

Main.cu

```
include <stdio.h>
include <cuda_runtime.h>
include "Matrix.cuh"
/ macros:
define precisionField 0
define SHOW_FUNCTION_CALLS 1
Matrix :: Matrix () : rows (0), cols (0), device_pointer (NULL), host_pointer
(NULL)
       return;
Matrix :: Matrix (int r, int c) : Matrix ()
       rows = r;
cols = c;
alloc ();
Matrix :: Matrix (const Matrix &M)
       #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix (const Matrix &M)\033[m\n");
      #endit
rows = M.rows;
cols = M.cols;
cudaMalloc (&device_pointer, rows * cols * sizeof (double));
cudaMemcpy (device_pointer, M.device_pointer, rows * cols * sizeof (double),
cudaMemcpyDeviceToDevice);
AMEMCPYDeviceToDevice);
** cols * sizeof (double));
** cols * sizeof (double));
Matrix :: Matrix (Matrix &&M)
       #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix (Matrix &&M)\033[m\n");
        rows = M.rows;
cols = M.cols;
```

```
Page | 5
      device_pointer = M.device_pointer;
host_pointer = M.host_pointer;
M.rows = M.cols = 0;
       M.device_pointer = M.host_pointer = NULL;
   trix Matrix :: operator = (Matrix &M)
       #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix operator = (Matrix &M)\033[m\n");
       clear ();
      clear ();
rows = M.rows;
cols = M.cols;
cudaMalloc (&device_pointer, rows * cols * sizeof (double));
cudaMemcpy (device_pointer, M.device_pointer, rows * cols * sizeof (double),
cudaMemcpyDeviceToDevice);
MemcpyDeviceToDevice);
** cols * sizeof (double));
** cols * sizeof (double));
      MemcpyDeviceToDevice);
host_pointer = (double *) (malloc (rows * cols * sizeof (double)));
memcpy (host_pointer, M.host_pointer, rows * cols * sizeof (double));
return *this;
  atrix Matrix :: operator = (Matrix &&M)
       #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix operator = (Matrix &&M)\033[m\n");
      #endit
rows = M.rows;
cols = M.cols;
device_pointer = M.device_pointer;
host_pointer = M.host_pointer;
M.rows = M.cols = 0;
M.device_pointer = M.host_pointer = NULL;
return *this;
Matrix :: ~Matrix ()
       #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90m~Matrix () : %p, %p\033[m\n", device_pointer, host_pointer);
       if (NULL != device_pointer)
              cudaFree (device_pointer);
           (NULL != host_pointer)
              free (host_pointer);
       rows = cols = 0;
device_pointer = host_pointer = NULL;
void Matrix :: alloc ()
       cudaMalloc (&device_pointer, rows * cols * sizeof (double));
host_pointer = (double *) (malloc (rows * cols * sizeof (double)));
// printf ("hello");
void Matrix :: clear ()
           printf ("%p, %p\n", device_pointer, host_pointer);
(NULL != device_pointer)
              cudaFree (device_pointer);
            (NULL != host_pointer)
              free (host_pointer);
           vs = cols = 0;
```

```
Page | 6
         device_pointer = host_pointer = NULL;
return;
 void Matrix :: display ()
         if (NULL == host_pointer)
#if warnings == 1
    printf ("\nIn function \'\e[33mprint_matrix_yu\e[m\':\n\e[35mwarning:\e[m
\'m\' is _(null)\n");
        #define BUFFER_SIZE 128
// double (*mat)[cols] = (double (*)[cols]) (host_pointer);
int *max_width_arr = (int *) (malloc (cols * sizeof (int)));
char **mat_of_strs = (char **) malloc (rows * cols * sizeof (char *));
// char *(*matrix_of_strings)[c] = mat_of_strs;
         char *str;
int width;
for (size_t i = 0; i < cols; i++)
                  max_width_arr[i] = 1;
for (size_t j = 0; j < rows; j++)</pre>
str = (char *) malloc (BUFFER_SIZE * sizeof (char));
    width = snprintf (str, BUFFER_SIZE, "%.*lf", precisionField,
host_pointer[j * cols + i]);
    str = (char *) realloc (str, ((size_t) (width + 1)) * sizeof (char));
    mat_of_strs[j * cols + i] = str;
    if (max_width_arr[i] < width)
        max_width_arr[i] = width;</pre>
            or (size_t i = 0; i < rows; i++)
                  printf ("\033[1;32m\xb3\033[m");
for (size_t j = 0; j < cols; j++)</pre>
                           width = strlen (mat_of_strs[i * cols + j]);
for (int x = 0; x < max_width_arr[j] - width; x++)
    printf ("");
printf ("%s", mat_of_strs[i * cols + j]);
if (j != (cols - 1))
    printf ("");</pre>
                  printf_("\033[1;32m\xb3\033[m");
                 // newline:
printf ("\n");
         for (size_t i = 0; i < rows; i++)
    for (size_t j = 0; j < cols; j++)
        free (mat_of_strs[i * cols + j]);
free (mat_of_strs);
free (max_width_arr);</pre>
void Matrix :: init ()
        ::init (host_pointer, rows, cols);
// cudaDeviceSynchronize ();
// printf ("\033[31mhere\033[m");
H2D ();
// printf ("here");
return;
 void Matrix :: H2D ()
         cudaMemcpy (device_pointer, host_pointer, cols * rows * sizeof (double),
MemcpyHostToDevice);
```

```
Page | 7
void Matrix :: D2H ()
      cudaMemcpy (host_pointer, device_pointer, cols * rows * sizeof (double),
 cudaMemcpyDeviceToHost);
    return;
  atrix Matrix :: operator + (const Matrix &M)
       if (rows != M.rows && cols != M.cols)
              printf ("Matrix1 (%dx%d); Matrix2 (%dx%d)\n", rows, cols, M.rows, M.cols);
                 turn Matrix ();
Matrix p (rows, M.cols);
dim3 block (1, 1, 1);
dim3 grid (rows, M.cols, 1);
add_GPU <<< block, grid>>> (
p.device_pointer, rows, cols);
cudaDeviceSynchronize ();
p.D2H ();
// p.display ();
return p:
                                                 atrix Matrix :: operator - (const Matrix &M)
           (rows != M.rows && cols != M.cols)
             printf ("Matrix1 (%dX%d); Matrix2 (%dX%d)\n", rows, cols, M.rows, M.cols);
return Matrix ();
Matrix p (rows, M.cols);
  dim3 block (1, 1, 1);
  dim3 grid (rows, M.cols, 1);
  sub_GPU <<< block, grid>>> (device_pointer, M.device_pointer,
  p.device_pointer, rows, cols);
  cudaDeviceSynchronize ();
  p.D2H ();
  // p.display ();
  return p;
    trix Matrix :: operator * (const Matrix &M)
        if (cols != M.rows)
             printf ("Matrix1 (%dx%d); Matrix2 (%dx%d)\n", rows, cols, M.rows, M.cols);
return Matrix ();
Matrix p (rows, M.cols);
  dim3 block (1, 1, 1);
  dim3 grid (rows, M.cols, 1);
  mul_GPU <<< block, grid>>> (device_pointer, M.device_pointer,
p.device_pointer, rows, cols, M.cols);
  cudaDeviceSynchronize ();
  p.D2H ();
  // p.displaye ();
       // p.display ();
   utrix Matrix :: operator ~ ()
      Matrix t (cols, rows);
dim3 block (1, 1, 1);
dim3 grid (rows, cols, 1);
trp_GPU <<<grid, block>>> (device_pointer, t.device_pointer, rows, cols);
cudaDeviceSynchronize ();
       t.D2H ();
         eturn t;
```

```
Page | 8
  _global__ void init_GPU (double *p, int rows, int cols)
      int r = threadIdx.x + blockIdx.x * blockDim.x; // x = rows
int c = threadIdx.y + blockIdx.y * blockDim.y; // y = cols
// printf ("%d;%d;%d;%d\n", r, c, M.rows, M.cols);
if (r < rows && c < cols)</pre>
            // printf ("<%d>", r * M.cols + c);
p[r * cols + c] = ((double) (r * cols + c));
// printf ("%lf ", M.device_pointer[r * M.cols + c]);
      return;
 roid init (double *p, int rows, int cols)
       for (int i = 0; i < rows * cols; i++)
            p[i] = rand () % 21 - 10;
  _device__ double add_GPU_dev (double m1, double m2)
      return m1 + m2;
  _global___ void add_GPU (double *m1, double *m2, double *a, int rows, int cols)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
if (Row < rows && Col < cols)</pre>
            a[Row * cols + Col] = add_GPU_dev (m1[Row * cols + Col], m2[Row * cols +
Col]);
       return;
  _global___ void sub_GPU (double *m1, double *m2, double *a, int rows, int cols)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
           (Row < rows && Col < cols)
            a[Row * cols + Col] = m1[Row * cols + Col] - m2[Row * cols + Col];
  _global___ void mul_GPU (double *m1, double *m2, double *p, int rows, int x, int
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
if (Row < rows && Col < cols)</pre>
            // printf ("{%d,%d}", Row, Col);
double a = 0;
for (int k = 0; k < x; k++)</pre>
                  // printf ("(%.0f,%.0f)", m1[Row * cols + k], m2[k * rows + Col]); a += m1[Row * x + k] * m2[k * cols + Col];
            p[Row * cols + Col] = a;
// printf ("=<%f>\n", a);
  _global___ void trp_GPU (double *m1, double *m2, int rows, int cols)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
```

Matrix.cu

```
__global___ void init_GPU (double *p, int rows, int cols);
__global__ void mul_GPU (double *m1, double *m2, double *p, int rows, int x, int
cols);
__global__ void trp_GPU (double *m1, double *m2, int rows, int cols);
__global__ void add_GPU (double *m1, double *m2, double *a, int rows, int cols);
__global__ void sub_GPU (double *m1, double *m2, double *a, int rows, int cols);
void init (double *p, int rows, int cols);
void init (double *p, int rows, int cols);
struct Matrix
{

    int rows, cols;
    double *device_pointer, *host_pointer;
    int flag = 0;
    Matrix (int r, int c);
    Matrix (const Matrix &M);
    Matrix (matrix &M);
    Matrix operator = (Matrix &M);
    Matrix operator = (Matrix &M);
    void alloc ();
    void clear ();
    void display ();
    void disp
```

Matrix.cuh

```
CC = nvcc
FLAGS = -dc -c

# Targets = Main.cu Matrix.cu
ALL: Lib\Main.obj Lib\Matrix.obj
    $(CC) Lib\Main.obj Lib\Matrix.obj -o Main
    .\Main.exe

Lib\Main.obj: Main.cu
    $(CC) $(FLAGS) Main.cu -o "Lib/Main"
Lib\Matrix.obj: Matrix.cu
    $(CC) $(FLAGS) Matrix.cu -o "Lib/Matrix"
CLEAN:
    del "Lib\*.obj"
    del "Main.exe"
    del "Main.exe"
    del "Main.exe"
    del "Main.lib"
    del "Main.exp"
```

Makefile

Output:

Lab Exercise 1.2: Write a CUDA program to demonstrate:

- 1. Write a header file for declaring functions.
- 2. Write device functions to transpose of Matrix A in GPU.
- 3. Then find the product of A and A^T using global functions.
- 4. Transfer results from device to host.
- 5. Print the result.

CODE:

```
#include <stdio.h>
#include "matrix.cuh"
int main ()
{
    srand (time (NULL));
    Matrix A (4, 3);
    A.init ();
    Matrix AT = ~A;
    printf ("Matrix A:\n");
    A.display ();
    printf ("Matrix AT:\n");
    AT.display ();
    Matrix P = A * AT;
    printf ("Matrix P:\n");
    P.display ();
    cudaDeviceReset ();
    return 0;
}
```

Main.cu

```
nclude <stdio.h>
nclude <cuda_runtime.h>
nclude "matrix.cuh"
 / macros:
define precisionField 0
define SHOW_FUNCTION_CALLS 1
 atrix :: Matrix () : rows (0), cols (0), device_pointer (NULL), host_pointer
(NULL)
 <u>latrix</u> :: Matrix (int <u>r</u>, int <u>c</u>) : <u>Matrix</u> ()
      cols = c;
alloc ();
  atrix :: Matrix (const Matrix &<u>M</u>)
      #if SHOW_FUNCTION_CALLS == 1
      printf ("\033[90mMatrix (const Matrix &M)\033[m\n");
      rows = M.rows;
cols = M.cols;
cudaMalloc (&device_pointer, rows * cols * sizeof (double));
cudaMemcpy (device_pointer, M.device_pointer, rows * cols * sizeof (double),
cudaMemcpyDeviceToDevice);
      host_pointer = (double *) (malloc (rows * cols * sizeof (double)));
memcpy (host_pointer, M.host_pointer, rows * cols * sizeof (double));
  atrix :: Matrix (Matrix &&M)
      #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix (Matrix &&M)\033[m\n");
      device_pointer = M.device_pointer;
host_pointer = M.host_pointer;
```

```
Page | 13
      M.rows = M.cols = 0;
M.device_pointer = M.host_pointer = NULL;
  \frac{\text{atrix}}{\text{Matrix}} :: \text{operator} = (\frac{\text{Matrix}}{\text{Matrix}})
      #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix operator = (Matrix &M)\033[m\n");
      clear ();
     rows = M.rows;
cols = M.cols;
cudaMalloc (&device_pointer, rows * cols * sizeof (double));
cudaMemcpy (device_pointer, M.device_pointer, rows * cols * sizeof (double),
cudaMemcpyDeviceToDevice);
  host_pointer = (double *) (malloc (rows * cols * sizeof (double)));
  memcpy (host_pointer, M.host_pointer, rows * cols * sizeof (double));
  return *this;
  \frac{\text{atrix}}{\text{matrix}} :: \text{operator} = (\frac{\text{Matrix}}{\text{Matrix}})
      #if SHOW_FUNCTION_CALLS == 1
printf ("\033[90mMatrix opera
                   \033[90mMatrix operator = (Matrix &&M)\033[m\n");
      host_pointer = \underline{M}.host_pointer;
      M.device_pointer = M.host_pointer = NULL;
      return *this:
  atrix :: ~Matrix ()
         f SHOW_FUNCTION_CALLS == 1
      printf ("\033[90m~Matrix (): %p, %p\033[m\n", device_pointer, host_pointer);
       if (NULL != device_pointer)
           cudaFree (device_pointer);
          (NULL != host_pointer)
            free (host_pointer);
      rows = cols = 0;
      device_pointer = host_pointer = NULL;
void <u>Matrix</u> :: alloc ()
     cudaMalloc (&device_pointer, rows * cols * sizeof (double));
host_pointer = (double *) (malloc (rows * cols * sizeof (double)));
// printf ("hello");
void <u>Matrix</u> :: clear ()
         printf ("%p, %p\n", device_pointer, host_pointer);
(NULL != device_pointer)
            cudaFree (device_pointer);
          (NULL != host_pointer)
            free (host_pointer);
      rows = cols = 0;
      device_pointer = host_pointer = NULL;
```

```
Page | 14
 void <u>Matrix</u> :: display ()
             (NULL == host_pointer)
#if WARNINGS == 1
    printf ("\nIn function \'\e[33mprint_matrix_yu\e[m\':\n\e[35mwarning:\e[m
\'m\' is (null)\n");
        #define BUFFER_SIZE 128
// double (*mat)[cols] = (double (*)[cols]) (host_pointer);
int *max_width_arr = (int *) (malloc (cols * sizeof (int)));
char **mat_of_strs = (char **) malloc (rows * cols * sizeof (char *));
// char *(*matrix_of_strings)[c] = mat_of_strs;
        int width;

for (<u>size_t</u> i = 0; i < cols; i++)
                max_width_arr[i] = 1;
for (size_t j = 0; j < rows; j++)</pre>
str = (char *) malloc (BUFFER_SIZE * sizeof (char));
    width = snprintf (str, BUFFER_SIZE, "%.*lf", precisionField,
host_pointer[j * cols + i]);
    str = (char *) realloc (str, ((size_t) (width + 1)) * sizeof (char));
    mat_of_strs[j * cols + i] = str;
    if (max_width_arr[i] < width)
        max_width_arr[i] = width;</pre>
             r (<u>size_t</u> i = 0; i < rows; i++)
                printf ("\033[1;32m\xb3\033[m");
for (size_t j = 0; j < cols; j++)</pre>
                        printf (" ");
printf ("%s", mat_of_strs[i * cols + j]);
if (j != (cols - 1))
    printf (" ");
                printf ("\033[1;32m\xb3\033[m");
                // newline:
printf ("\n");
        for (size_t i = 0; i < rows; i++)
    for (size_t j = 0; j < cols; j++)
        free (mat_of_strs[i * cols + j]);
free (max_width_arr);
return;</pre>
void Matrix :: init ()
         // cudaDeviceSynchronize ();
// printf ("\033[31mhere\033[m");
        H2D ();
// printf ("here");
void Matrix :: H2D ()
cudaMemcpy (device_pointer, host_pointer, cols * rows * sizeof (double),
cudaMemcpyHostToDevice);
```

```
Page | 15
void <u>Matrix</u> :: D2H ()
     cudaMemcpy (host_pointer, device_pointer, cols * rows * sizeof (double),
cudaMemcpyDeviceToHost);
 <u>atrix Matrix</u> :: operator + (const <u>Matrix</u> &<u>M</u>)
         (rows != M.rows && cols != M.cols)
           printf ("Matrix1 (%dX%d); Matrix2 (%dX%d)\n", rows, cols, M.rows, M.cols);
Matrix p (rows, M.cols);
    dim3 block (1, 1, 1);
    dim3 grid (rows, M.cols, 1);
    add_GPU <<< block, grid>>> (device_pointer, M.device_pointer,
p.device_pointer, rows, cols);
    cudaDeviceSynchronize ();
     p.D2H ();
// p.display ();
 (rows != \underline{M}.rows \&\& cols <math>!= \underline{M}.cols)
           printf ("Matrix1 (%dX%d); Matrix2 (%dX%d)\n", rows, cols, M.rows, M.cols);
     p.device_pointer, rows, cols);
    cudaDeviceSynchronize ();
     p.D2H ();
// p.display ();
 <u>atrix Matrix</u> :: operator * (const <u>Matrix</u> &<u>M</u>)
         (cols != M.rows)
           printf ("Matrix1 (%dx%d); Matrix2 (%dx%d)\n", rows, cols, M.rows, M.cols);
     Matrix p (rows, M.cols);
dim3 block (1, 1, 1);
     dim3 grid (rows, M.cols, 1);
mul_GPU <<< block, grid>>> (device_pointer, M.device_pointer,
p.device_pointer, rows, cols, M.cols);
    cudaDeviceSynchronize ();
     p.D2H ();
// p.display ();
 <u>latrix Matrix</u> :: operator ~ ()
     Matrix t (cols, rows);
dim3 block (1, 1, 1);
dim3 grid (rows, cols, 1);
trp_GPU <<<grid, block>>> (device_pointer, t.device_pointer, rows, cols);
cudaDeviceSynchronize ();
     t.D2H ();
  _global__ void init_GPU (double *p, int <u>rows</u>, int <u>cols</u>)
```

```
Page | 16
      int r = threadIdx.x + blockIdx.x * blockDim.x; //
int c = threadIdx.y + blockIdx.y * blockDim.y; //
// printf ("%d;%d;%d;%d\n", r, c, M.rows, M.cols)
                                                                                    y = cols
           (r < \frac{rows}{c} & c < \frac{cols}{c})
            // printf ("<%d>", r * M.cols + c);
p[r * cols + c] = ((double) (r * cols + c));
// printf ("%lf ", M.device_pointer[r * M.cols + c]);
        eturn;
void init (double *p, int rows, int cols)
       or (int i = 0; i < <u>rows</u> * <u>cols</u>; i++)
            p[i] = rand () \% 21 - 10;
   _device__ double add_GPU_dev (double <u>m1</u>, double <u>m2</u>)
  _global___ void add_GPU (double *<u>m1</u>, double *<u>m2</u>, double *<u>a</u>, int <u>rows</u>, int <u>cols</u>)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
          (Row < rows && Col < cols)
            \underline{a}[Row * \underline{cols} + Col] = add_GPU_dev (\underline{m1}[Row * \underline{cols} + Col], \underline{m2}[Row * \underline{cols} +
Col]);
  _global___ void sub_GPU (double *m1, double *m2, double *a, int rows, int cols)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
if (Row < rows && Col < cols)</pre>
            \underline{a}[Row * \underline{cols} + Col] = \underline{m1}[Row * \underline{cols} + Col] - \underline{m2}[Row * \underline{cols} + Col];
  _global__ void mul_GPU (double *m1, double *m2, double *p, int rows, int x, int
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
int Col = blockIdx.y * blockDim.y + threadIdx.y;
          (Row < <u>rows</u> && Col < cols)
            // printf ("{%d,%d}", Row, Col);
double a = 0;
for (int k = 0; k < x; k++)</pre>
                      printf ("(%.0f,%.0f)", m1[Row * cols + k], m2[k * rows + Col]);
                  a += m1[Row * x + k] * m2[k * cols + Col];
            p[Row * cols + Col] = a;
// printf ("=<%f>\n", a);
      return;
   _device__ double trp_GPU_dev (double *m, int cols, int Row, int Col)
      return m[Row * cols + Col];
   _global__ void trp_GPU (double *m1, double *m2, int rows, int cols)
      int Row = blockIdx.x * blockDim.x + threadIdx.x;
```

```
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   int Col = blockIdx.y * blockDim.y + threadIdx.y;
   if (Row < rows && Col < cols)
   {
       m2[Col * rows + Row] = trp_GPU_dev (m1, cols, Row, Col);
   }
   return;
}</pre>
```

Matrix.cu

```
__global__ void init_GPU (double *p, int rows, int cols);
__global__ void mul_GPU (double *m1, double *m2, double *p, int rows, int x, int
cols);
__global__ void trp_GPU (double *m1, double *m2, int rows, int cols);
__global__ void add_GPU (double *m1, double *m2, double *a, int rows, int cols);
__global__ void sub_GPU (double *m1, double *m2, double *a, int rows, int cols);
void init (double *p, int rows, int cols);
struct Matrix
{
    int rows, cols;
    double *device_pointer, *host_pointer;
    int flag = 0;
    Matrix (int r, int c);
    Matrix (int r, int c);
    Matrix (matrix &dm);
    Matrix (matrix &dm);
    Matrix operator = (Matrix &dm);
    Matrix operator = (Matrix &dm);
    void alloc ();
    void clear ();
    void display ();
    void display ();
    void dipl ();
    void dipl ();
    void dipl ();
    Matrix operator + (const Matrix &dm);
    Matrix operator * (const Matrix &
```

Matrix.cuh

```
CC = nvcc
FLAGS = -dc -c

# Targets = Main.cu Matrix.cu
ALL: Lib\Main.obj Lib\Matrix.obj
    $(CC) Lib\Main.obj Lib\Matrix.obj -o Main
    .\Main.exe

Lib\Main.obj: Main.cu
    $(CC) $(FLAGS) Main.cu -o "Lib/Main"
Lib\Matrix.obj: Matrix.cu
    $(CC) $(FLAGS) Matrix.cu -o "Lib/Matrix"

CLEAN:
    del "Lib\*.obj"
    del "Main.exe"
    del "Main.exe"
    del "Main.exp"
```

Makefile

Outputs: