

Final Assessment Test - April 2018

Course: CSE2006 - Microprocessor and Interfacing

Class NBR(s):3487 / 3488 / 3489/ 3490 / 3491 / 3492 /

3493 / 4332 / 4172

Slot: B2

Time: Three Hours

Max. Marks: 100

PART - A (8 X 5 = 40 Marks) Answer ALL Questions

Justify how 8086 architecture supports following features:

Faster Execution

(b) Optimal pin usage

F Pipelining

increased Memory addressing capability

Identify the output of the following code and rewrite the code without using SAR/SAL instruction which will produce the same output?

MOV AL, OCOH

MOV CL. 01H

SAL AL, CL

b) MOVAL OCOH

MOV CL, 02H

SAR AL, CL

Determine the value of AL and the value of the status flags after executing the following instruction sequence.

MOV AL, 7Fh

NEG AL

If AL=09h and BL=08H, what will be the content of AX, after the execution of instruction sequence.

MUL BL

AAM

Explain the need for an assembler directive in ALP. List out various assembler directives with an example.

List and explain in brief various instructions in 8086 for handling interrupts.

Calculate the Vector Address for Interrupt Type Number-50.

Discuss the issues in using 8255 for software polling of IO devices. Explain how 8259 overcomes the

Briefly explain about digital data transmission using modern with necessary diagram.

List out the data types of 8087 with an example.

PART - B (6 X 10 = 60 Marks) Answer any SIX Questions

Discuss how 8086 operates in maximum mode with necessary functional diagram and timing diagram.

- 10. Write a program to find the number of positive and negative numbers from a given series of signed numbers using 8086 instruction set.
- 11. Suggest the optimal programming construct to be used for the following functionalities:
 - a) To read 100 char from an input device
 - To calculate the factorial of a given number

Justify the programming construct with respect to Execution Time, Memory Usage and Passing Parameters.

16. Explain your CAL 'J' project in the following aspects:

Objectives with social impact

b) List of optimal number of components used

O) Use of any on-chip peripherals in MCU

Block and Interface diagram

e) Software logic by flow chart

Cost – Power – Time – Space effectiveness