Lab report 2: Design of Bulk MOSFET
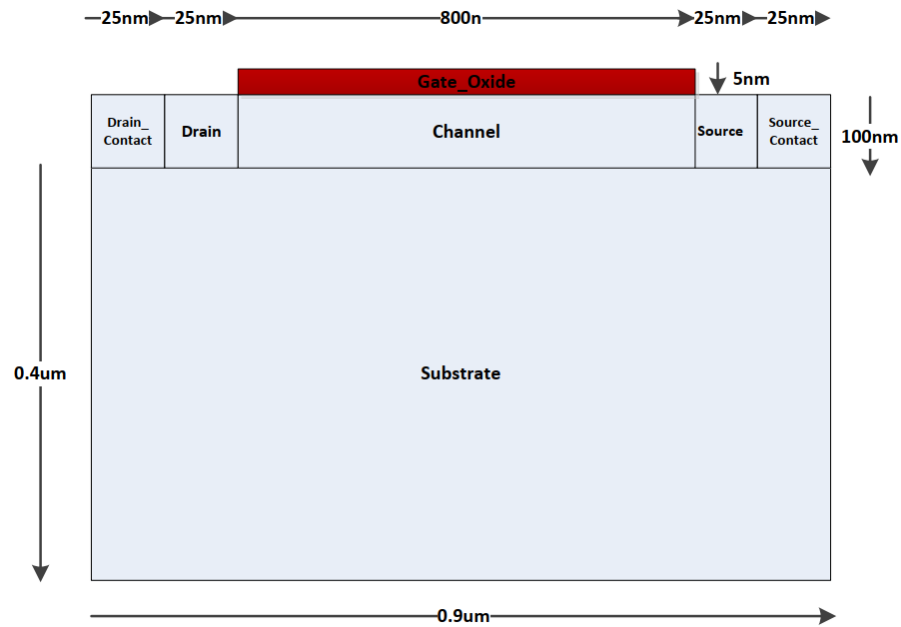
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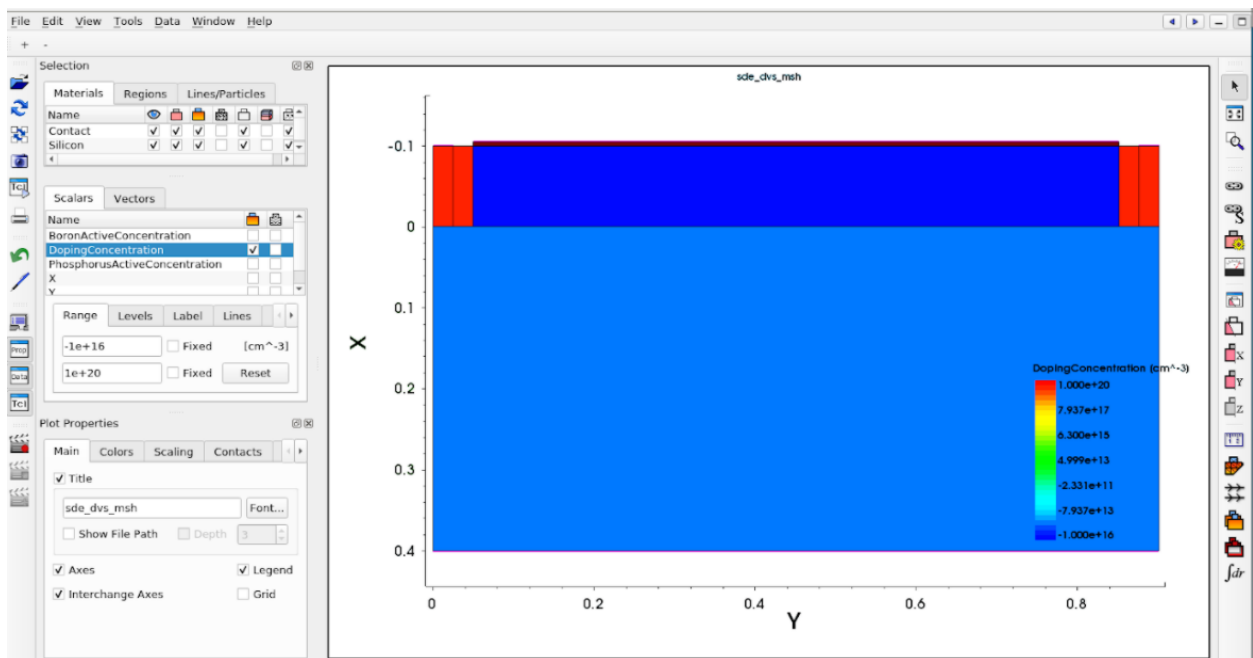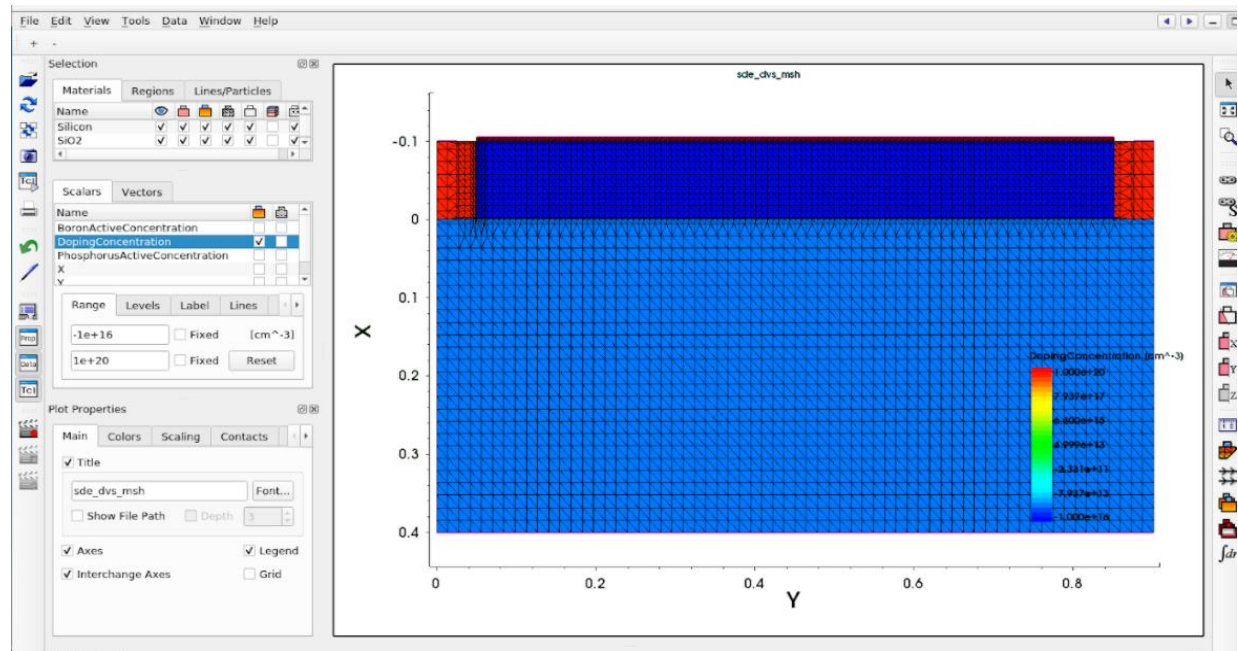## Table of Contents

I. PROBLEM

This bulk MOSFET is to be simulated on Sentaurus workbench along with sweeping on both Vgs and Vds.
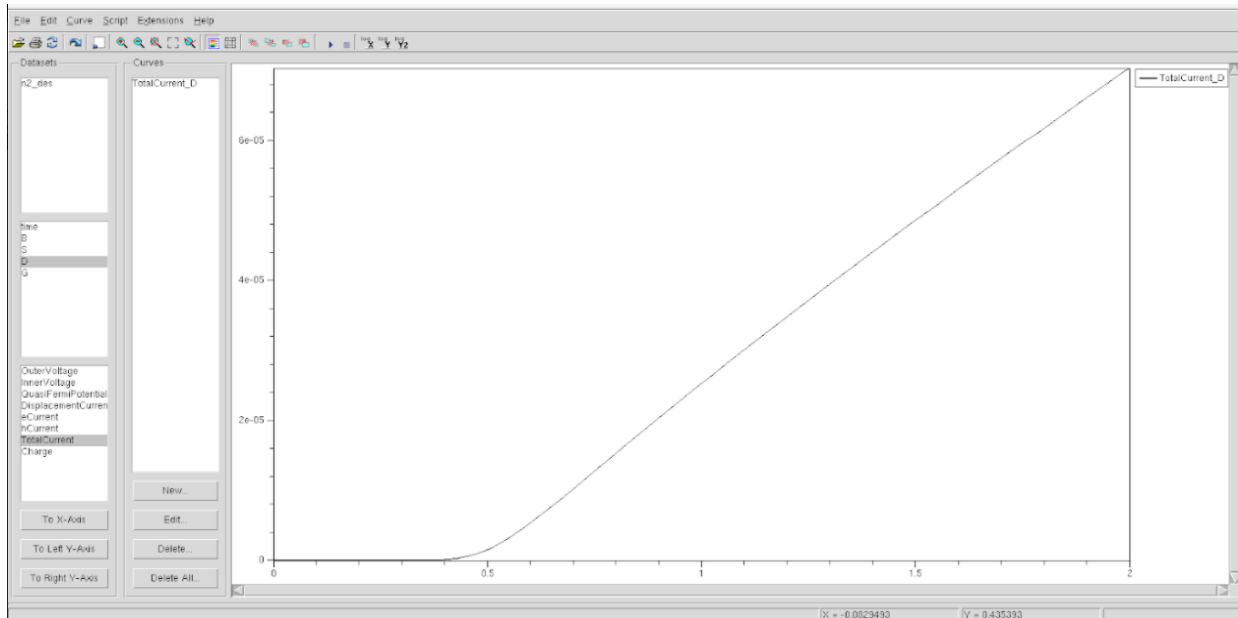
II. STRUCTURE & DOPING

III. MESHING

IV. SDEVICE SIMULATION

A. *First sweep*

B. Second Sweep