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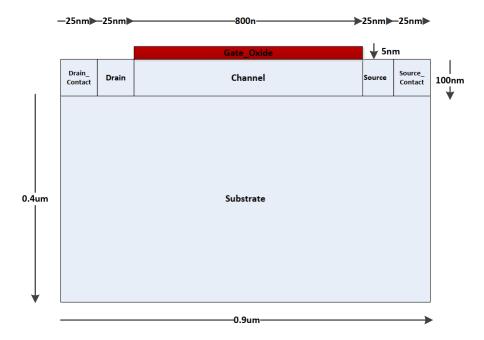
Lab report 2: Design of Bulk MOSFET

Rami Wail Shoula, ID: 201600112 Supervised by: Professor Amr Byoumi, Eng Reem Mostafa, Eng Nehad Mansour

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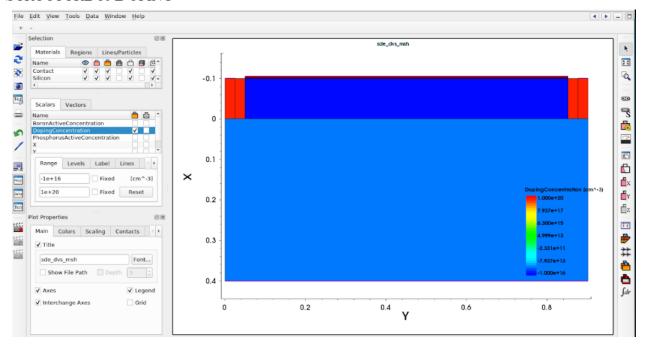
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I. PROBLEM



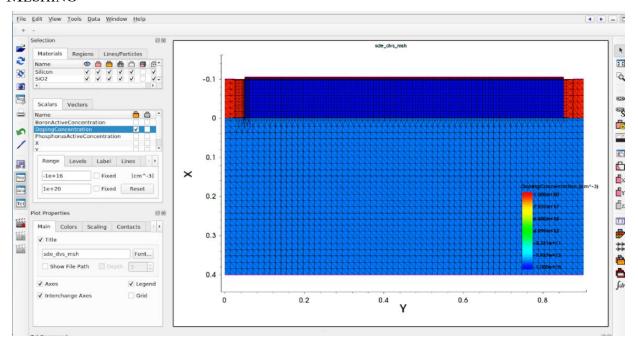
This bulk MOSFET is to be simulated on Sentaurus workbench along with sweeping on both Vgs and Vds.

II. STRUCTURE & DOPING



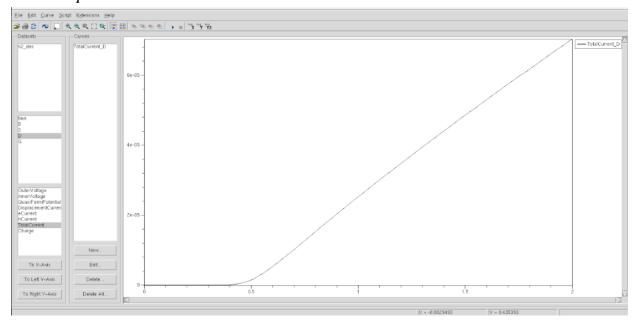
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III. MESHING



IV. SDEVICE SIMULATION

A. First sweep



B. Second Sweep

