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Simulation Assignment 1

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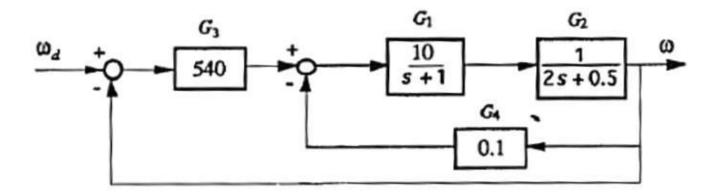
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I. Introduction & Objective

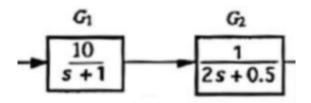
The objective is to obtain the transfer function of the following block diagram using MATLAB simulation software: -



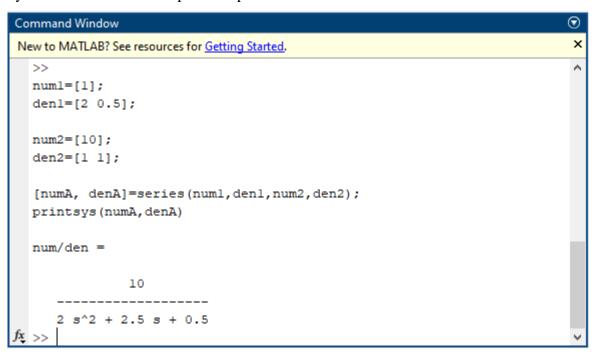
II. METHOD

As shown in the block diagram, the transfer from wd to w is achieved over three phases. To simplify the simulation process, each phase is encoded separately, starting from the output (right) side. Accordingly, phase A consists of blocks G1 & G2; while phase B consists of blocks G1, G2 & G4; concluding with phase C including all blocks. (Each phase was saved as a script but was run in command window).

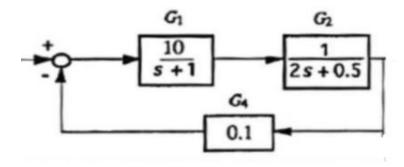
A. Phase A



Our process is from the output (right) end, so normal series system starts with block G2 as unit 1, followed by G1 which is unit 2. Output from phase A is shown below:



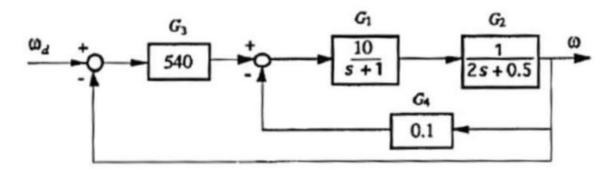
B. Phase B



Phase B consists of phase A in addition to block G4 which is unit 3. Output from phase B: -

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C. Phase C



Phase C simply consists of previous two phases in addition to series G3 block or unit 4. Output of phase:

Employing the closed loop in this phase:

```
>> [numD, denD] = cloop (numC, denC, -1);
printsys (numD, denD)

num/den = 

5400

2 s^2 + 2.5 s + 5401.5
```