



Sardar Patel Institute of Technology

Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058, India

(Autonomous College Affiliated to University of Mumbai)

End Semester Examination

April/May 2018

Max. Marks: 100

Class: S.E.

Course Code: CE43/IT44

Name of the Course: Operating Systems

Duration: 3 Hours

Semester: IV

Branch: Computer/IT

Instruction:

- (1) All questions are compulsory
- (2) Draw neat diagrams
- (3) Assume suitable data if necessary

Q No.		Max. Marks	CO																												
Q.1 (a)	Explain multiprogramming operating system with neat diagram.	05	CO1																												
Q.1 (b)	Write any four advantages and one disadvantage of layered architecture of operating system.	05	CO1																												
Q.2 (a)	<p>For the process parameters in the table below, find average waiting time and average turnaround time for FCFS and preemptive SJF scheduling algorithms.</p> <table><tr><th>Process</th><th>Arrival Time</th><th>Burst Time</th></tr><tr><td>P1</td><td>0</td><td>5</td></tr><tr><td>P2</td><td>1</td><td>3</td></tr><tr><td>P3</td><td>2</td><td>8</td></tr></table> <p>OR</p> <p>Assume the following processes arrive for execution at the time indicated and the length of CPU burst time given in ms.</p> <table><tr><th>Job</th><th>Burst Time</th><th>Priority</th><th>Arrival Time</th></tr><tr><td>P1</td><td>19</td><td>3</td><td>0</td></tr><tr><td>P2</td><td>10</td><td>2</td><td>2</td></tr><tr><td>P3</td><td>7</td><td>1</td><td>4</td></tr></table> <p>For the process parameters in the table above, find average waiting time and average turnaround time for non-preemptive and preemptive priority scheduling algorithms for time slice 5. Lower number indicates higher priority.</p>	Process	Arrival Time	Burst Time	P1	0	5	P2	1	3	P3	2	8	Job	Burst Time	Priority	Arrival Time	P1	19	3	0	P2	10	2	2	P3	7	1	4	10	CO2
Process	Arrival Time	Burst Time																													
P1	0	5																													
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Job	Burst Time	Priority	Arrival Time																												
P1	19	3	0																												
P2	10	2	2																												
P3	7	1	4																												
Q.2 (b)	Differentiate between user level thread and kernel level thread for 5 different points.	05	CO2																												
Q.2 (c)	Explain multilevel feedback queue scheduling with neat diagram.	05	CO2																												

Q.3 (a)	<p>Consider the following state of a system and answer the following questions:</p> <table border="1"> <thead> <tr> <th>Process</th><th>Max</th><th>Allocation</th><th>Available</th></tr> <tr> <th></th><th>A B C D</th><th>A B C D</th><th>A B C D</th></tr> </thead> <tbody> <tr> <td>P0</td><td>6 0 1 2</td><td>4 0 0 1</td><td>3 2 1 1</td></tr> <tr> <td>P1</td><td>2 7 5 0</td><td>1 1 0 0</td><td></td></tr> <tr> <td>P2</td><td>2 3 5 6</td><td>1 2 5 4</td><td></td></tr> <tr> <td>P3</td><td>1 6 5 3</td><td>0 6 3 3</td><td></td></tr> <tr> <td>P4</td><td>1 6 5 6</td><td>0 2 1 2</td><td></td></tr> </tbody> </table> <p>Using Banker's algorithm, answer the following questions:-</p> <p>i) How many total instances of the resource types A, B, C, D are there?</p> <p>ii) What are the contents of need matrix?</p> <p>iii) Find if the system is in safe state? If it is, find the safe sequence.</p>	Process	Max	Allocation	Available		A B C D	A B C D	A B C D	P0	6 0 1 2	4 0 0 1	3 2 1 1	P1	2 7 5 0	1 1 0 0		P2	2 3 5 6	1 2 5 4		P3	1 6 5 3	0 6 3 3		P4	1 6 5 6	0 2 1 2		10	CO3
Process	Max	Allocation	Available																												
	A B C D	A B C D	A B C D																												
P0	6 0 1 2	4 0 0 1	3 2 1 1																												
P1	2 7 5 0	1 1 0 0																													
P2	2 3 5 6	1 2 5 4																													
P3	1 6 5 3	0 6 3 3																													
P4	1 6 5 6	0 2 1 2																													
Q.3 (b)	<p>Describe deadlock prevention by breaking circular wait condition.</p> <p style="text-align: center;">OR</p> <p>Explain test and set construct to solve critical section problem with example(pseudo code).</p>	05	CO3																												
Q.3 (d)	<p>State dining philosopher's problem. Solve by writing pseudo code and explain the dining philosopher's problem solution using Monitors.</p> <p style="text-align: center;">OR</p> <p>State sleeping barber problem. Solve it by writing pseudo code using Semaphores and explain the same.</p>	10	CO3																												
Q.4 (a)	<p>A paging system has the following parameters: 2^{31} bytes of physical memory; page size of 2^{10} bytes; 2^{16} pages of logical address space.</p> <ol style="list-style-type: none"> How many bits are in a logical address? How many bytes are in a frame? How many bits are in the physical address specifying the frame number? How many entries in the page table? How many bits in each page table entry? Assume each page table entry contains valid/invalid bit. <p style="text-align: center;">OR</p> <p>If cost of accessing main memory is 100 ns and TLB hit ratio is 90%. What is the cost of accessing the TLB if the effective memory access time is 119ms.</p>	05	CO4																												
Q.4 (b)	<p>Explain memory allocation techniques with neat diagram and example.</p>	10	CO4																												

Q.4 (c)	Given page reference string : 1,2,3,4,2,1,5,6,2,1,2,3,7,6,3,2,1,2,3,6. Calculate total number of page faults using optimal page replacement policy using 4 frames.	5	CO4
Q.5 (a)	Given the following queue: 95, 180, 34, 119, 11, 123, 62, 64 with the read-write head initially at the track 50 and the tail track being at 199. Calculate total head movement and average seek length using FCFS and C-SCAN disk scheduling algorithm when head is moving towards decreasing track number.	10	CO5
Q.5 (b)	Compare sequential and index sequential file organization method with neat diagram. OR What is record blocking? Explain three methods of record blocking with neat diagram.	10	CO6
Q.5 (c)	Explain any five objectives of file management system.	05	CO6