|  |  | Deterministic | Nondeterministic | |
|---|---|---|---|---|
|  |  |  | $n = 5$ | $n = 10$ |
| **Average Transpose (6 configurations) @ 28 SM's** | | | | |
| DRAM reads | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| DRAM writes | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| L1D hit rate | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| L2D hit rate | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| Cycles | $t = 4$ | 0% | 0.4% | 0.5% |
|  | $t = 8$ | 0% | 0.5% | 0.3% |
| Exec time | $t = 4$ | $1.9x$ | $\mathbf{2.4x}$ | $2.4x$ |
|  | $t = 8$ | $2x$ | $2.8x$ | $\mathbf{2.9x}$ |
| **Average Transpose (6 configurations) @ 112 SM's** | | | | |
| DRAM reads | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| DRAM writes | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| L1D hit rate | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| L2D hit rate | $t = 4$ | 0% | 0% | 0% |
|  | $t = 8$ | 0% | 0% | 0% |
| Cycles | $t = 4$ | 0% | 1.6% | 1.6% |
|  | $t = 8$ | 0% | 1.6% | 1.4% |
| Exec time | $t = 4$ | $2.4x$ | $\mathbf{2.9x}$ | $2.9x$ |
|  | $t = 8$ | $3x$ | $3.7x$ | $\mathbf{3.8x}$ |