| | | ACCELSIM | MICROGPUSIM | MICROGPUSIM (memory only) | MICROGPUSIM (trace reconstr.) |
|---|-------|----------|-------------|------------------------------|-------------------------------|
| Naive Matrixmul (29 benchmark configurations) @ 28 SM's | | | | | |
| Norm. DRAM reads | RMSE | 0.70% | 0.00% | 0.00% | 0.00% |
| | MAE | 0.66% | 0.00% | 0.00% | 0.00% |
| | Corr. | | 1.000 | 1.000 | 1.000 |
| Norm. DRAM writes | RMSE | 99.87% | 0.30% | 0.30% | 0.30% |
| | MAE | 99.87% | 0.13% | 0.13% | 0.13% |
| | Corr. | 0.519 | | | |
| L1D hit rate | RMSE | 23.96% | 0.85% | 0.82% | 1.28% |
| | MAE | 23.84% | 0.85% | 0.82% | 0.99% |
| | Corr. | 0.943 | 1.000 | 0.999 | 1.000 |
| L2D hit rate | RMSE | 67.10% | 0.96% | 0.96% | 0.96% |
| | MAE | 63.58% | 0.24% | 0.24% | 0.24% |
| | Corr. | -0.524 | | | |
| Cycles | RMSPE | 29.56% | 25.74% | 27.24% | 18.32% |
| | MAPE | 29.30% | 25.36% | 26.56% | 10.16% |
| | Corr. | 0.997 | 0.993 | 0.990 | 0.969 |