Table 1: Simulator configuration parameters.

| | |
|---:|:---|
| **mode** | serial, deterministic, nondeterministic |
| **memory only** | true, false |
| **clusters** | 28, 112 |
| **cores per cluster** | 1, 4 |
| **threads** (mode $\neq$ serial) | 4, 8 |
| **run ahead** (mode $=$ nondeterministic) | 5, 10 |