	Benchmark	TRANSPOSE	<u> </u>				
	Dimensions	128		256		512	
	Variant	coalesced	naive	coalesced	naive	coalesced	naive
Exec. time $(s)$	Native	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1
	Ours	1.8	2	3.8	7.5	13.4	29.5
	ACCELSIM	2.5	3.2	4.9	7.3	17	26
Cycles	Native	6.1K	7.6K	7.2K	11.9K	12.3K	29.9K
	Ours	6K	6.5K	7.1K	11.1K	12K	27.7K
	ACCELSIM	7.6K	8.5K	10.1K	12.6K	22.3K	29.5K
Instruction count	Native	557.1K	360.4K	2.2M	1.4M	8.9M	5.8M
	Ours	655.4K	458.8K	2.6M	1.8M	10.5M	7.3M
	ACCELSIM	655.4K	458.8K	2.6M	1.8M	10.5M	7.3M
L1 accesses	Native	2K	2K	8.2K	8.2K	32.8K	32.8K
	Ours	2K	2K	8.2K	8.2K	32.8K	32.8K
	ACCELSIM	2K	2K	8.2K	8.2K	32.8K	32.8K
L1 hit rate (%)	Native	0%	0%	0%	0%	0%	0%
	Ours	0%	0%	0%	0%	0%	0%
	ACCELSIM	0%	0%	0%	0%	0%	0%
L2 read hits	Native	2K	2K	8.2K	8.2K	22.3K	22.2K
	Ours	2K	2K	8.2K	8.2K	0	0
	ACCELSIM	0	0	0	0	0	0
L2 read hit rate (%)	Native	100%	100%	100%	100%	67.92%	67.83%
	Ours	100%	100%	100%	100%	0%	0%
	ACCELSIM	0%	0%	0%	0%	0%	0%
L2 write hits	Native	2K	16.4K	8.2K	65.5K	32.7K	262.1K
	Ours	2K	16.4K	8.2K	65.5K	32.8K	262.1K
	ACCELSIM	0	6.1K	0	24.6K	0	98.3K
L2 write hit rate (%)	Native	100%	100%	100%	100%	99.91%	99.99%
	Ours	100%	100%	100%	100%	100%	100%
	ACCELSIM	0%	75%	0%	75%	0%	75%
DRAM reads	Native	6	6	0	0	31.5K	10.6K
	Ours	0	0	0	0	32.8K	32.8K
	ACCELSIM	4.1K	4.1K	16.4K	16.4K	65.5K	65.5K
DRAM writes	Native	0	0	25	25	14.8 K	345.5
	Ours	0	0	0	0	0	0
	ACCELSIM	2K	6.7K	8.2K	22.3K	32.8K	74.4K