



AcceISim



NVIDIA TITAN X (Pascal)



gpucachesim

L1 accesses

1M
10K
100
1

Naive MatrixMul
f32 32x32x32
1 block

Naive MatrixMul
f32 32x64x128
4 blocks

Naive MatrixMul
f32 128x32x32
4 blocks

Naive MatrixMul
f32 128x128x128
16 blocks

Naive MatrixMul
f32 512x32x512
256 blocks

Naive MatrixMul
f32 128x512x128
16 blocks

