

L2 writes

100M
10M
1M
100K
10K
1K
100



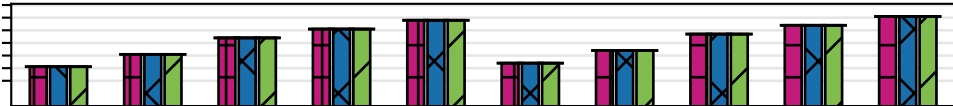
AcceISim



NVIDIA TITAN X (Pascal)



gpucachesim



Scenario	AcceISim (L2 writes)	NVIDIA TITAN X (Pascal) (L2 writes)	gpucachesim (L2 writes)
VectorAdd f32 100 1 block	~100K	~100K	~100K
VectorAdd f32 1K 1 block	~100K	~100K	~100K
VectorAdd f32 20K 20 blocks	~100K	~100K	~100K
VectorAdd f32 100K 98 blocks	~100K	~100K	~100K
VectorAdd f32 500K 489 blocks	~100K	~100K	~100K
VectorAdd f64 100 1 block	~100K	~100K	~100K
VectorAdd f64 1K 1 block	~100K	~100K	~100K
VectorAdd f64 20K 20 blocks	~100K	~100K	~100K
VectorAdd f64 100K 98 blocks	~100K	~100K	~100K
VectorAdd f64 500K 489 blocks	~100K	~100K	~100K