		Deterministic	Nondeterministic	
			n = 5	n = 10
Average Naive Matrixmul (29 configurations) @ 28 SM's				
DRAM reads	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
DRAM writes	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
L1D hit rate	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
L2D hit rate	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
Cycles	t = 4	0%	0.5%	0.5%
	t = 8	0%	0.5%	0.6%
Exec time	t = 4	1.6x	1.9x	2x
	t = 8	1.5x	1.8x	1.9x
Average Naive Matrixmul (29 configurations) @ 112 SM's				
DRAM reads	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
DRAM writes	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
L1D hit rate	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
L2D hit rate	t = 4	0%	0%	0%
	t = 8	0%	0%	0%
Cycles	t = 4	0%	0.5%	0.5%
	t = 8	0%	0.5%	0.5%
Exec	t = 4	1.3x	2.2x	2.4x
time	t = 8	1.3x	2.2x	2.4x