	Benchmark	BABELSTREAM											
	Size	10.24K $102.4K$											
	Kernel ID	0	1	2	3	4	5	0	1	2	3	4	5
	Kernel	init kernel	copy kernel	mul kernel	add kernel	triad kernel	dot kernel	init kernel	copy kernel	mul kernel	add kernel	triad kernel	dot kernel
Exec. time (s)	Native	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1	< 0.1
	Ours	1	0.9	0.9	1.1	1.1	32.8	8.1	4.6	4.7	6.5	6.4	36.6
	ACCELSIM	5.1	5.1	5.1	5.1	5.1	5.1	15.8	15.8	15.8	15.8	15.8	15.8
Cycles	Native	5.9K	5.4K	5.6K	5.6K	5.9K	33.6K	9.7K	7.1K	7.3K	10.4K	9.9K	35.6K
	Ours	6.1K	5.8K	5.9K	6.2K	6.2K	22.2K	11.3K	8.1K	8.1K	9.1K	9.1K	23.8K
	ACCELSIM	8.6K	6K	6K	6.4K	6.3K	22.6K	33.5K	9.4K	9.7K	11.8K	11.5K	24.9K
Instruction count	Native	245.8K	153.6K	163.8K	194.6K	194.6K	29.8M	2.5M	1.5M	1.6M	1.9M	1.9M	31.7M
	Ours	245.8K	153.6K	163.8K	194.6K	194.6K	29.6M	2.5M	1.5M	1.6M	1.9M	1.9M	31.4M
	ACCELSIM	245.8K	153.6K	163.8K	194.6K	194.6K	29.6M	2.5M	1.5M	1.6M	1.9M	1.9M	31.4M
L1 accesses	Native	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	25.6K	25.6K	51.2K	51.2K	51.2K
	Ours	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	25.6K	25.6K	51.2K	51.2K	51.2K
	ACCELSIM	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	25.6K	25.6K	51.2K	51.2K	51.2K
L1 hit rate (%)	Native	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%
	Ours	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%
	ACCELSIM	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%	0%
L2 read hits	Native	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	25.6K	25.6K	21.8K	28.8K	51.2K
	Ours	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	25.6K	25.6K	51.2K	51.2K	51.2K
	ACCELSIM	0	2.6K	2.6K	5.1K	5.1K	5.1K	0	24.3K	25.6K	45.7K	46K	48.4K
L2 read hit rate $(\%)$	Native	0%	100%	100%	100%	100%	100%	0%	100%	100%	42.65%	56.3%	100%
	Ours	0%	100%	100%	100%	100%	100%	0%	100%	100%	100%	100%	100%
	ACCELSIM	0%	100%	100%	100%	100%	100%	0%	94.94%	99.83%	89.23%	89.88%	94.53%
L2 write hits	Native	7.7K	2.6K	2.6K	2.6K	2.6K	256	49.3K	25.6K	25.6K	25.6K	25.6K	256
	Ours	7.7K	2.6K	2.6K	2.6K	2.6K	256	51.2K	25.6K	25.6K	25.6K	25.6K	256
	ACCELSIM	0	2.6K	2.6K	2.6K	2.6K	192	0	23.7K	20.8K	22.7K	21K	192
10 " 1"	Native	100%	100%	100%	100%	100%	100%	64.24%	100%	100%	100%	100%	100%
L2 write hit rate $(\%)$	Ours	100%	100%	100%	100%	100%	100%	66.67%	100%	100%	100%	100%	100%
,	ACCELSIM	0%	100%	100%	100%	100%	75%	0%	92.66%	81.22%	88.69%	82.14%	75%
DRAM reads	Native	0	0	0	0	0	0	27	0	0	28.3K	23.1K	0
	Ours	0	0	0	0	0	0	0	0	0	0	0	0
	ACCELSIM	7.7K	0	0	0	0	64	76.8K	3.2K	4.9K	8.4K	9.8K	2.9K
	Native	21	4	0	17	17	24	44K	8	521.5	505	27	16
DRAM writes	Ours	0	0	0	0	0	0	25.6K	0	0	0	0	0
	ACCELSIM	7.7K	0	0	0	0	197	76.8K	1.9K	4.8K	5K	5.4K	624