

DRAM writes



AcceISim

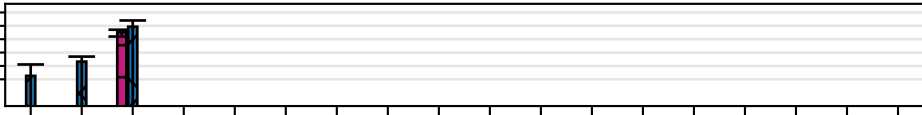


NVIDIA TITAN X (Pascal)



gpucachesim

100K  
10K  
1K  
100



Babel State Stream State Stream State Stream State Stream State Stream State Stream State Stream State Stream State Stream  
1024  
1 block 1 block 1 block 1 block 1 block 256 blocks 10 blocks 10 blocks 10 blocks 10 blocks 256 blocks 10 blocks 10 blocks 10 blocks 10 blocks 10 blocks 10 blocks 10 blocks 256 blocks 10 blocks