

EdgeBlade™ - DC4800FR

A 480 Channel FrameRelay Platform

EdgeBlade™ DC4800FR is a high-density **data communication platform** capable of generating and processing Frame Relay traffic on a T1/E1/J1 interface.

DC4800FR is implemented with industry-standard Frame Relay protocols and associated standards as defined by ANSI, Frame Relay Forum (FRF), ITU/CCITT, and the IETF.

DC4800FR supports both Permanent Virtual Circuit (PVC) connections and Switched Virtual Circuit (SVC) connections across public and private Frame Relay networks.

DC4800FR platform can be rapidly integrated and deployed for high-performance and high density Frame Relay communications to many different hardware devices.



480 HDLC Channel FrameRelay EdgeBlade™

KEY FEATURES

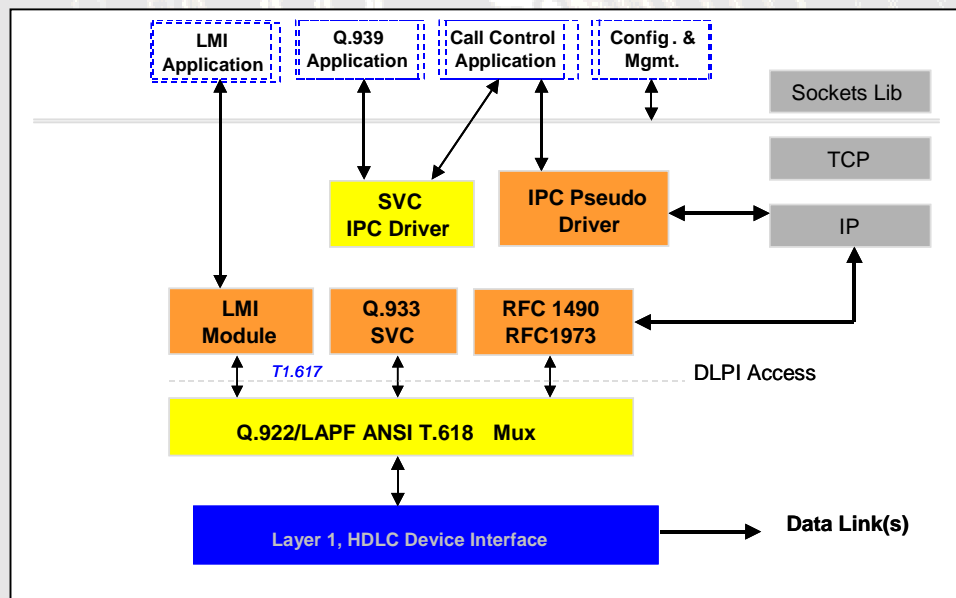
- High density platform - HDLC over choice of up to 16 E1/T1/J1 interface for 480 PHY channels fully loaded
- UNI (FRF.1.1), SVC (FRF.4), and NNI (FRF.2.1) support
- Selectable Permanent Virtual Circuit (PVC) and Switched Virtual Circuit (SVC) connections
- Multi-Protocol and end-to-end IP session support.
- Q.933 Layer 3/ANSI T1.617
- Local Management Interface (LMI) per Revision 1 LMI, Q.933 Annex A and Q.933 Annex D procedures
- CCITT I.370 Congestion Management procedures
- RFC 1490/RFC 1973 MultiProtocol Encapsulation
- Supports RFC 1773 (PPP in FR); rapid integration with PPP-ML for PPP over Frame Relay.Q.922/ANSI T1.618/LAPD Layer 2 Multiplexor with DL_CORE and DL_CONTROL services scales to support large number of physical channels.
- DLPI Interface Library
- Support for 2-, 3-, and 4-byte DLCIs
- CCITT I.430 interface
- Pre-integrated device drivers; generic Layer 1 I/O interface supports existing or third party device drivers



HIGH DENSITY FRAMERELAY PLATFORM

TECHNICAL SUMMARY

FEATURES	DETAILS
Channel Capacity	Up to 480 HDLC Channels
Ports	Available in E1/T1/J1s in optional 16, 12, 8 & 4 spans
Data	V.110 or RLP or HDLC or V.23
Operating Environment	Windows XP, NT, 2000, Intel/Solaris & Sun/Solaris
Clock	T1/E1 BITS Clock: Timing Reference
Management Bus	Primary PCI bus: 32/64bit 33/66MHz; Secondary PCI bus: 32bit 33/66MHz
Ethernet	10/100Mbps management port
Control Plane	APIs available for Solaris and Windows over PCI and Ethernet
RFC Support	<ul style="list-style-type: none"> ▪ RFC 1490 ▪ RFC 1973 ▪ Q.933 ▪ Q.922 ▪ ANSI T.617/618



FrameRelay Implementation on board EdgeBlade™ DC4800FR



Why EdgeBlade™ ?

- ✓ Built with robust and optimized technology
- ✓ Pre-ported with required components
- ✓ Fully integrated with the hardware and OS
- ✓ Tested on various CPUs, chassis and interfaces - Can be integrated easily
- ✓ Rich suite of features
- ✓ Comprehensive suite of APIs drawn out
- ✓ Carrier grade system design with High Availability
- ✓ Optimized cost per port

ABOUT COSYSTEMS

CoSystems, headquartered in the Silicon Valley, is a leading edge Technology solution providing company that has a proven track record of delivering high-quality Technology solutions to global Telecom and Datacom companies. Backed by a 22-year existence in the communications technology horizon, CoSystems is dedicated to delivering complex systems and building blocks to telecom operators, ISPs, Cable Operators, MSOs, and to the emerging wireless voice and packet infrastructure markets. CoSystems solutions offer high-value applications, optimized costs and minimized risk global customers who benefit in rapid Return on Investments in their deployments. CoSystems addresses the market needs through a network of distributors, VARs, Systems Integrators and service providers across the globe. CoSystems solutions include Signal Conversions products, Access Gateways, Signaling Gateways, Media Gateways, products for Broadband using DSL, Cable and WiFi technology. For more information please contact:

Headquarters

CoSystems, Inc
1263 Oakmead Parkway,
Sunnyvale, CA 94085
Tel: 408 - 522 - 0500
Fax: 408 - 720 - 9114

Asia Pacific Office

562/640, Janardhan Towers
Bannerghatta Road
Bangalore 560076
India
Tel: +91- 80- 678 1612
Fax: +91- 80- 678 1847

Email: info@cosystech.com
URL: www.cosystech.com

