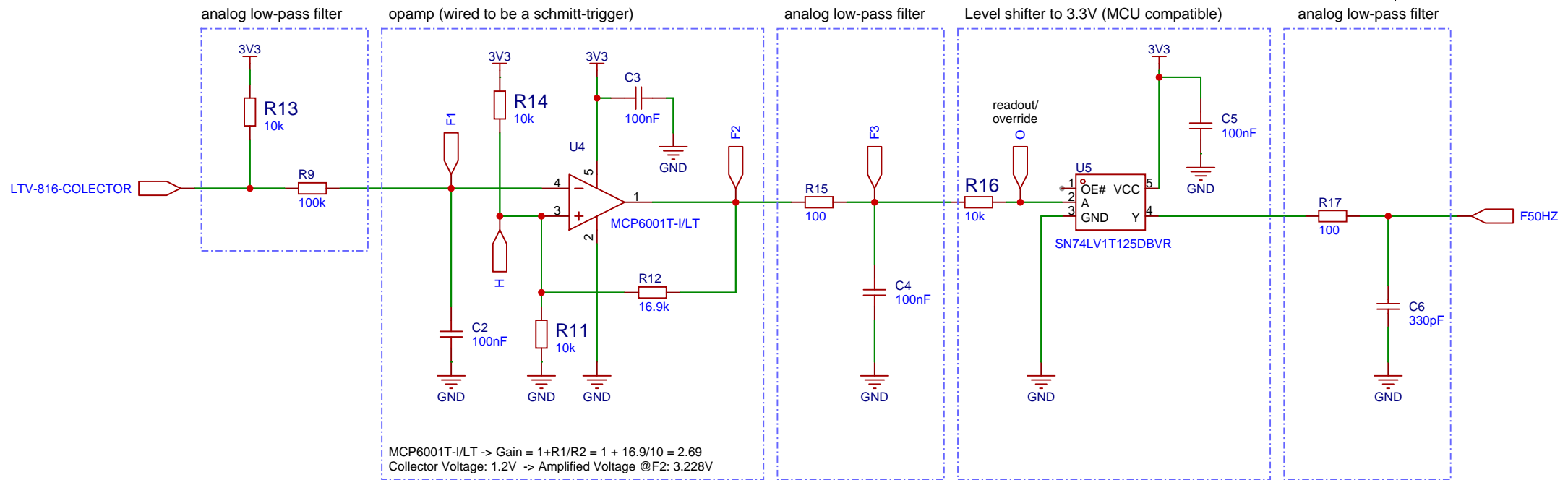
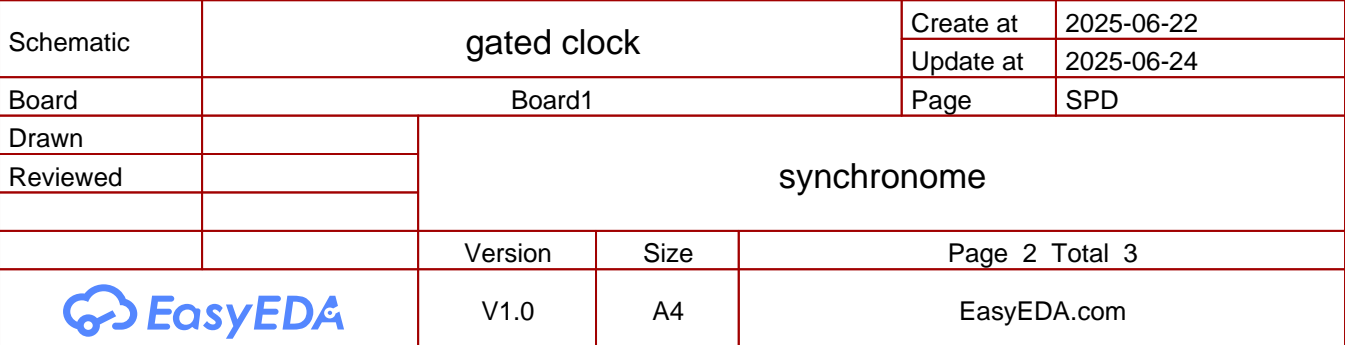
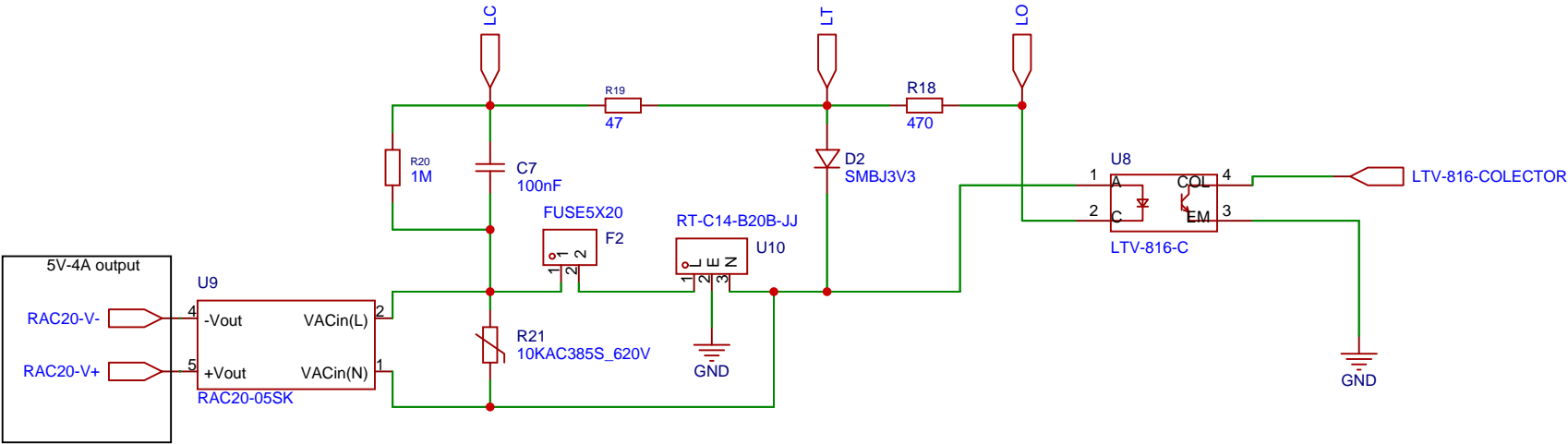


The last two low-pass filter are much higher frequency than the first lowpass filter (C2/R9), and are more used to slow down the signals to give them predictable timing and avoid cross talk/EMI. Since the F50Hz signal is processed by the IC/SW, it is important to avoid glitches as otherwise the clock could see more than one tick per grid cycle due to noise



Schematic	gated clock			Create at	2025-06-22
Board	Board1			Update at	2025-06-25
Drawn		synchronome			
Reviewed					
		Version	Size	Page 1 Total 3	
EasyEDA		V1.0	A4	EasyEDA.com	





Schematic	gated clock			Create at	2025-06-23
				Update at	2025-06-24
Board	Board1			Page	220V
Drawn		synchronome			
Reviewed					
		Version	Size	Page 3 Total 3	
EasyEDA		V1.0	A4	EasyEDA.com	